



# Phase Control Thyristor

## Types N3930Z#120 to N3930Z#160

Development Type No.: NX188ZD160

### Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
$V_{DRM}$	Repetitive peak off-state voltage, (note 1)	1200-1600	V
$V_{DSM}$	Non-repetitive peak off-state voltage, (note 1)	1200-1600	V
$V_{RRM}$	Repetitive peak reverse voltage, (note 1)	1200-1600	V
$V_{RSM}$	Non-repetitive peak reverse voltage, (note 1)	1300-1700	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
$I_{T(AV)M}$	Maximum average on-state current, $T_{sink}=55^{\circ}C$ , (note 2)	3930	A
$I_{T(AV)M}$	Maximum average on-state current, $T_{sink}=85^{\circ}C$ , (note 2)	2658	A
$I_{T(AV)M}$	Maximum average on-state current, $T_{sink}=85^{\circ}C$ , (note 3)	1576	A
$I_{T(RMS)M}$	Nominal RMS on-state current, $T_{sink}=25^{\circ}C$ , (note 2)	7820	A
$I_{T(d.c.)}$	D.C. on-state current, $T_{sink}=25^{\circ}C$ , (note 4)	6629	A
$I_{TSM}$	Peak non-repetitive surge $t_p=10ms$ , $V_{rm}=0.6V_{RRM}$ , (note 5)	54.0	kA
$I_{TSM2}$	Peak non-repetitive surge $t_p=10ms$ , $V_{rm}\leq 10V$ , (note 5)	59.4	kA
$I^2t$	$I^2t$ capacity for fusing $t_p=10ms$ , $V_{rm}=0.6V_{RRM}$ , (note 5)	$14.6 \times 10^6$	A <sup>2</sup> s
$I^2t$	$I^2t$ capacity for fusing $t_p=10ms$ , $V_{rm}\leq 10V$ , (note 5)	$17.6 \times 10^6$	A <sup>2</sup> s
(di/dt) <sub>cr</sub>	Critical rate of rise of on-state current (Note 6)	(continuous, 50Hz) (repetitive, 50Hz, 60s) (non-repetitive)	75 150 300
$V_{RGM}$	Peak reverse gate voltage	5	V
$P_{G(AV)}$	Mean forward gate power	5	W
$P_{GM}$	Peak forward gate power	30	W
$T_{j op}$	Operating temperature range	-40 to +125	°C
$T_{stg}$	Storage temperature range	-40 to +150	°C

### Notes:-

- 1) De-rating factor of 0.13% per °C is applicable for  $T_j$  below 25°C.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave, 125°C  $T_j$  initial.
- 6)  $V_D=67\% V_{DRM}$ ,  $I_{FG}=2A$ ,  $t_r\leq 0.5\mu s$ ,  $T_{case}=125^{\circ}C$ .

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V <sub>TM</sub>	Maximum peak on-state voltage	-	-	1.25	I <sub>TM</sub> =5000A	V
V <sub>TM</sub>	Maximum peak on-state voltage	-	-	1.45	I <sub>TM</sub> =8000A	V
V <sub>TO</sub>	Threshold voltage	-	-	0.841		V
I <sub>T</sub>	Slope resistance	-	-	0.080	Valid from 2000A to 6000A	mΩ
(dv/dt) <sub>cr</sub>	Critical rate of rise of off-state voltage	1000	-	-	V <sub>D</sub> =80% V <sub>DRM</sub> , linear ramp, gate o/c	V/μs
I <sub>DRM</sub>	Peak off-state current	-	-	200	Rated V <sub>DRM</sub>	mA
I <sub>RRM</sub>	Peak reverse current	-	-	200	Rated V <sub>RRM</sub>	mA
V <sub>GT</sub>	Gate trigger voltage	-	-	3.0		V
I <sub>GT</sub>	Gate trigger current	-	-	300	T <sub>j</sub> =25°C      V <sub>D</sub> =10V, I <sub>T</sub> =3A	mA
V <sub>GD</sub>	Gate non-trigger voltage	-	-	0.25	Rated V <sub>DRM</sub>	V
I <sub>H</sub>	Holding current	-	-	1000	T <sub>j</sub> =25°C	mA
t <sub>gd</sub>	Gate-controlled turn-on delay time		0.7	1.0	V <sub>D</sub> =67% V <sub>DRM</sub> , I <sub>T</sub> =1000A, di/dt=10A/μs,	μs
t <sub>gt</sub>	Turn-on time	-	1.5	2.0	I <sub>FG</sub> =2A, t <sub>r</sub> =0.5μs, T <sub>j</sub> =25°C	μs
Q <sub>rr</sub>	Recovered charge	-	4000	-		μC
Q <sub>ra</sub>	Recovered charge, 50% Chord	-	2600	3200	I <sub>TM</sub> =4000A, t <sub>p</sub> =2000μs, di/dt=10A/μs,	μC
I <sub>rr</sub>	Reverse recovery current	-	170	-	V <sub>r</sub> =50V	A
t <sub>rr</sub>	Reverse recovery time, 50% Chord	-	31	-		μs
t <sub>q</sub>	Turn-off time	-	150	-	I <sub>TM</sub> =4000A, t <sub>p</sub> =2000μs, di/dt=10A/μs, V <sub>r</sub> =50V, V <sub>dr</sub> =67%V <sub>DRM</sub> , dV <sub>dr</sub> /dt=20V/μs	μs
		-	200	-	I <sub>TM</sub> =4000A, t <sub>p</sub> =2000μs, di/dt=10A/μs, V <sub>r</sub> =50V, V <sub>dr</sub> =67%V <sub>DRM</sub> , dV <sub>dr</sub> /dt=200V/μs	
R <sub>thJK</sub>	Thermal resistance, junction to heatsink	-	-	0.011	Double side cooled	K/W
F	Mounting force	27	-	47		kN
W <sub>t</sub>	Weight	-	1.7	-	Outline option ZC & ZT	kg
		-	1.2	-	Outline option ZD & ZV	

Notes:-

- 1) Unless otherwise indicated T<sub>j</sub>=125°C.
- 2) For other clamp forces, please consult factory.

## Notes on rupture rated packages.

This product is available with a non-rupture rated package.

For additional details on these products, please consult factory.

## Notes on Ratings and Characteristics

### 1.0 Voltage Grade Table

Voltage Grade	$V_{DRM}$ V	$V_{DSM}$ V	$V_{RRM}$ V	$V_{RSM}$ V	$V_D$ DC V	$V_R$ DC V
12	1200			1300		810
14	1400			1500		930
16	1600			1600		1040

### 2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

### 3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for  $T_j$  below 25°C.

### 4.0 Repetitive dv/dt

Standard dv/dt is 1000V/μs.

### 5.0 Snubber Components

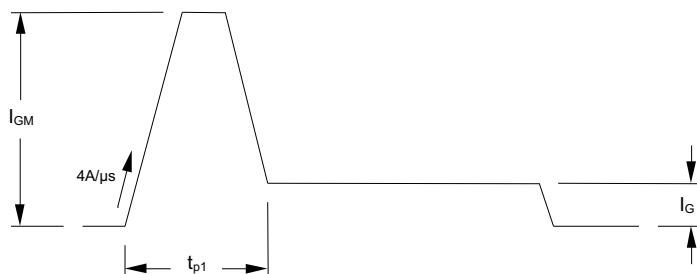
When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

### 6.0 Rate of rise of on-state current

The maximum un-primed rate of rise of on-state current must not exceed 300A/μs at any time during turn-on on a non-repetitive basis. For repetitive performance, the on-state rate of rise of current must not exceed 150A/μs at any time during turn-on. Note that these values of rate of rise of current apply to the total device current including that from any local snubber network.

### 7.0 Gate Drive

The nominal requirement for a typical gate drive is illustrated below. An open circuit voltage of at least 30V is assumed. This gate drive must be applied when using the full di/dt capability of the device.



The magnitude of  $I_{GM}$  should be between five and ten times  $I_{GT}$ , which is shown on page 2. Its duration ( $t_{p1}$ ) should be 20μs or sufficient to allow the anode current to reach ten times  $I_L$ , whichever is greater. Otherwise, an increase in pulse current could be needed to supply the necessary charge to trigger. The 'back-porch' current  $I_G$  should remain flowing for the same duration as the anode current and have a magnitude in the order of 1.5 times  $I_{GT}$ .

## 8.0 Computer Modelling Parameters

### 8.1 Device Dissipation Calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T}$$

and:

$$W_{AV} = \frac{\Delta T}{R_{th}}$$

$$\Delta T = T_{j\max} - T_K$$

Where  $V_{T0}=0.841V$ ,  $r_T=0.080m\Omega$ ,

$R_{th}$  = Supplementary thermal impedance, see table below and

$ff$  = Form factor, see table below.

Supplementary Thermal Impedance							
Conduction Angle	30°	60°	90°	120°	180°	270°	d.c.
Square wave Double Side Cooled	0.0124	0.0122	0.0121	0.0119	0.0117	0.0113	0.0110
Square wave Single Side Cooled	0.0249	0.0248	0.0247	0.0246	0.0244	0.0241	0.022
Sine wave Double Side Cooled	0.0168	0.0140	0.0131	0.0118	0.0112		
Sine wave Single Side Cooled	0.0249	0.0247	0.0246	0.0244	0.0241		

Form Factors							
Conduction Angle	30°	60°	90°	120°	180°	270°	d.c.
Square wave	3.46	2.45	2	1.73	1.41	1.15	1
Sine wave	3.98	2.78	2.22	1.88	1.57		

### 8.2 Calculating $V_T$ using ABCD Coefficients

The on-state characteristic  $I_T$  vs.  $V_T$ , on page 6 is represented in two ways;

- (i) the well established  $V_{T0}$  and  $r_T$  tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for  $V_T$  in terms of  $I_T$  given below:

$$V_T = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for  $V_T$  agree with the true device characteristic over a current range, which is limited to that plotted.

25°C Coefficients		125°C Coefficients	
A	1.359308154	A	0.619740054
B	-0.09795577	B	$7.69989 \times 10^3$
C	$2.09655 \times 10^{-6}$	C	$2.95324 \times 10^{-5}$
D	0.0107923	D	$5.897501 \times 10^3$

### 8.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left( 1 - e^{\frac{-t}{\tau_p}} \right)$$

Where  $p = 1$  to  $n$ ,  $n$  is the number of terms in the series and:

$t$  = Duration of heating pulse in seconds.

$r_t$  = Thermal resistance at time  $t$ .

$r_p$  = Amplitude of  $p$ th term.

$\tau_p$  = Time Constant of  $r_p$ th term.

The coefficients for this device are shown in the tables below:

D.C. Double Side Cooled				
Term	1	2	3	4
$r_p$	$6.72 \times 10^{-3}$	$2.78 \times 10^{-3}$	$9.476 \times 10^{-4}$	$7.12 \times 10^{-4}$
$\tau_p$	1.0226	0.226	0.0586	$9.06 \times 10^{-3}$

D.C. Single Side Cooled				
Term	1	2	3	4
$r_p$	0.01688	$4.42 \times 10^{-3}$	$1.79 \times 10^{-3}$	$8.72 \times 10^{-4}$
$\tau_p$	7.055	0.5206	0.1198	0.0128

### 9.0 Reverse recovery ratings

(i)  $Q_{ra}$  is based on 50%  $I_{rm}$  chord as shown in Fig. 1

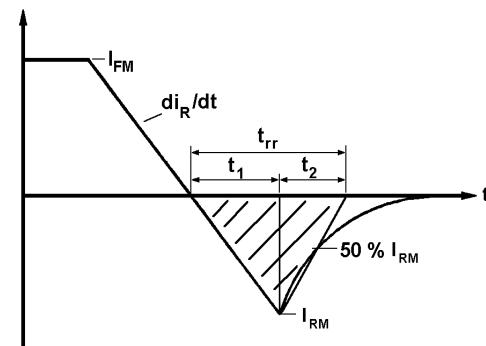


Fig. 1

(ii)  $Q_{rr}$  is based on a  $150\mu s$  integration time i.e.

$$Q_{rr} = \int_0^{150\mu s} i_{rr} \cdot dt$$

(iii)

$$K \text{ Factor} = \frac{t_1}{t_2}$$

## Curves

Figure 1 - On-state characteristics of Limit device

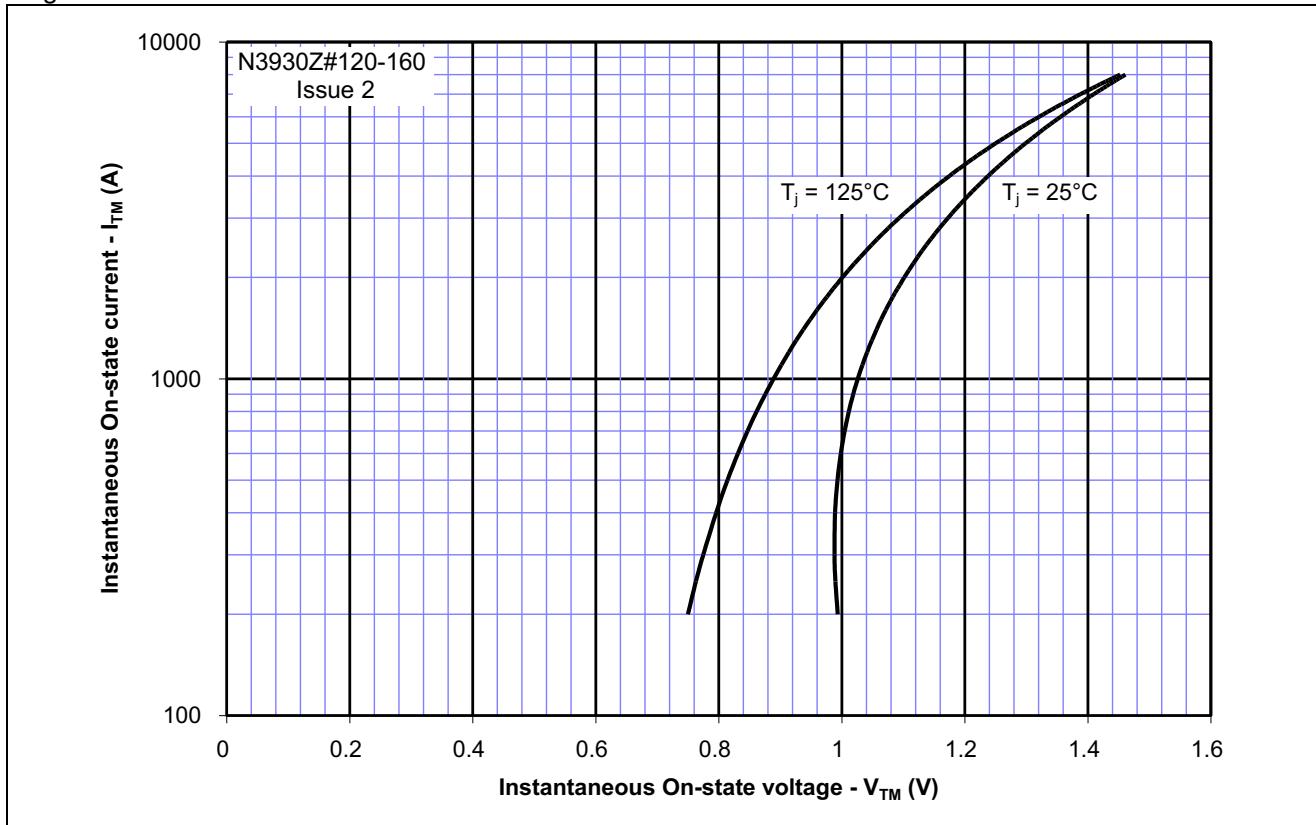


Figure 2 - Gate characteristics - Trigger limits

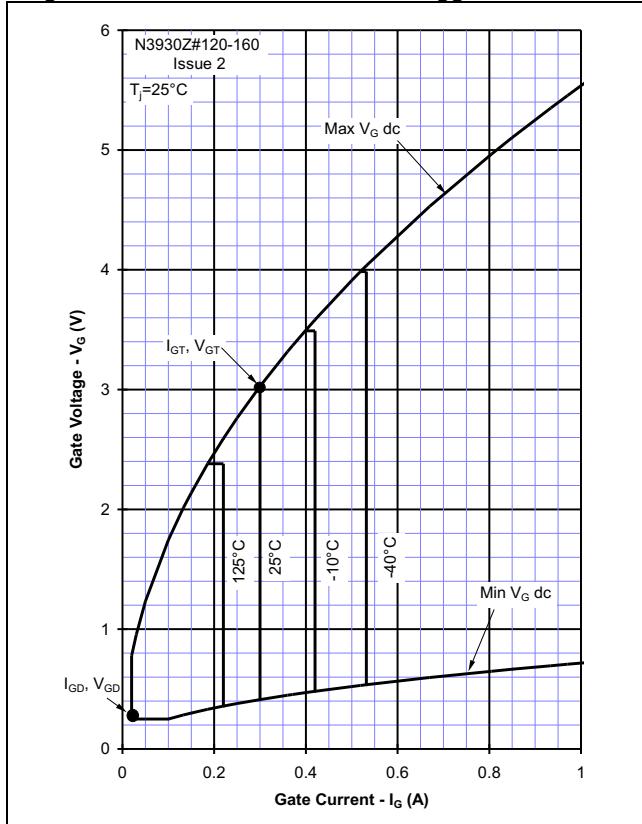


Figure 3 - Gate characteristics - Power curves

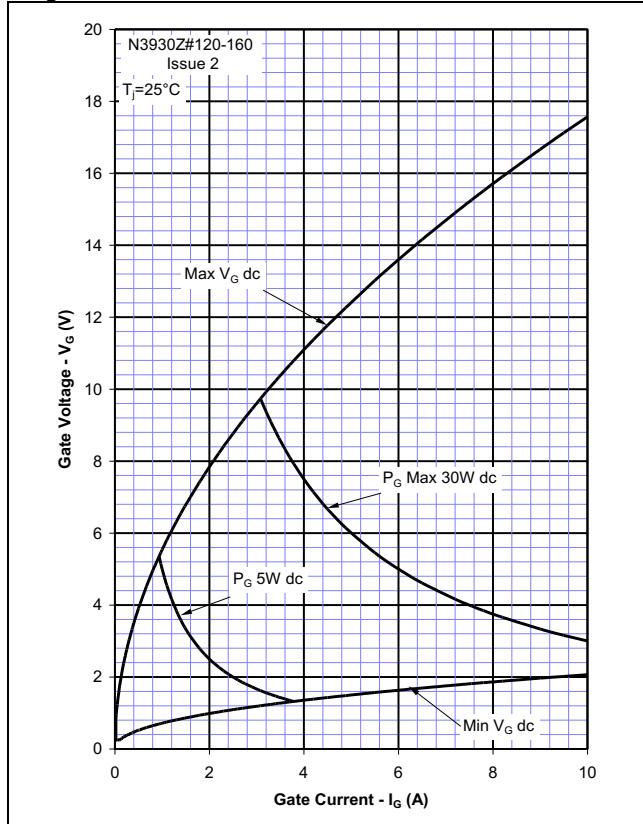


Figure 4 - Total recovered charge,  $Q_{rr}$

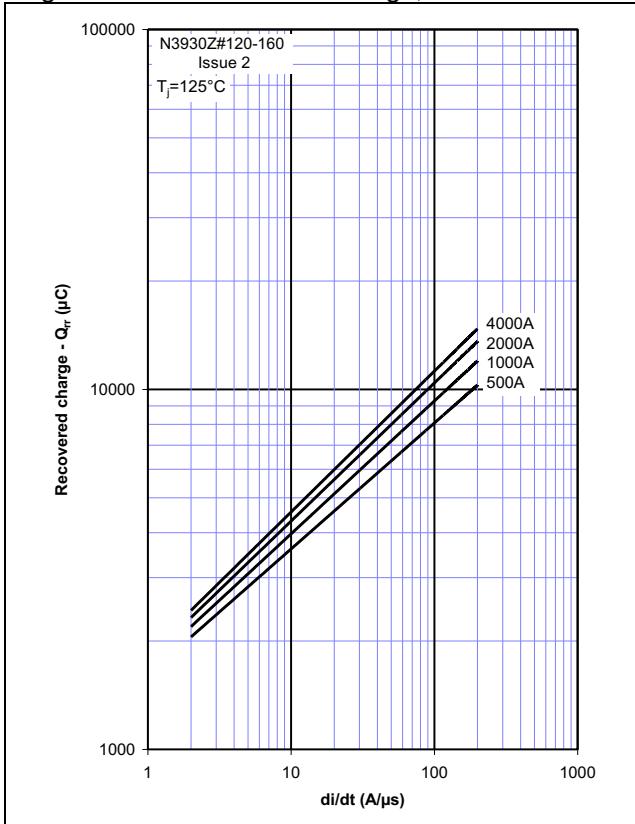


Figure 5 - Recovered charge,  $Q_{ra}$  (50% chord)

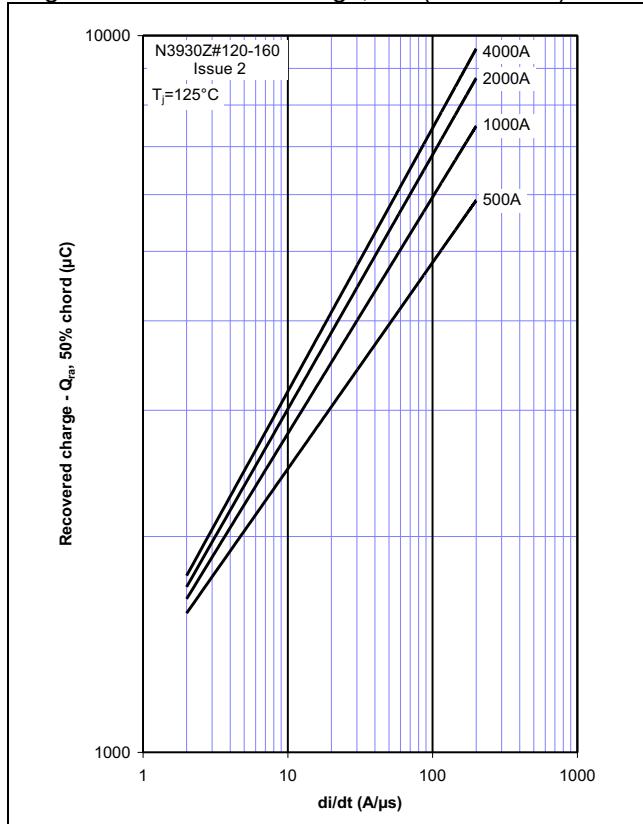


Figure 6 - Peak reverse recovery current,  $I_{rm}$

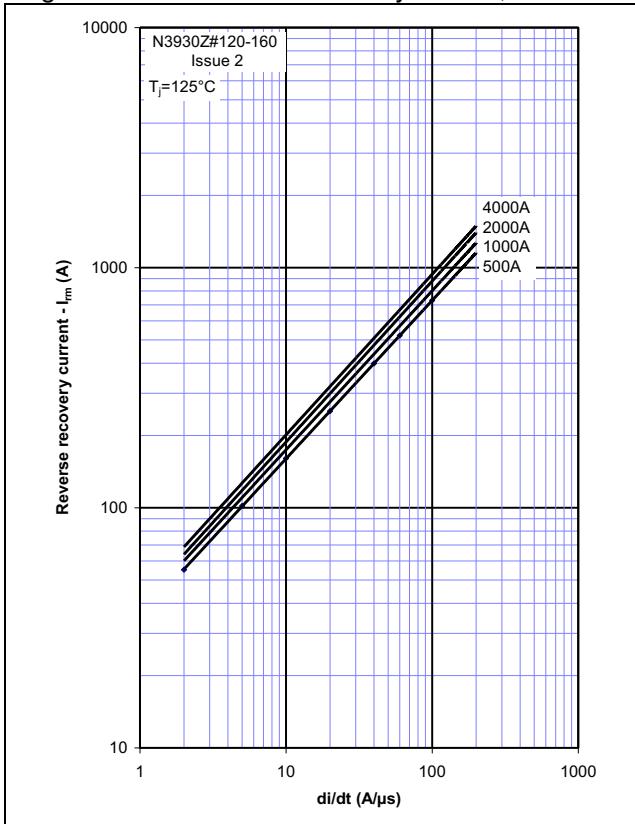


Figure 7 - Maximum recovery time,  $t_{rr}$  (50% chord)

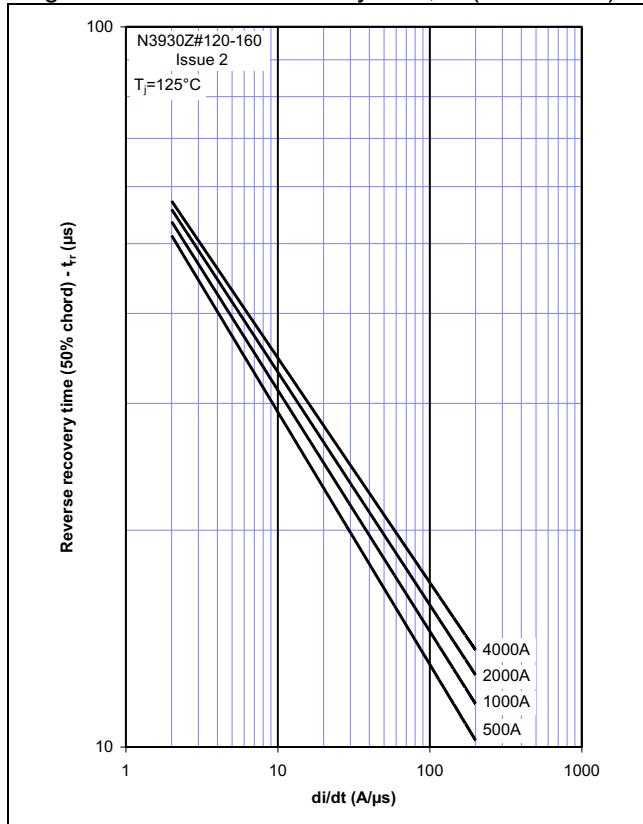


Figure 8 – On-state current vs. Power dissipation – Double Side Cooled (Sine wave)

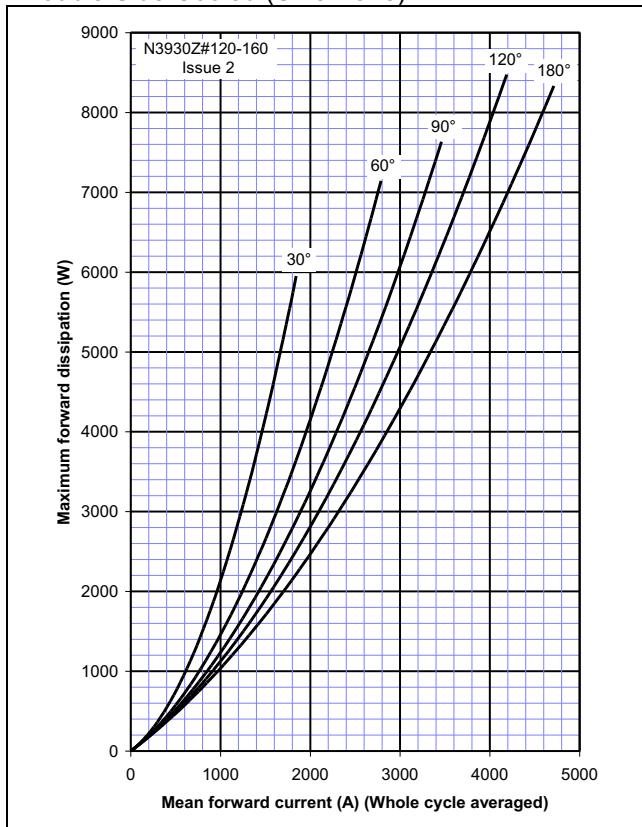


Figure 9 – On-state current vs. Heatsink temperature - Double Side Cooled (Sine wave)

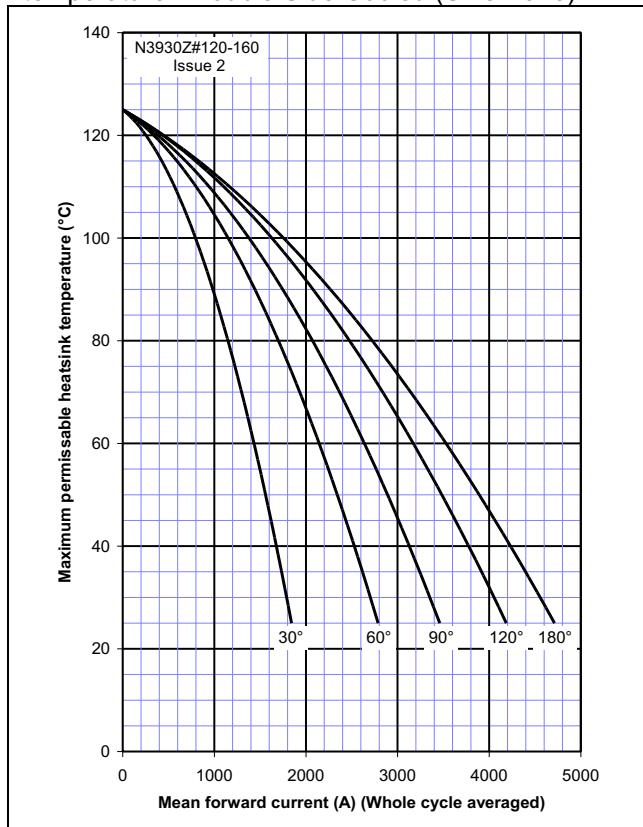


Figure 10 – On-state current vs. Power dissipation – Double Side Cooled (Square wave)

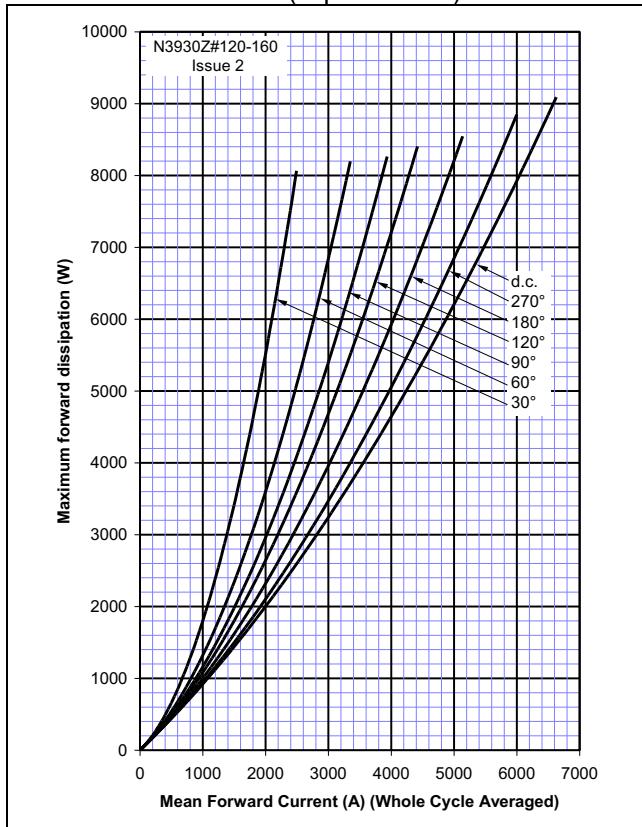


Figure 11 – On-state current vs. Heatsink temperature – Double Side Cooled (Square wave)

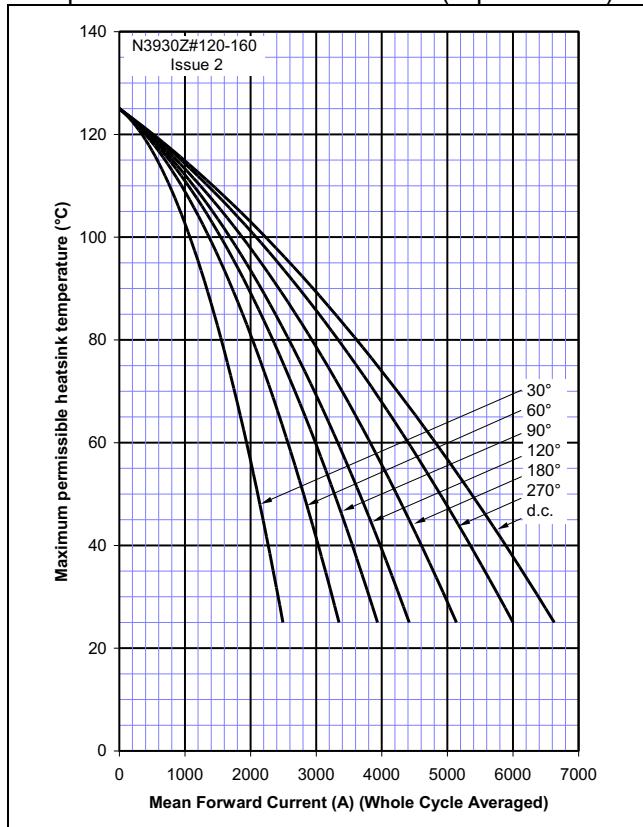


Figure 12 – On-state current vs. Power dissipation  
– Single Side Cooled (Sine wave)

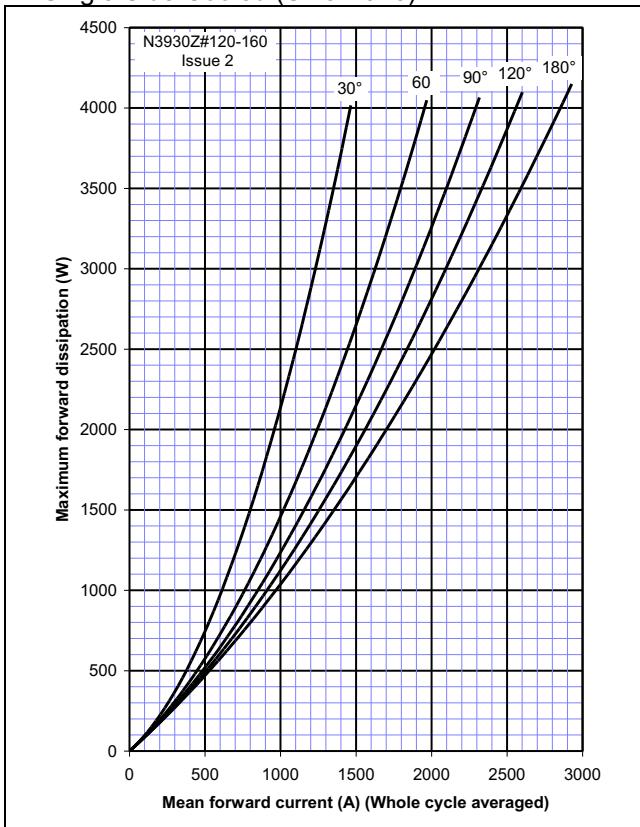


Figure 13 – On-state current vs. Heatsink temperature – Single Side Cooled (Sine wave)

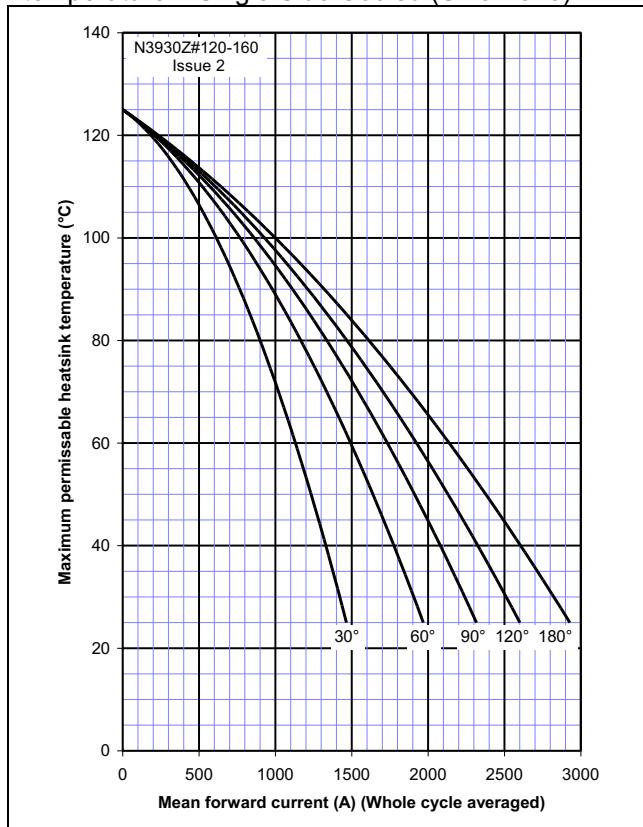


Figure 14 – On-state current vs. Power dissipation  
– Single Side Cooled (Square wave)

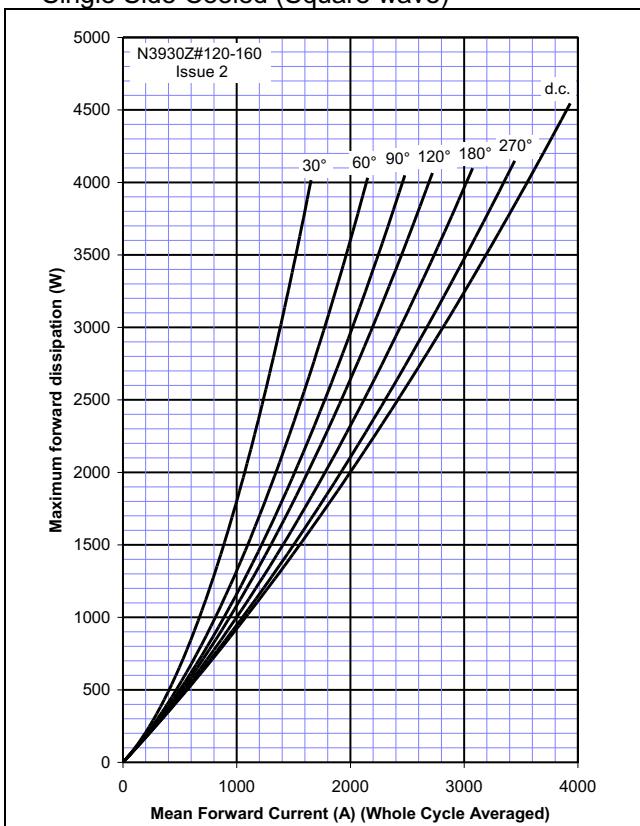


Figure 15 – On-state current vs. Heatsink temperature – Single Side Cooled (Square wave)

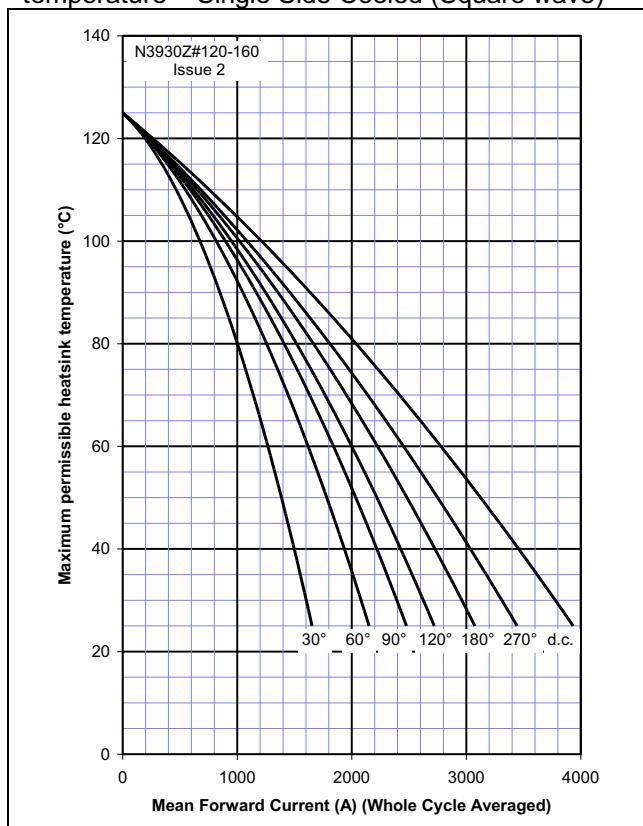


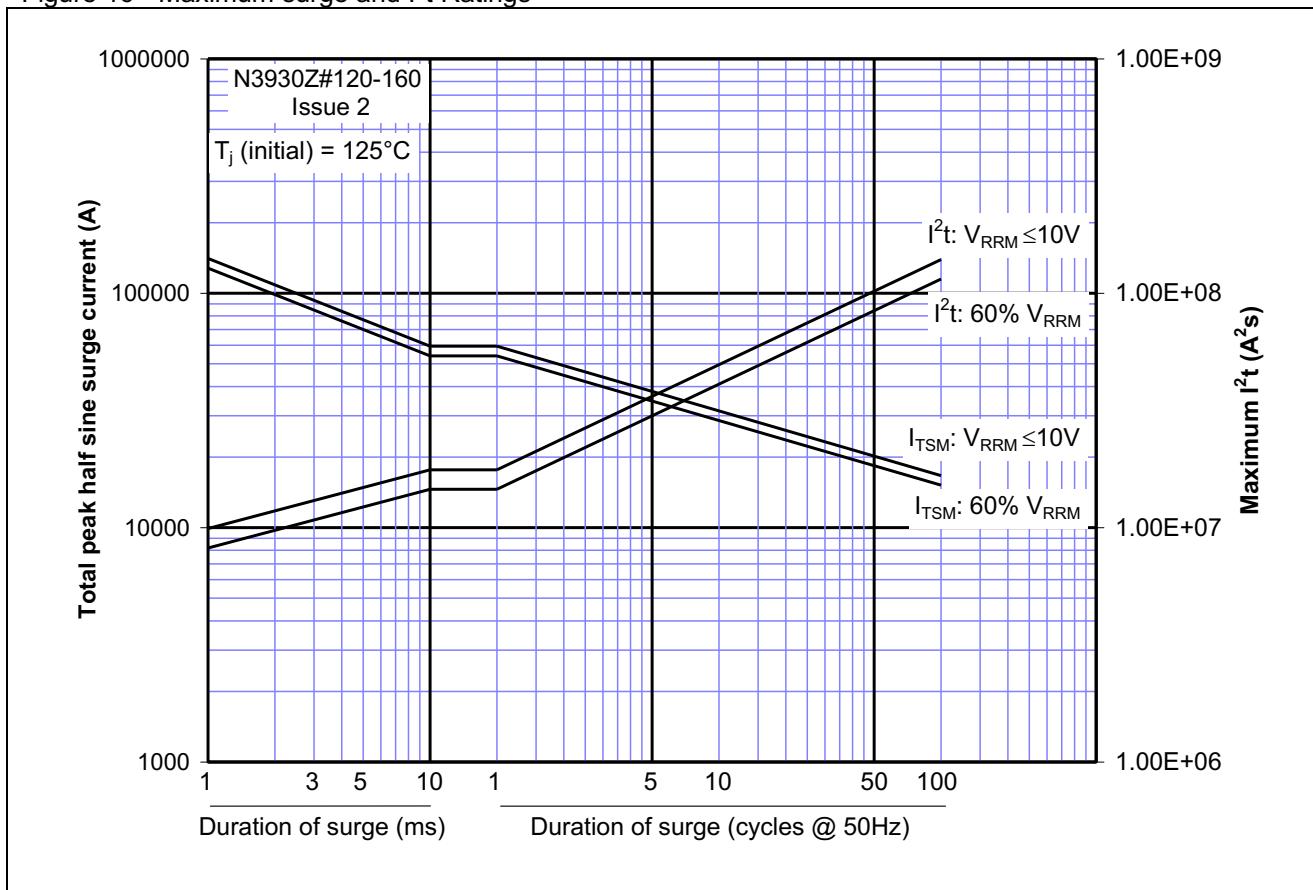
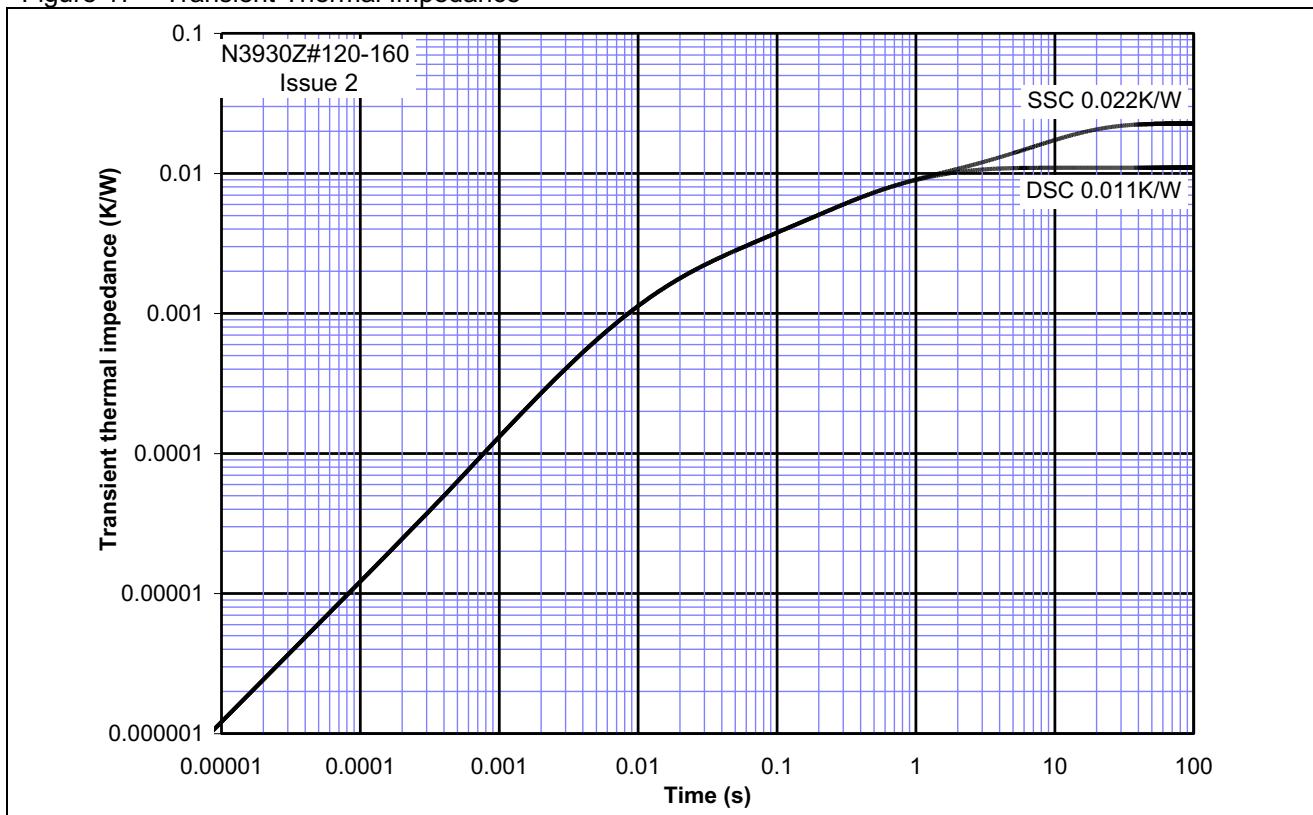
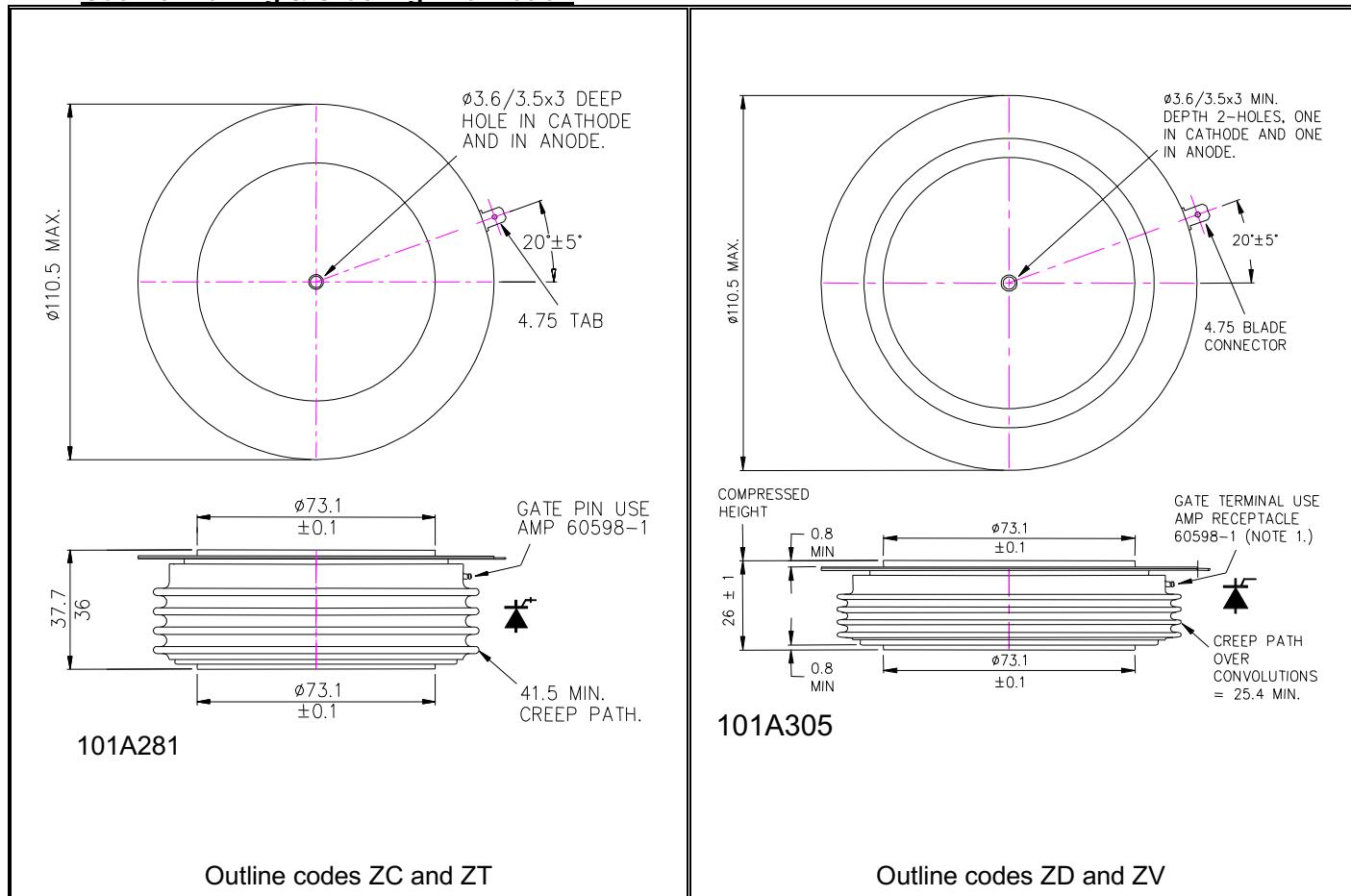
Figure 16 - Maximum surge and  $I^2t$  Ratings


Figure 17 – Transient Thermal Impedance



## Outline Drawing &amp; Ordering Information


**ORDERING INFORMATION**

(Please quote 10 digit code as below)

**N3930**

◆◆

0

Fixed  
Type Code

Outline code  
ZC = 37.7mm clamp height, ZT = 37.7mm clamp height, rupture rated  
ZD = 26mm clamp height, ZV = 26mm clamp height, rupture rated

Voltage code  
 $V_{DRM}/100$   
12-16

Fixed turn-off  
time code

Order code: N3930ZD160 – 1600V  $V_{DRM}$ ,  $V_{RRM}$ , 26mm clamp height capsule.

**IXYS Semiconductor GmbH**  
Edisonstraße 15  
D-68623 Lampertheim  
Tel: +49 6206 503-0  
Fax: +49 6206 503-627  
E-mail: [marcom@ixys.de](mailto:marcom@ixys.de)

**IXYS**  
A Littelfuse Technology

**IXYS UK Westcode Ltd**  
Langley Park Way, Langley Park,  
Chippenham, Wiltshire, SN15 1GE.  
Tel: +44 (0)1249 444524  
Fax: +44 (0)1249 659448  
E-mail: [sales@ixysuk.com](mailto:sales@ixysuk.com)
**IXYS Corporation**  
1590 Buckeye Drive  
Milpitas CA 95035-7418  
Tel: +1 (408) 457 9000  
Fax: +1 (408) 496 0670  
E-mail: [sales@ixys.net](mailto:sales@ixys.net)
[www.littelfuse.com](http://www.littelfuse.com)
[www.ixysuk.com](http://www.ixysuk.com)
[www.ixys.com](http://www.ixys.com)
**IXYS Long Beach Inc**  
2500 Mira Mar Ave, Long Beach  
CA 90815  
Tel: +1 (562) 296 6584  
Fax: +1 (562) 296 6585  
E-mail: [service@ixyslongbeach.com](mailto:service@ixyslongbeach.com)

The information contained herein is confidential and is protected by Copyright. The information may not be used or disclosed except with the written permission of and in the manner permitted by the proprietors IXYS UK Westcode Ltd.

© IXYS UK Westcode Ltd.

In the interest of product improvement, Westcode reserves the right to change specifications at any time without prior notice.  
Devices with a suffix code (2-letter, 3-letter or letter/digit/letter combination) added to their generic code are not necessarily subject to the conditions and limits contained in this report.



---

Disclaimer Notice - Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littelfuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at [www.littelfuse.com/disclaimer-electronics](http://www.littelfuse.com/disclaimer-electronics).