

Date: 10th January 2023

**Data Sheet Issue: 2** 

# Rectifier Diode Types W0790LG620 to W0790LG680

## **Absolute Maximum Ratings**

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
$V_{RRM}$	Repetitive peak reverse voltage, (note 1)	6200-6800	V
V <sub>RSM</sub>	Non-repetitive peak reverse voltage, (note 1)	6300-6900	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
I <sub>F(AV)M</sub>	Maximum average forward current, T <sub>sink</sub> =55°C, (note 2)	790	А
I <sub>F(AV)M</sub>	Maximum average forward current. T <sub>sink</sub> =100°C, (note 2)	535	А
I <sub>F(AV)M</sub>	Maximum average forward current, T <sub>sink</sub> =100°C, (note 3)	310	Α
I <sub>F(RMS)M</sub>	Nominal RMS forward current, T <sub>sink</sub> =25°C, (note 2)	1460	А
I <sub>F(d.c.)</sub>	D.C. forward current, T <sub>sink</sub> =25°C, (note 4)	1325	Α
I <sub>FSM</sub>	Peak non-repetitive surge t <sub>p</sub> =10ms, V <sub>rm</sub> =0.6V <sub>RRM</sub> , (note 5)	7000	А
I <sub>FSM2</sub>	Peak non-repetitive surge t <sub>p</sub> =10ms, V <sub>rm</sub> ≤10V, (note 5)	7700	Α
l <sup>2</sup> t	$I^{2}t$ capacity for fusing $t_{p}$ =10ms, $V_{rm}$ =0.6 $V_{RRM}$ , (note 5)	245×10 <sup>3</sup>	A <sup>2</sup> s
l <sup>2</sup> t	I²t capacity for fusing t <sub>p</sub> =10ms, V <sub>rm</sub> ≤10V, (note 5)	296×10 <sup>3</sup>	A <sup>2</sup> s
T <sub>j op</sub>	Operating temperature range	-40 to +150	°C
$T_{stg}$	Storage temperature range	-55 to +150	°C

## Notes:-

- 1) De-rating factor of 0.13% per °C is applicable for  $T_j$  below 25°C.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Cathode side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave, 150°C T<sub>j</sub> initial.



# **Characteristics**

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V <sub>FM</sub>	Maximum peak forward voltage	-	-	3.8	I <sub>TM</sub> =2625A	V
$V_{T0}$	Threshold voltage	-	-	0.98		V
r⊤	Slope resistance	-	-	1.06		mΩ
I <sub>RRM</sub>	Peak reverse current	-	-	50	Rated V <sub>RRM</sub>	mA
I <sub>RRM</sub>	Peak reverse current	-	-	2	Rated V <sub>RRM</sub> , T <sub>j</sub> =25°C	mA
Qrr	Total recovered charge	-	5815	6450		μC
Qra	Reverse recovery charge (50% chord)	-	2211	-	1000A + -1ma di/dt-10A/up \/ -100\/	μC
I <sub>rm</sub>	Reverse recovery current	-	137	-	$I_{FM}=1000A$ , $t_p=1$ ms, di/dt=10A/ $\mu$ s, $V_r=100V$	Α
t <sub>rr</sub>	Reverse recovery time (50% chord)	-	33	-		μs
		-	-	0.0394	Double side cooled	K/W
$R_{thJK}$	Thermal resistance, junction to heatsink	-	-	0.0707	Anode side cooled	K/W
				0.0898	Cathode side cooled	K/W
F	Mounting force	10	-	20		kN
Wt	Weight	-	400	-		g

#### Notes:-

1) Unless otherwise indicated  $T_j=150^{\circ}C$ .



# **Notes on Ratings and Characteristics**

## 1.0 Voltage Grade Table

Voltage Grade	V <sub>RRM</sub> V	V <sub>RSM</sub> V	V <sub>R</sub> DC V
62	6200	6300	4150
65	6500	6600	4350
68	6800	6900	4550

#### 2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

## 3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for  $T_j$  below 25°C.

#### 4.0 Repetitive dv/dt

Standard dv/dt is 1000V/µs.

# 5.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.



## 6.0 Computer Modelling Parameters

## 6.1 Device Dissipation Calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{{V_{T0}}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T} \qquad W_{AV} = \frac{\Delta T}{R_{th}} \\ \Delta T = T_{j\,\text{max}} - T_C$$
 and:

Where  $V_{T0}$ =0.98V,  $r_{T}$ =1.06 $m\Omega$ ,

 $R_{\it th}$  = Supplementary thermal impedance, see table below.

ff = Form factor, see table below.

Supplementary Thermal Impedance						
Conduction Angle	6 phase (60°)	3 phase (120°)	½ wave (180°)	d.c.		
Square wave Double Side Cooled	0.0480	0.0445	0.0425	0.03940		
Square wave Anode Side Cooled	0.0797	0.0759	0.0738	0.0707		
Square wave Cathode Side Cooled	0.0983	0.0950	0.0929	0.0898		
Sine wave Double Side Cooled	0.0451	0.0422	0.0396			
Sine wave Anode Side Cooled	0.0762	0.0732	0.0707			
Sine wave Cathode Side Cooled	0.0957	0.0928	0.0901			

Form Factors							
Conduction Angle 6 phase (60°) 3 phase (120°) ½ wave (180°) d.c							
Square wave	2.449	1.732	1.414	1			
Sine wave	2.778	1.879	1.57				



## 6.2 Calculating V<sub>F</sub> using ABCD Coefficients

The on-state characteristic  $I_F$  vs.  $V_F$ , on page 6 is represented by a set of constants A, B, C, D, forming the coefficients of the representative equation for  $V_F$  in terms of  $I_F$  given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V<sub>F</sub> agree with the true device characteristic over a current range, which is limited to that plotted.

25°C Coefficients		150°C Coefficients	
Α	0.110406435	Α	0.101875316
В	0.200243555	В	0.168859960
С	7.131702×10 <sup>-4</sup>	С	1.068433×10 <sup>-3</sup>
D	-1.513197×10 <sup>-2</sup>	D	-9.060147×10 <sup>-3</sup>

#### 6.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}}\right)$$

Where p = 1 to n and:

n = number of terms in the series

t = Duration of heating pulse in seconds

rt = Thermal resistance at time t

 $r_p$  = Amplitude of  $p^{th}$  term

 $\tau_p$  = Time Constant of r<sup>th</sup> term

The coefficients for this device are shown in the table below:

D.C. Double Side Cooled								
Term	Term 1 2 3 4							
$r_p$	0.02350677	0.01080067	3.111832×10 <sup>-3</sup>	1.946426×10 <sup>-3</sup>				
$ au_{ m p}$	1.261361	0.1407335	0.01999451	2.677464×10 <sup>-3</sup>				

D.C. Anode Side Cooled						
Term	1	2	3	4	5	
$r_p$	0.04517370	0.01092023	0.01002468	2.972535×10 <sup>-3</sup>	1.644544×10 <sup>-3</sup>	
$ au_{\mathcal{P}}$	6.555359	0.7723341	0.1155237	0.01578855	2.018220×10 <sup>-3</sup>	



D.C. Cathode Side Cooled							
Term	erm 1 2 3 4						
$r_p$	r <sub>p</sub> 0.06840358 0.01444154 4.487340×10 <sup>-3</sup> 2.389196×10 <sup>-3</sup>						
$ au_{ ho}$	7.458694	0.2510212	0.03334104	3.271107×10 <sup>-3</sup>			

# 7.0 Reverse recovery ratings

(i)  $Q_{\text{ra}}$  is based on 50%  $I_{\text{RM}}$  chord as shown in Fig. 1

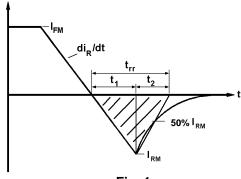


Fig. 1

(ii)  $Q_{rr}$  is based on a 150 $\mu$ s integration time i.e.

$$Q_{rr} = \int_{0}^{150\mu s} i_{rr}.dt$$

(ii) 
$$K Factor = \frac{t_1}{t_2}$$



#### **Curves**

Figure 1 – Forward characteristics of Limit device

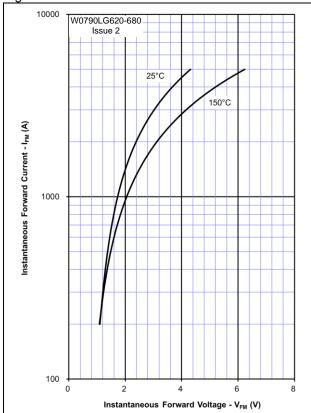


Figure 2 – Transient Thermal Impedance

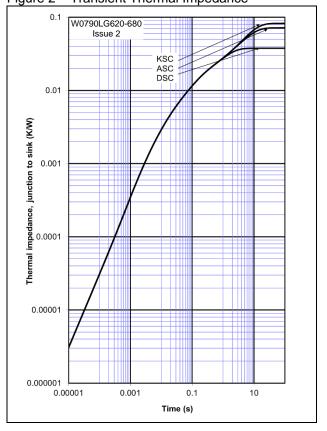


Figure 3 – Maximum surge and I2t Ratings

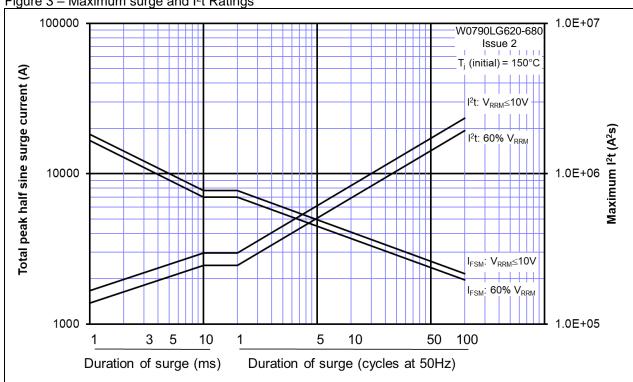




Figure 4 - Total recovered charge, Q<sub>rr</sub>

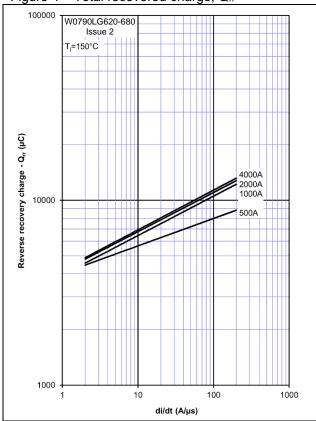


Figure 5 - Recovered charge (50% chord), Qra

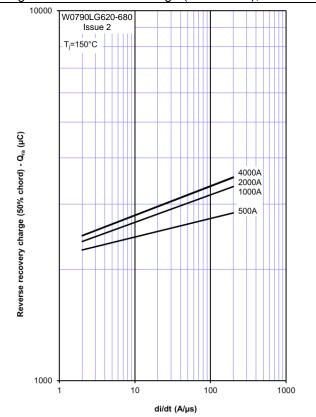


Figure 6 - Peak reverse recovery current, Irm

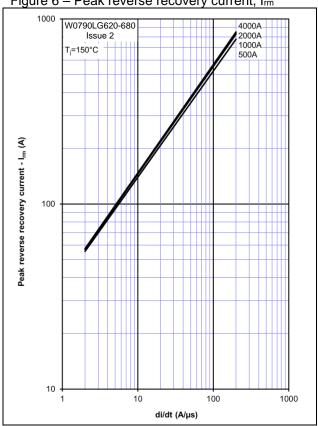


Figure 7 – Reverse recovery time (50% chord), t<sub>rr</sub>

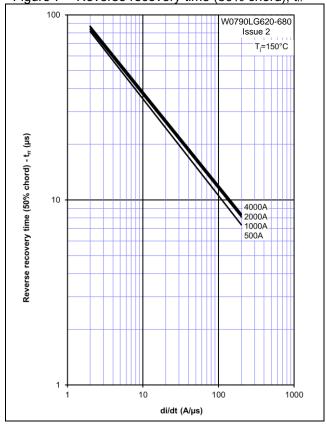


Figure 8 – Forward current vs. Power dissipation – Double Side Cooled

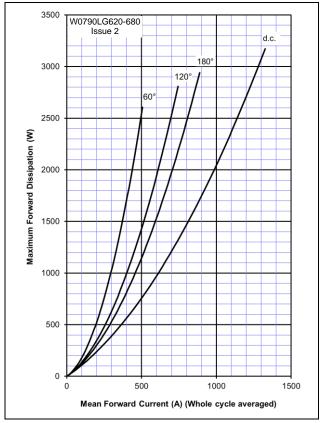


Figure 10 – Forward current vs. Power dissipation – Cathode Side Cooled

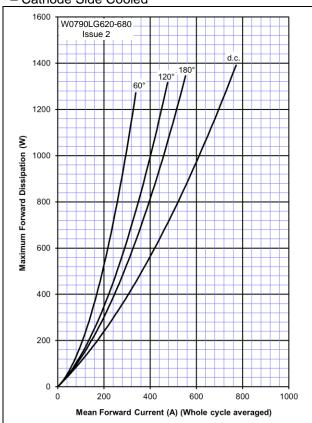


Figure 9 – Forward current vs. Heatsink temperature - Double Side Cooled

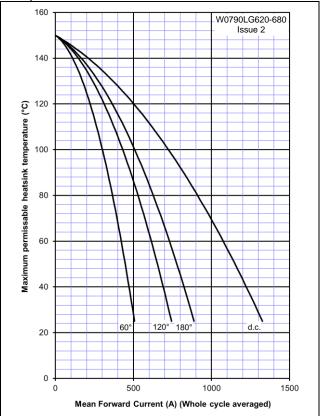
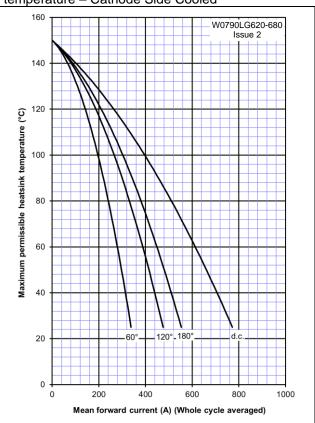
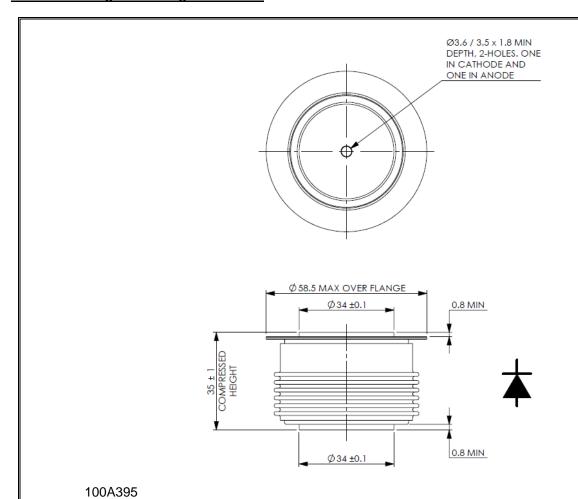


Figure 11 – Forward current vs. Heatsink temperature – Cathode Side Cooled





## **Outline Drawing & Ordering Information**



ORDERI	NG INFORMATION	(Please quote 10 digit code as below)		
W0790	LG	<b>**</b>	0	
Fixed Type Code	Fixed Outline Code	Voltage code V <sub>DRM</sub> /100 62-68	Fixed turn-off time code	

Order code: W0790LG680 - 6800V V<sub>RRM</sub>, 35mm clamp height capsule

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powerstacksus@littelfuse.com

# www.littelfuse.com

#### https://www.littelfuse.com/products/power-semiconductors/high-power.aspx

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