

Advance Data

# Wespack Rectifier Diode

## Types W5130MK240 to W5130MK280

Development Type No.: WX404MK240-280

### Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
$V_{RRM}$	Repetitive peak reverse voltage, (note 1)	2400-2800	V
$V_{RSM}$	Non-repetitive peak reverse voltage, (note 1)	2500-2900	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
$I_{F(AV)M}$	Maximum average forward current, $T_{sink}=55^{\circ}C$ , (note 2)	5130	A
$I_{F(AV)M}$	Maximum average forward current. $T_{sink}=100^{\circ}C$ , (note 2)	3715	A
$I_{F(AV)M}$	Maximum average forward current. $T_{sink}=100^{\circ}C$ , (note 3)	2080	A
$I_{F(RMS)M}$	Nominal RMS forward current, $T_{sink}=25^{\circ}C$ , (note 2)	9345	A
$I_{F(d.c.)}$	D.C. forward current, $T_{sink}=25^{\circ}C$ , (note 4)	7890	A
$I_{FSM}$	Peak non-repetitive surge $t_p=10ms$ , $V_{rm}=60\%V_{RRM}$ , (note 5)	42.0	kA
$I_{FSM2}$	Peak non-repetitive surge $t_p=10ms$ , $V_{rm}\leq 10V$ , (note 5)	46.2	kA
$I^2t$	$I^2t$ capacity for fusing $t_p=10ms$ , $V_{rm}=60\%V_{RRM}$ , (note 5)	$8.82\times 10^6$	$A^2s$
$I^2t$	$I^2t$ capacity for fusing $t_p=10ms$ , $V_{rm}\leq 10V$ , (note 5)	$10.67\times 10^6$	$A^2s$
$T_{j\ op}$	Operating temperature range	-40 to +175	$^{\circ}C$
$T_{stg}$	Storage temperature range	-55 to +175	$^{\circ}C$

### Notes:-

- 1) De-rating factor of 0.13% per  $^{\circ}C$  is applicable for  $T_j$  below  $25^{\circ}C$ .
- 2) Double side cooled, single phase; 50Hz,  $180^{\circ}$  half-sinewave.
- 3) Cathode side cooled, single phase; 50Hz,  $180^{\circ}$  half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave,  $175^{\circ}C$   $T_j$  initial.

### Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V <sub>FM</sub>	Maximum peak forward voltage	-	-	1.10	I <sub>FM</sub> =3000A	V
V <sub>FM</sub>	Maximum peak forward voltage	-	-	1.98	I <sub>FM</sub> =16000A	V
V <sub>T0</sub>	Threshold voltage	-	-	0.91		V
r <sub>T</sub>	Slope resistance	-	-	0.07		mΩ
I <sub>RRM</sub>	Peak reverse current	-	-	75	Rated V <sub>RRM</sub>	mA
Q <sub>rr</sub>	Recovered charge	-	5000	5500	I <sub>TM</sub> =1000A, t <sub>p</sub> =1000μs, di/dt=10A/μs, V <sub>r</sub> =50V	μC
Q <sub>ra</sub>	Recovered charge, 50% Chord	-	3550	-		μC
I <sub>rm</sub>	Reverse recovery current	-	220	-		A
t <sub>rr</sub>	Reverse recovery time, 50% chord	-	32	-		μs
R <sub>thJK</sub>	Thermal resistance, junction to heatsink	-	-	0.0130	Double side cooled	K/W
		-	-	0.0240	Anode side cooled	K/W
		-	-	0.0284	Cathode side cooled	K/W
F	Mounting force	25	-	31	Note 2	kN
W <sub>t</sub>	Weight		320			g

Notes:-

- 1) Unless otherwise indicated T<sub>j</sub>=175°C.
- 2) For other clamp forces, please consult factory.

## Notes on Ratings and Characteristics

### 1.0 Voltage Grade Table

Voltage Grade	$V_{RRM}$ V	$V_{RSM}$ V	$V_R$ DC V
24	2400	2500	1430
26	2600	2700	1550

### 2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

### 3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for  $T_j$  below 25°C.

### 4.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

### 5.0 Computer Modelling Parameters

#### 5.1 Device Dissipation Calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T} \quad \text{and:} \quad W_{AV} = \frac{\Delta T}{R_{th}}$$

$$\Delta T = T_{j \max} - T_K$$

Where  $V_{T0}=0.91V$ ,  $r_T=0.07m\Omega$ ,

$R_{th}$  = Supplementary thermal impedance, see table below and

$ff$  = Form factor, see table below.

Supplementary Thermal Impedance				
Conduction Angle	6 phase (60°)	3 phase (120°)	½ wave (180°)	d.c.
Square wave Double Side Cooled	0.01595	0.01492	0.01420	0.01300
Square wave Cathode Side Cooled	0.03182	0.03062	0.02979	0.02840
Sine wave Double Side Cooled	0.01524	0.01429	0.01326	
Sine wave Cathode Side Cooled	0.03099	0.02988	0.02871	

Form Factors				
Conduction Angle	6 phase (60°)	3 phase (120°)	½ wave (180°)	d.c.
Square wave	2.449	1.732	1.414	1
Sine wave	2.778	1.879	1.57	

## 5.2 Calculating $V_F$ using ABCD Coefficients

The on-state characteristic  $I_F$  vs.  $V_F$ , on page 6 is represented in two ways;

- (i) the well established  $V_{T0}$  and  $r_T$  tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for  $V_F$  in terms of  $I_F$  given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for  $V_F$  agree with the true device characteristic over a current range, which is limited to that plotted.

25°C Coefficients		175°C Coefficients	
A	1.051814	A	0.8929391
B	-0.06800862	B	-0.0845593
C	$-1.66854 \times 10^{-5}$	C	$-1.19951 \times 10^{-5}$
D	0.01288996	D	0.01682412

### 5.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left( 1 - e^{\frac{-t}{\tau_p}} \right)$$

Where  $p = 1$  to  $n$ ,  $n$  is the number of terms in the series and:

$t$  = Duration of heating pulse in seconds.

$r_t$  = Thermal resistance at time  $t$ .

$r_p$  = Amplitude of  $p_{th}$  term.

$\tau_p$  = Time Constant of  $r_{th}$  term.

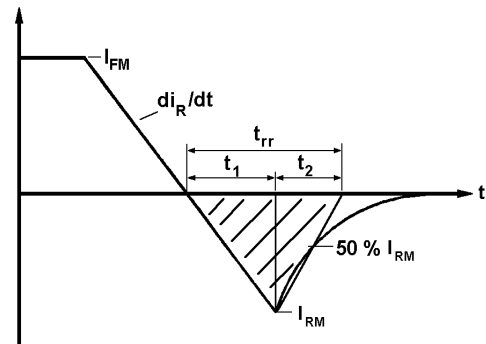
The coefficients for this device are shown in the tables below:

D.C. Double Side Cooled			
Term	1	2	3
$r_p$	$8.477143 \times 10^{-3}$	$3.147235 \times 10^{-3}$	$1.303295 \times 10^{-3}$
$\tau_p$	0.3623228	0.09512753	$5.059684 \times 10^{-3}$

D.C. Cathode Side Cooled			
Term	1	2	3
$r_p$	0.02049789	$6.404445 \times 10^{-3}$	$1.490447 \times 10^{-3}$
$\tau_p$	2.478652	0.1517615	$4.891107 \times 10^{-3}$

### 6.0 Reverse recovery ratings

(i)  $Q_{ra}$  is based on 50%  $I_{RM}$  chord as shown in Fig. 1



**Fig. 1**

(ii)  $Q_{rr}$  is based on a  $150\mu s$  integration time i.e.

$$Q_{rr} = \int_0^{150\mu s} i_{rr} \cdot dt$$

(iii)

$$K \text{ Factor} = \frac{t_1}{t_2}$$

## Curves

Figure 1 – Forward characteristics of Limit device

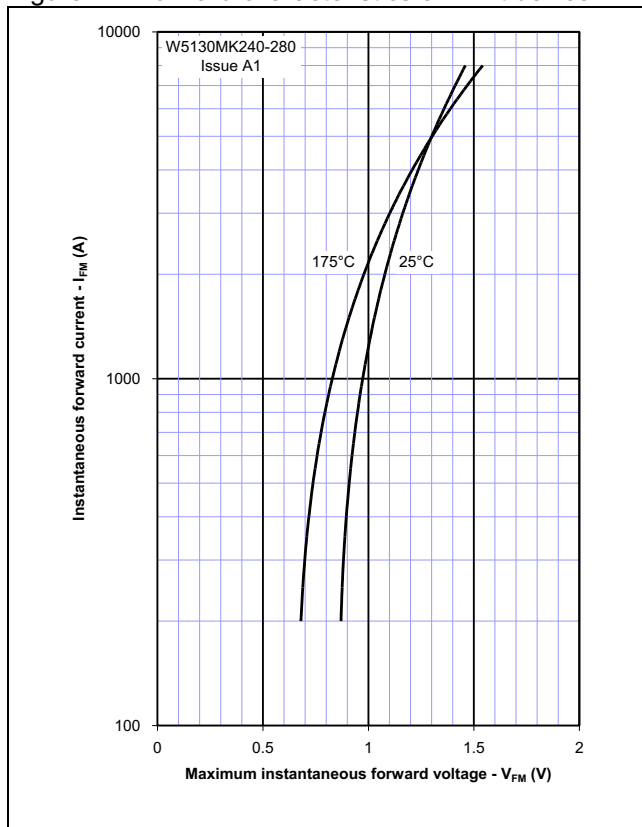


Figure 2 – Transient thermal impedance

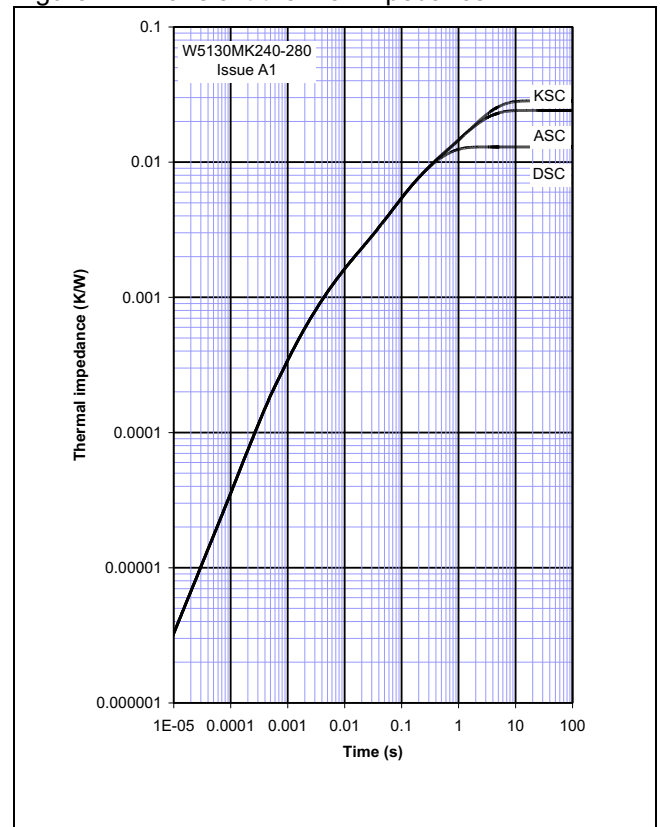


Figure 3 – Maximum Surge Rating

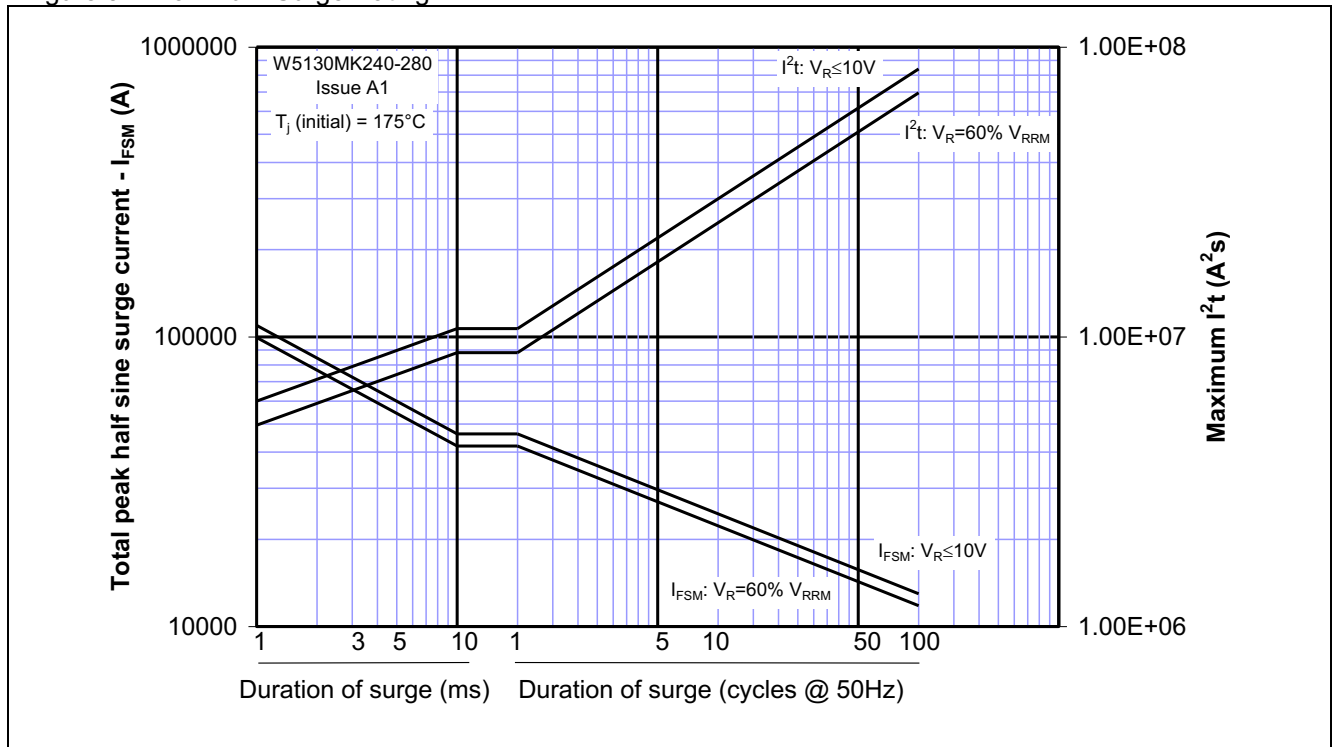


Figure 4 – Total recovered charge,  $Q_{rr}$

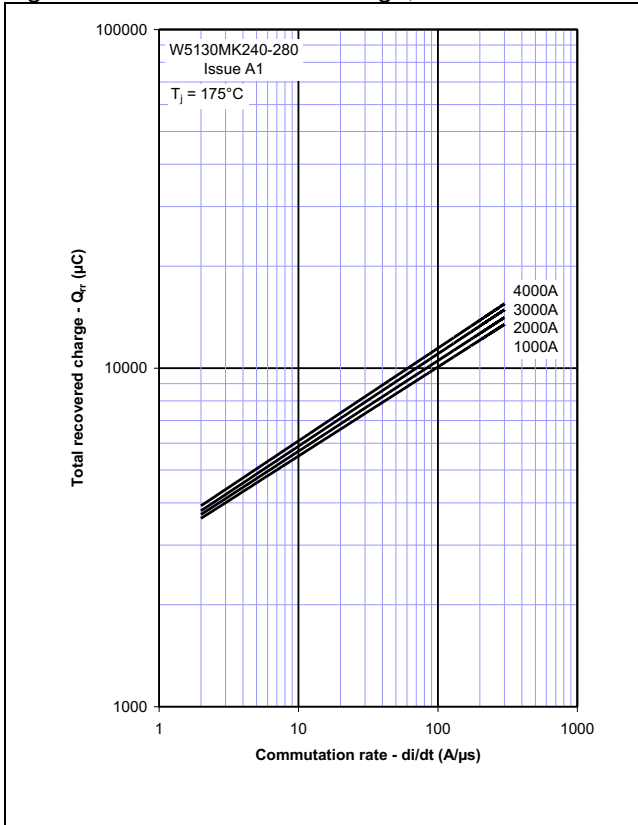


Figure 5 – Recovered charge,  $Q_{ra}$  (50% chord)

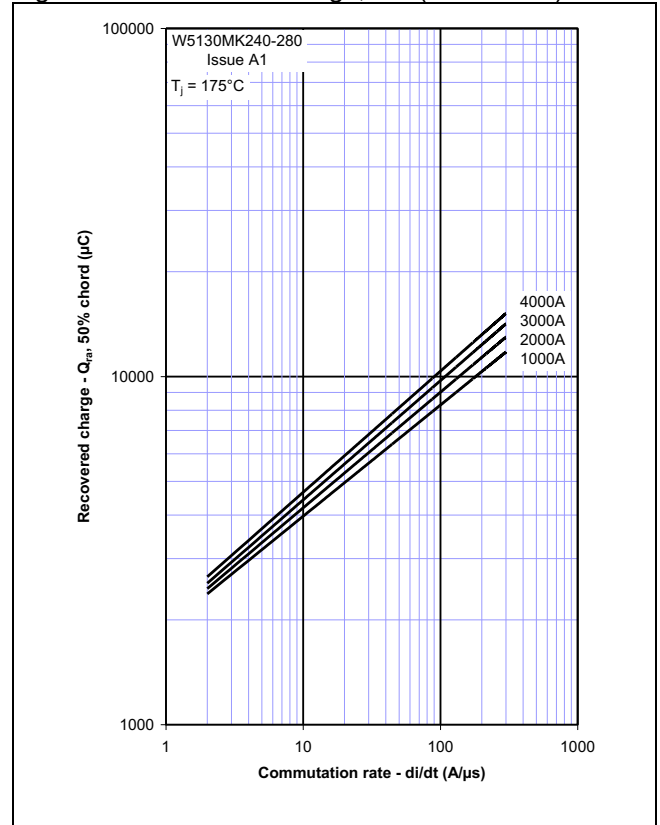


Figure 6 – Peak reverse recovery current,  $I_{rm}$

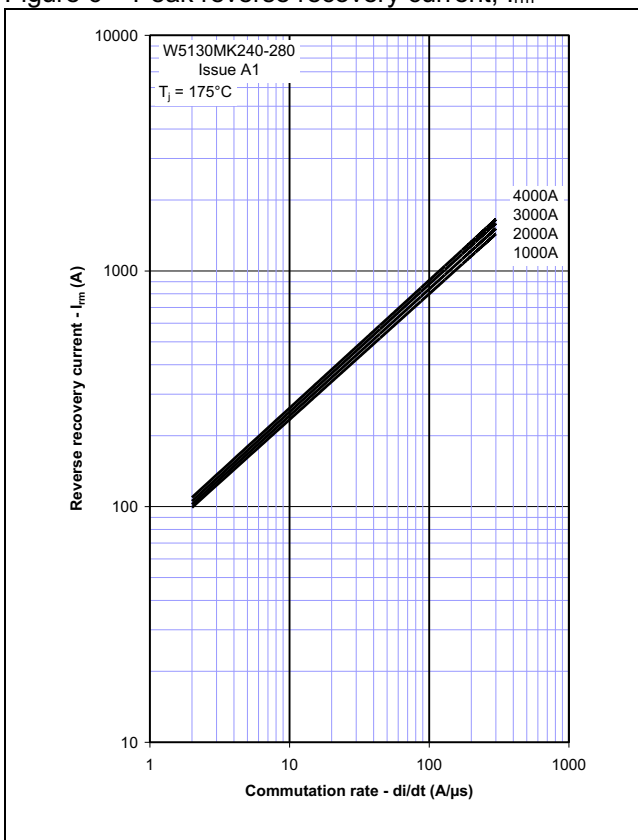


Figure 7 – Maximum recovery time,  $t_{rr}$  (50% chord)

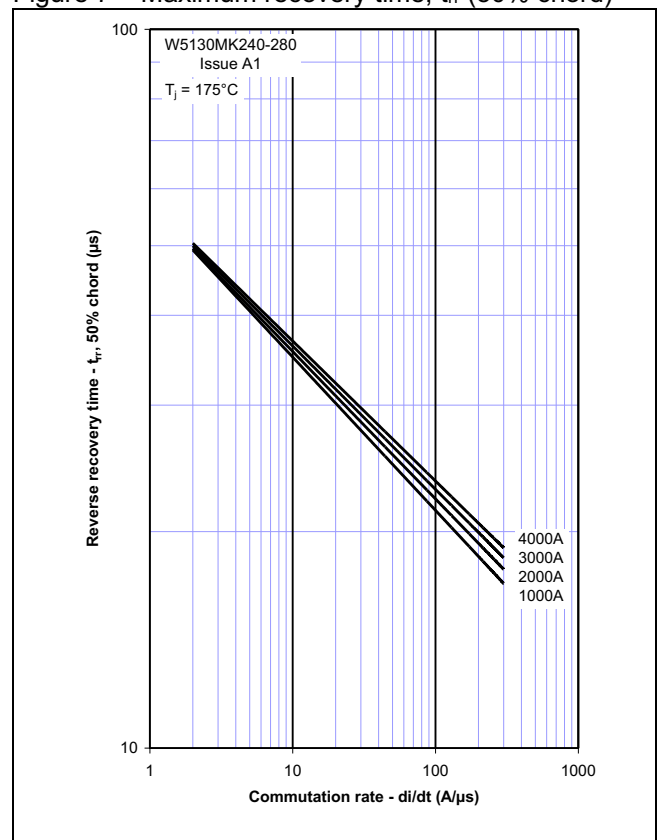


Figure 8 – Forward current vs. Power dissipation – Double Side Cooled

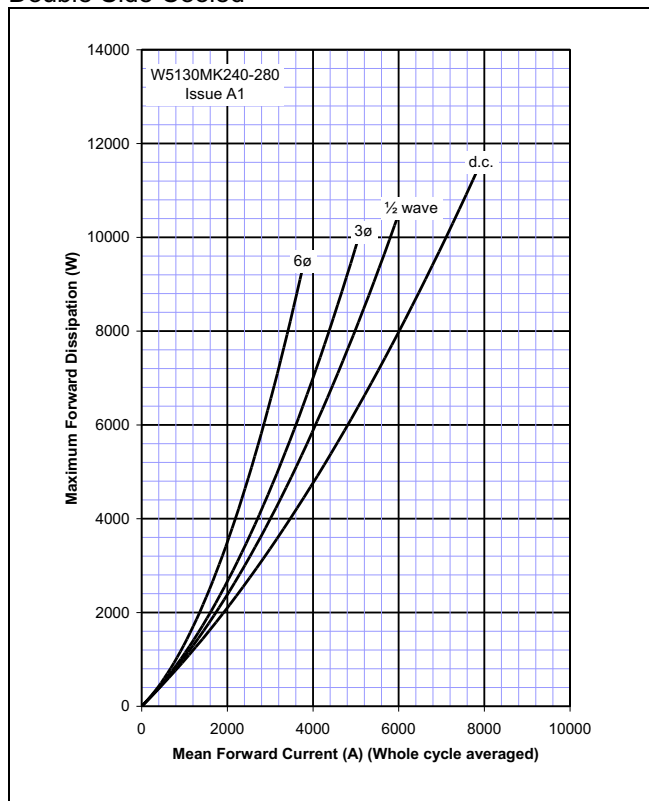


Figure 9 – Forward current vs. Heatsink temperature – Double Side Cooled

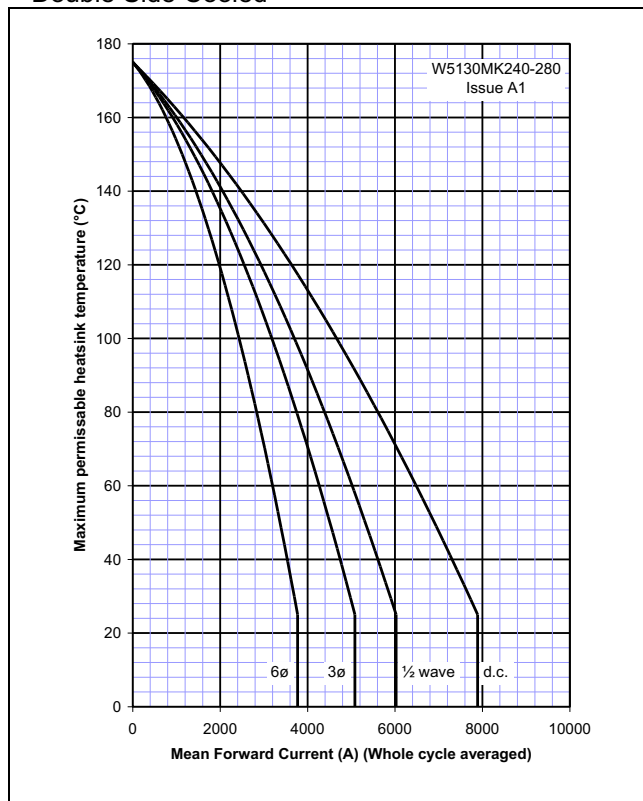


Figure 10 – Forward current vs. Power dissipation – Cathode Side Cooled

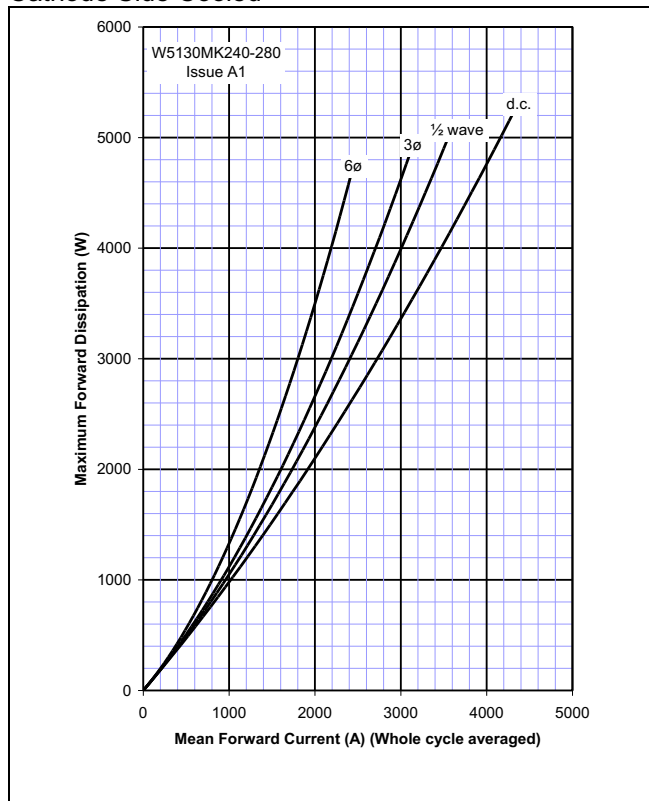
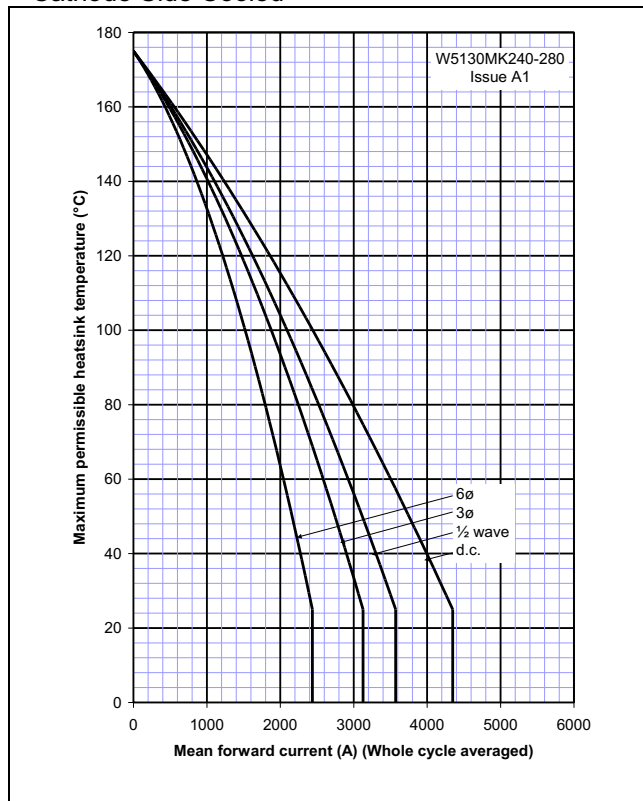
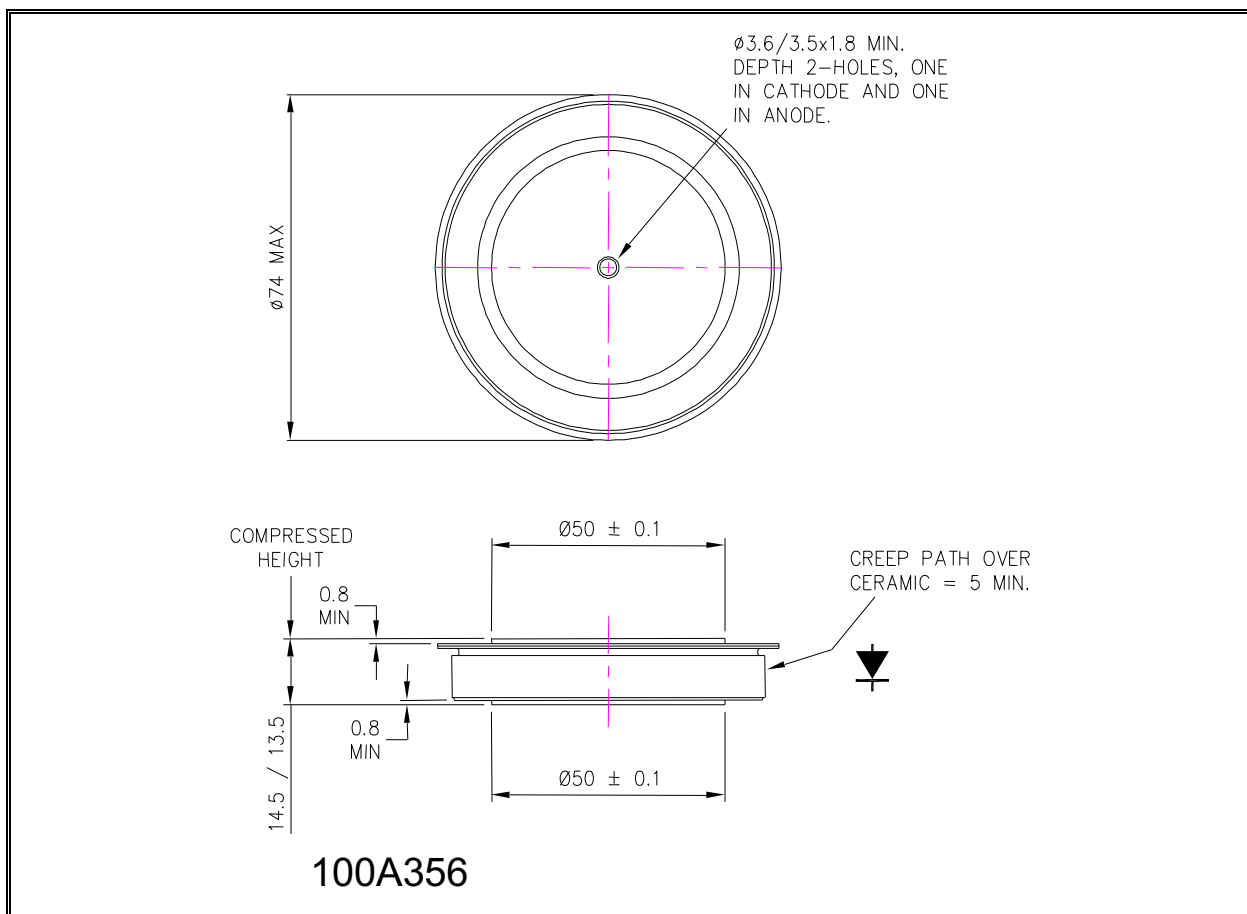


Figure 11 – Forward current vs. Heatsink temperature – Cathode Side Cooled





## Outline Drawing & Ordering Information



### ORDERING INFORMATION

(Please quote 10 digit code as below)

W5130	MK	◆◆	0
Fixed Type Code	Fixed Outline Code	Voltage code $V_{RRM}/100$ 24-28	Fixed code

Order code: W5130MK280 – 2800V  $V_{RRM}$ , 14mm clamp height capsule.

**IXYS Semiconductor GmbH**  
Edisonstraße 15  
D-68623 Lampertheim  
Tel: +49 6206 503-0  
Fax: +49 6206 503-627  
E-mail: [marcom@ixys.de](mailto:marcom@ixys.de)



**IXYS UK Westcode Ltd**  
Langley Park Way, Langley Park,  
Chippenham, Wiltshire, SN15 1GE.  
Tel: +44 (0)1249 444524  
Fax: +44 (0)1249 659448  
E-mail: [sales@ixysuk.com](mailto:sales@ixysuk.com)

**IXYS Corporation**  
1590 Buckeye Drive  
Milpitas CA 95035-7418  
Tel: +1 (408) 457 9000  
Fax: +1 (408) 496 0670  
E-mail: [sales@ixys.net](mailto:sales@ixys.net)

[www.ixysuk.com](http://www.ixysuk.com)

[www.ixys.com](http://www.ixys.com)

**IXYS Long Beach**  
IXYS Long Beach, Inc  
2500 Mira Mar Ave, Long Beach  
CA 90815  
Tel: +1 (562) 296 6584  
Fax: +1 (562) 296 6585  
E-mail: [service@ixyslongbeach.com](mailto:service@ixyslongbeach.com)

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