SC1006-01LTG

6V, 30kV, 5A, SOD523, Unidirectional TVS, General Purpose ESD protection









Note: This package image is for example and reference only. for detail package drawing, please refer to the package section in this datasheet.

Web Resources

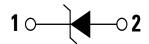


Download ECAD models, order samples, and find technical recources at www.littelfuse.com

Pinout



Functional Block Diagram



Description

The SC1006-01LTG unidirectional TVS is fabricated in a proprietary silicon avalanche technology. These diodes provide a high ESD (electrostatic discharge) protection level for electronic equipment.

The SC1006-01LTG TVS can safely absorb repetitive ESD strikes of ±30 kV (contact and air discharge as defined in IEC 61000-4-2) without any performance degradation. In addition, it can safely dissipate a 5A 8/20µs surge event as defined in IEC 61000-4-5, 2nd

Features & Benefits

- ESD, IEC 61000-4-2, ±30kV contact/air
- EFT, IEC 61000-4-4, 40A (5/50ns)
- Maximum surge tolerance, IEC 61000-4-5, 2nd Edition, 5A (8/20µs)
- Low leakage current of 0.5µA (MAX) at 6V
- Halogen-free, lead-free and RoHS compliant
- Moisture Sensitivity Level (MSL-1)

Applications

- Power tools
- PDAs
- Power tools
- Portable medical components
- Portable navigation components
- Battery protection

Life Support Note:

Not Intended for Use in Life Support or Life Saving Applications The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated



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Absolute Maximum Ratings

Symbol	Parameter	Value	Units
l _{pp}	Peak Current (t _p =8/20µs)	5	А
T _{op}	Operating Temperature	-40 to 125	°C
T_{stor}	Storage Temperature	-55 to 150	°C

Caution: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the component. This is a stress only rating and operation of the component at these or any other conditions above those indicated in the operational sections of this specification is not implied.

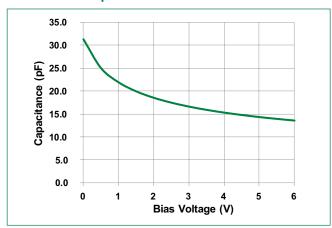
Electrical Characteristics (T_{OP}=25°C)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Reverse Standoff Voltage	V _{RWM}				6	V
Breakdown Voltage	V_{BR}	I _R =1mA		7.0		V
Forward Voltage Drop	V _F	I _F =1mA		8.0		V
Reverse Leakage Current	I _{LEAK}	V _R =6V			0.5	μΑ
01 1/11 1	\ /	I_{pp} =1A, t_p =8/20 μ s, I/O to GND		8.5		V
Clamp Voltage ¹	V _c	$I_{pp} = 5A$, $t_p = 8/20 \mu s$, I/O to GND		10.5		V
Dynamic Resistance ¹	R _{DYN}	$(V_c 2 - V_c 1)/(I_{PP} 2 - I_{PP} 1)$, I/O to GND		0.55		Ω
ESD Withstand Voltage ^{1,3}	V _{ESD}	IEC 61000-4-2 (Contact Discharge)	±30			kV
		IEC 61000-4-2 (Air Discharge)	±30			kV
Diode Capacitance ¹	C _{IO-GND}	Reverse Bias=0V, f=1MHz, I/O to GND		30		pF

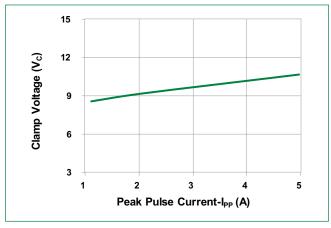
Note:

- 1. Parameter is guaranteed by design and/or device characterization.
- 2. Transmission Line Pulse (TLP) with 100ns width, 0.2ns rise time, and average window t1=70ns to t2= 90ns
- 3. Device stressed with ten non-repetitive ESD pulses.

Capacitance vs. Reverse Bias

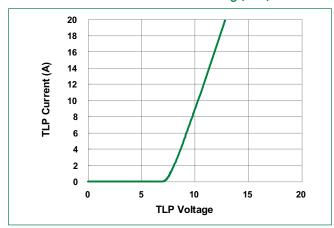


Clamping Voltage vs I_{PP}

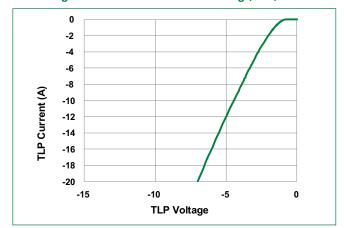




Positive Transmission Line Pulsing (TLP) Plot



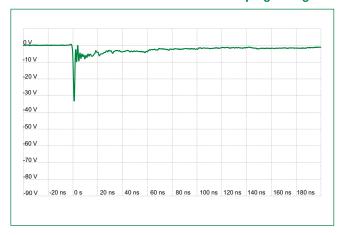
Negative Transmission Line Pulsing (TLP) Plot



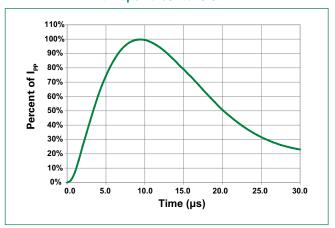
IEC 61000-4-2 +8 kV Contact ESD Clamping Voltage



IEC 61000-4-2 -8 kV Contact ESD Clamping Voltage



8/20µs Pulse Waveform



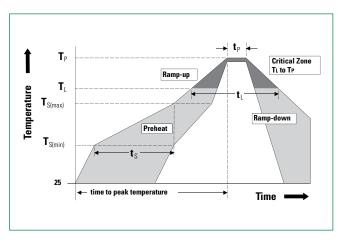


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Soldering Parameters

Reflow Condition Pb – Free assembly			
Pre Heat	-Temperature Min (T _{s(min)})	150°C	
	- Temperature Max (T _{s(max)})	200°C	
	-Time (min to max) (t.)	60 – 120 secs	
Average ramp up rate (Liquidus) Temp (T _L) to peak		3°C/second max	
T _{S(max)} to T _L - Ramp-up Rate		3°C/second max	
Reflow	-Temperature (T _L) (Liquidus)	217°C	
	-Temperature (t _L)	60 - 150 seconds	
Peak Temperature (T _P)		260 ^{+0/-5} °C	
Time within 5°C of actual peak Temperature (t _p)		30 seconds	
Ramp-down Rate		6°C/second max	
Time 25°C to peak Temperature (T _p)		8 minutes Max.	
Do not exceed		260°C	



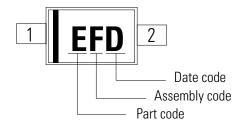
Ordering Information

Part Number	Package	Min. Order Qty.
SC1006-01LTG	SOD523	5,000

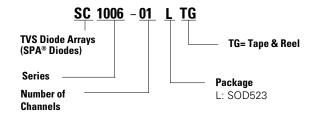
Product Characteristics

Lead Plating	Matte Tin
Lead material	Copper Alloy
Lead Coplanarity	0.0004 inches (0.102mm)
Substrate Material	Silicon
Body Material	Molded Compound
Flammability	UL Recognized compound meeting flammability rating V-0

Part Marking System

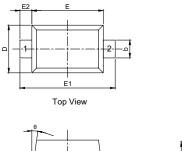


Part Numbering System

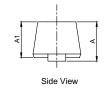


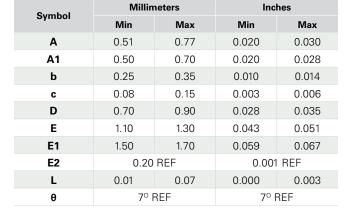


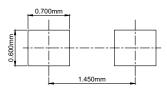
Package Dimensions — SOD523



Front View

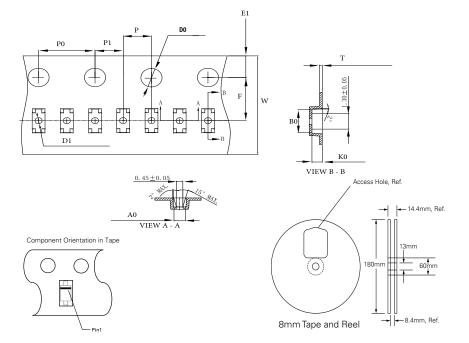






Recommended Soldering Pad Layout

Embossed Carrier Tape & Reel Specification — SOD523



Symbol	Millimeters
A0	0.85min/1.01max
В0	1.91+/-0.08
W	8.0+0.3/-0.10
D0	1.50+0.10
D1	ø1.00min/ø1.25max
E1	1.75+/-0.10
F	3.50+/-0.05
P0	4.00+/-0.10
P	2.00+/-0.05
P1	2.00+/-0.05
K0	0.68min/0.78max
T	0.254+/-0.13

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