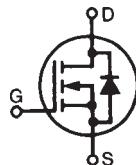
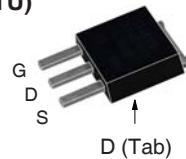


**X2-Class
Power MOSFET**

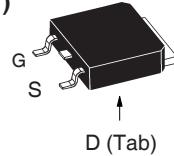
N-Channel Enhancement Mode

**IXTU4N70X2
IXTY4N70X2
IXTA4N70X2
IXTP4N70X2**
 V_{DSS} = 700V
 I_{D25} = 4A
 $R_{DS(on)}$ ≤ 850mΩ


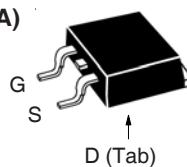
TO-251 (IXTU)



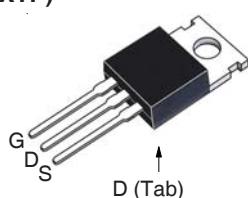
TO-252 (IXTY)



TO-263 (IXTA)



TO-220 (IXTP)


 G = Gate D = Drain
 S = Source Tab = Drain

Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	700	V
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C , $R_{GS} = 1\text{M}\Omega$	700	V
V_{GSS}	Continuous	±30	V
V_{GSM}	Transient	±40	V
I_{D25}	$T_C = 25^\circ\text{C}$	4	A
I_{DM}	$T_C = 25^\circ\text{C}$, Pulse Width Limited by T_{JM}	8	A
I_A	$T_C = 25^\circ\text{C}$	2	A
E_{AS}	$T_C = 25^\circ\text{C}$	150	mJ
dv/dt	$I_S \leq I_{DM}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ\text{C}$	50	V/ns
P_D	$T_C = 25^\circ\text{C}$	80	W
T_J		-55 ... +150	°C
T_{JM}		150	°C
T_{stg}		-55 ... +150	°C
T_L	Maximum Lead Temperature for Soldering	300	°C
T_{SOLD}	1.6 mm (0.062in.) from Case for 10s	260	°C
F_c	Mounting Force (TO-263 & TO-251)	10.65 / 2.2 ... 14.6	N/lb
M_d	Mounting Torque (TO-220)	1.13 / 10	Nm/lb.in
Weight	TO-251	0.40	g
	TO-252	0.35	g
	TO-263	2.50	g
	TO-220	3.00	g

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV_{DSS}	$V_{GS} = 0\text{V}$, $I_D = 250\mu\text{A}$	700		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	2.5		V
I_{GSS}	$V_{GS} = \pm 30\text{V}$, $V_{DS} = 0\text{V}$			±100 nA
I_{DSS}	$V_{DS} = V_{DSS}$, $V_{GS} = 0\text{V}$ $T_J = 125^\circ\text{C}$			5 μA 50 μA
$R_{DS(on)}$	$V_{GS} = 10\text{V}$, $I_D = 0.5 \cdot I_{D25}$, Note 1			850 mΩ

Features

- International Standard Packages
- Low $R_{DS(on)}$ and Q_G
- Avalanche Rated
- Low Package Inductance

Advantages

- High Power Density
- Easy to Mount
- Space Savings

Applications

- Switch-Mode and Resonant-Mode Power Supplies
- DC-DC Converters
- PFC Circuits
- AC and DC Motor Drives
- Robotics and Servo Controls

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
g_{fs}	$V_{DS} = 10\text{V}$, $I_D = 0.5 \cdot I_{D25}$, Note 1	2.6	4.0	S
R_{Gi}	Gate Input Resistance		13	Ω
C_{iss}	$V_{GS} = 0\text{V}$, $V_{DS} = 25\text{V}$, $f = 1\text{MHz}$	386		pF
C_{oss}		280		pF
C_{rss}		1		pF
Effective Output Capacitance				
$C_{o(er)}$	Energy related } $V_{GS} = 0\text{V}$	29		pF
$C_{o(tr)}$	Time related } $V_{DS} = 0.8 \cdot V_{DSS}$	80		pF
$t_{d(on)}$	$V_{GS} = 10\text{V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = 0.5 \cdot I_{D25}$ $R_G = 50\Omega$ (External)	20		ns
t_r		27		ns
$t_{d(off)}$		66		ns
t_f		28		ns
$Q_{g(on)}$	$V_{GS} = 10\text{V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = 0.5 \cdot I_{D25}$	11.8		nC
Q_{gs}		3.8		nC
Q_{gd}		3.5		nC
R_{thJC}	TO-220		1.56	$^\circ\text{C}/\text{W}$
R_{thCS}		0.50		$^\circ\text{C}/\text{W}$

Source-Drain Diode

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
I_s	$V_{GS} = 0\text{V}$		4	A
I_{SM}	Repetitive, pulse Width Limited by T_{JM}		16	A
V_{SD}	$I_F = I_S$, $V_{GS} = 0\text{V}$, Note 1		1.4	V
t_{rr}	$I_F = 2\text{A}$, $-di/dt = 100\text{A}/\mu\text{s}$ $V_R = 100\text{V}$	186		ns
Q_{RM}		1.3		μC
I_{RM}		14.0		A

Note 1. Pulse test, $t \leq 300\mu\text{s}$, duty cycle, $d \leq 2\%$.

PRELIMINARY TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,835,592 4,931,844 5,049,961 5,237,481 6,162,665 6,404,065B1 6,683,344 6,727,585 7,005,734B2 7,157,338B2
4,860,072 5,017,508 5,063,307 5,381,025 6,259,123B1 6,534,343 6,710,405B2 6,759,692 7,063,975B2
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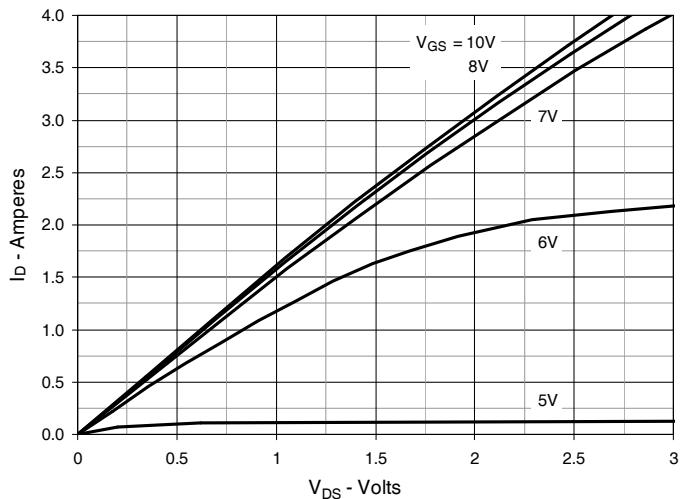
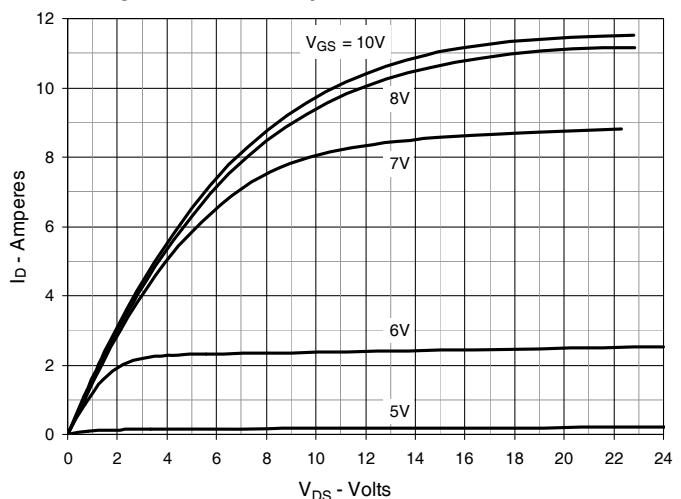
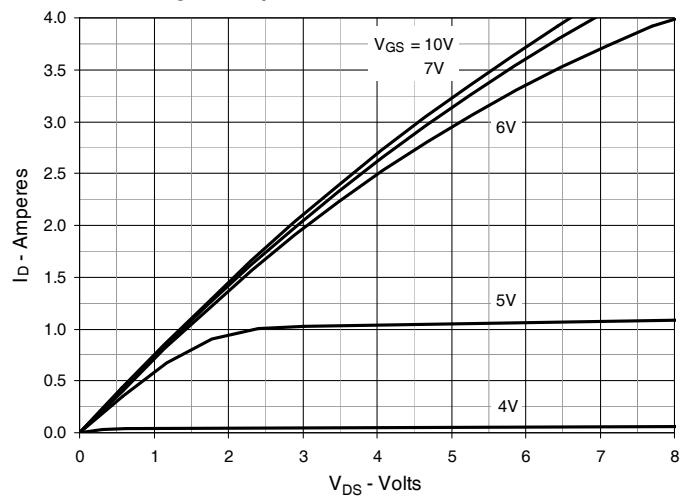
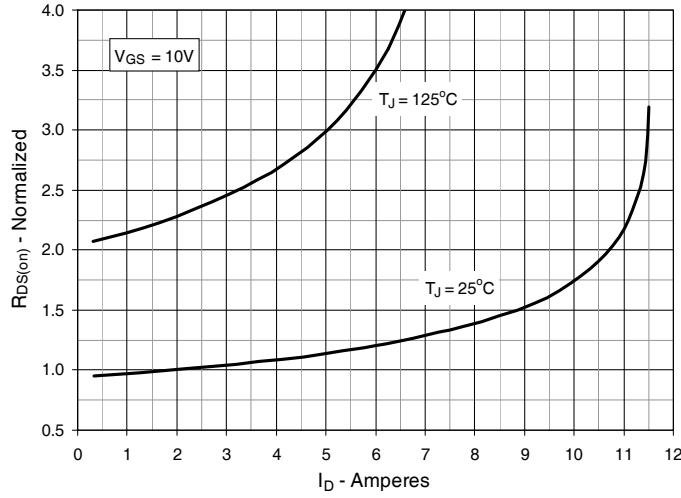
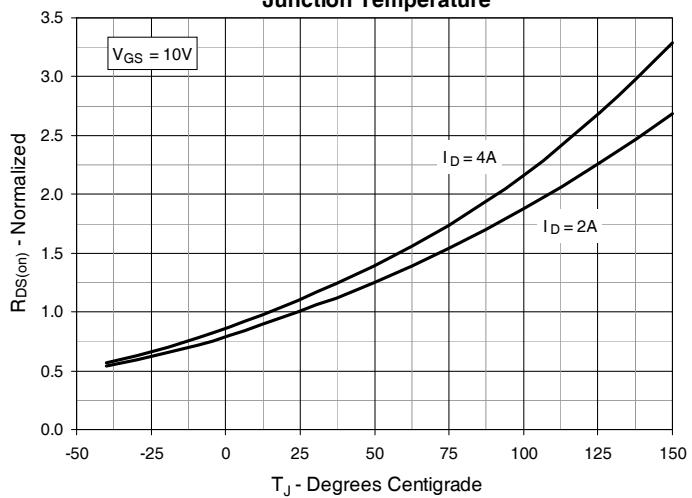
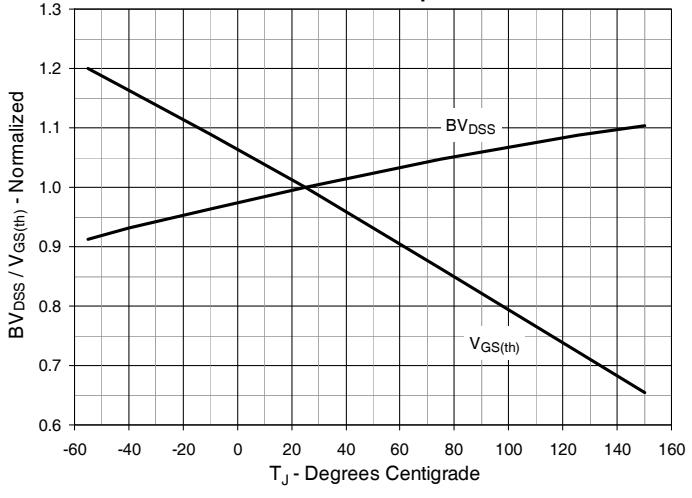
Fig. 1. Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 2. Extended Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 3. Output Characteristics @ $T_J = 125^\circ\text{C}$

Fig. 5. $R_{DS(on)}$ Normalized to $I_D = 2A$ Value vs. Drain Current

Fig. 4. $R_{DS(on)}$ Normalized to $I_D = 2A$ Value vs. Junction Temperature

Fig. 6. Normalized Breakdown & Threshold Voltages vs. Junction Temperature


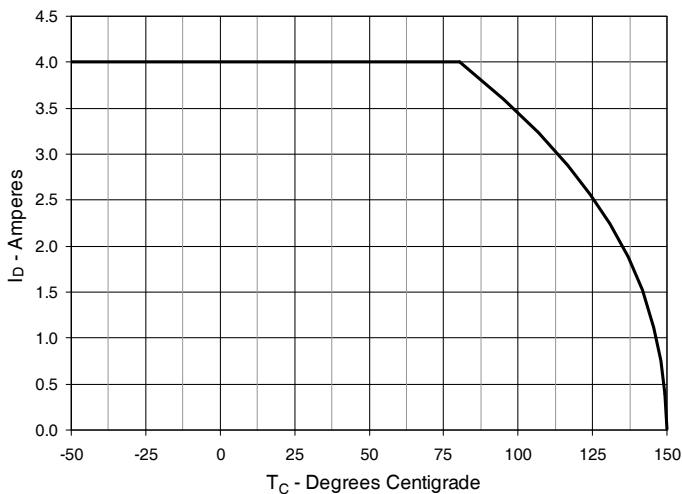
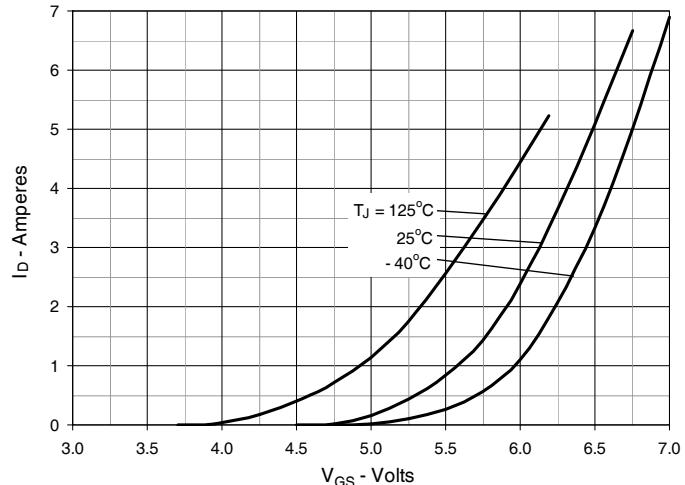
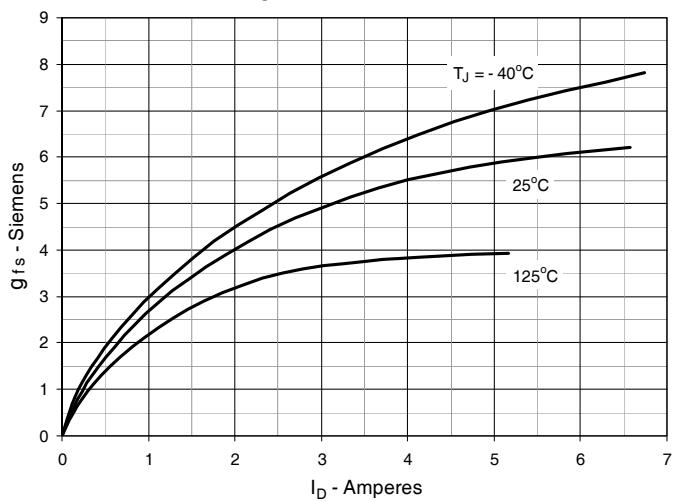
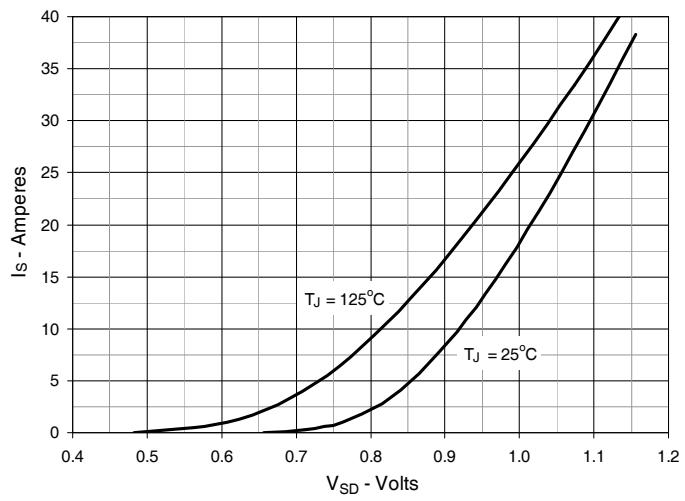
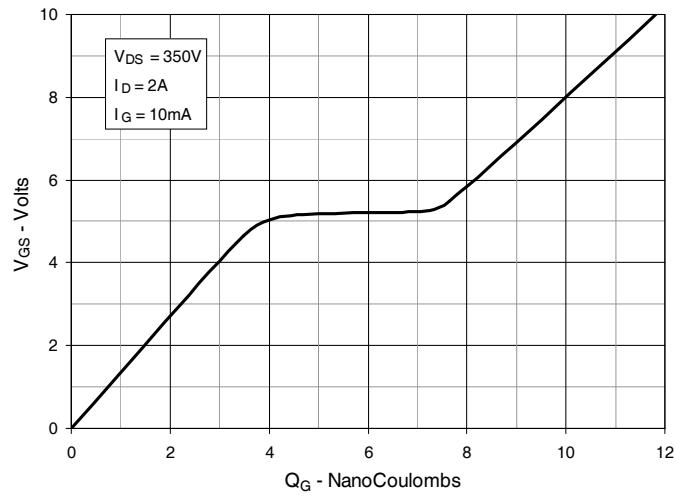
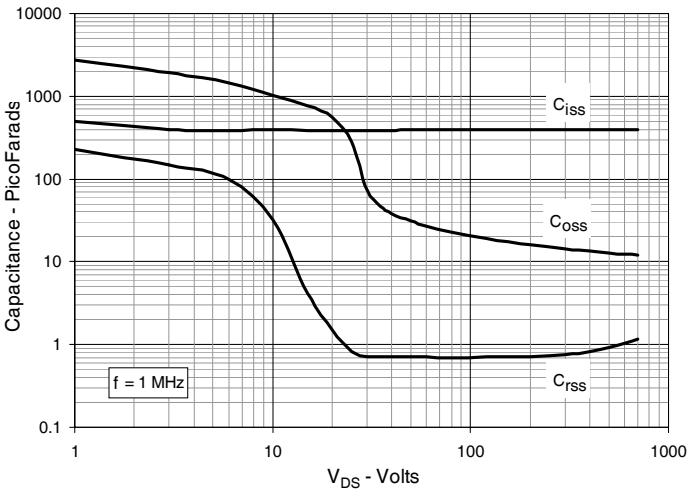
Fig. 7. Maximum Drain Current vs. Case Temperature

Fig. 8. Input Admittance

Fig. 9. Transconductance

Fig. 10. Forward Voltage Drop of Intrinsic Diode

Fig. 11. Gate Charge

Fig. 12. Capacitance


Fig. 13. Output Capacitance Stored Energy

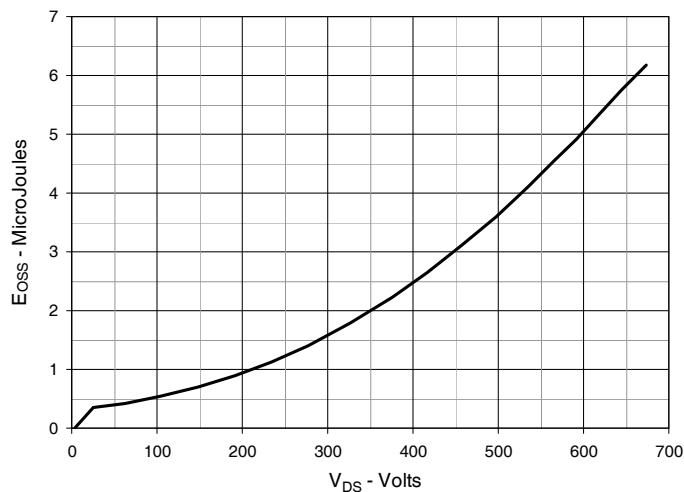


Fig. 14. Forward-Bias Safe Operating Area

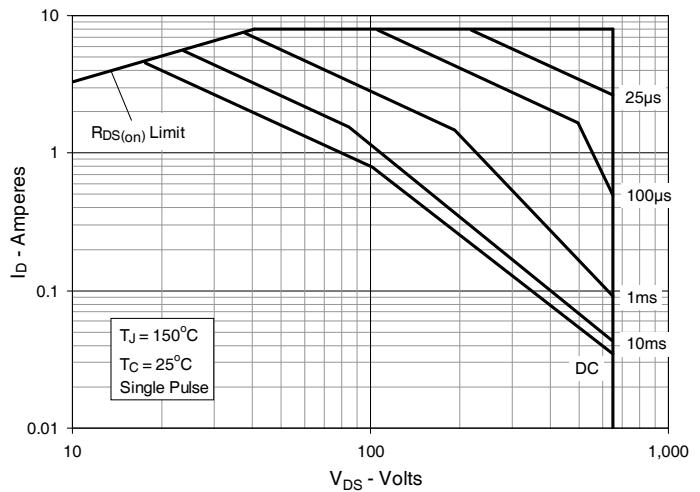
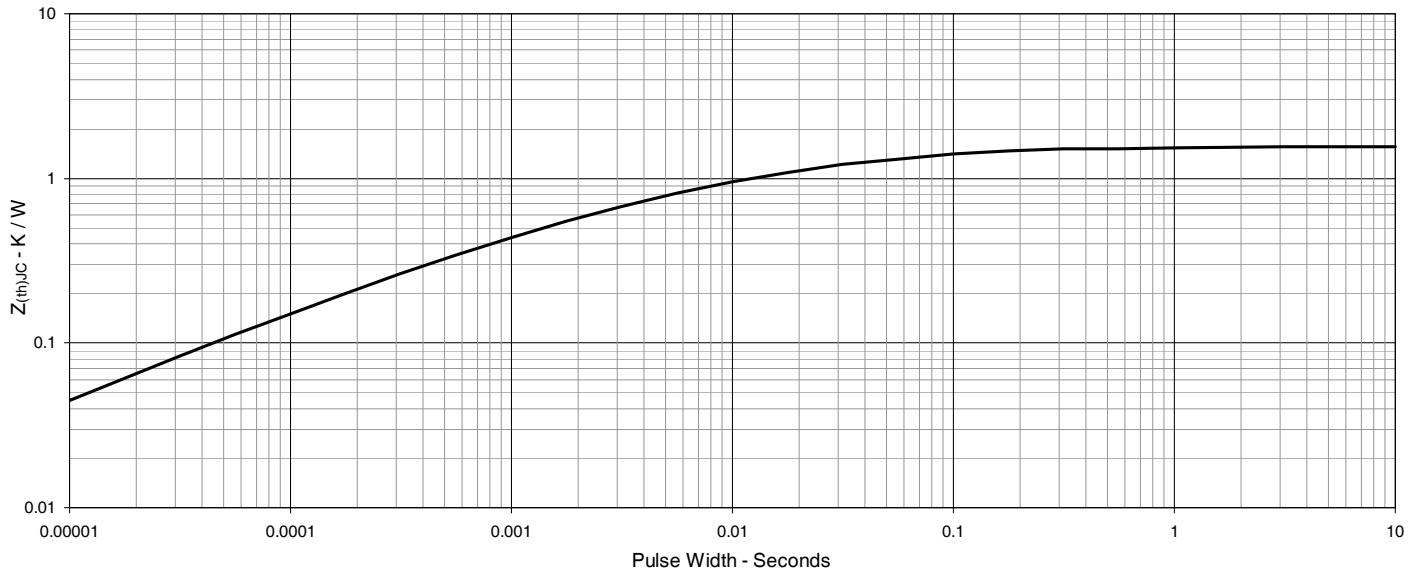
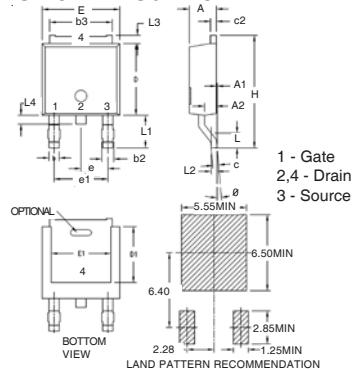
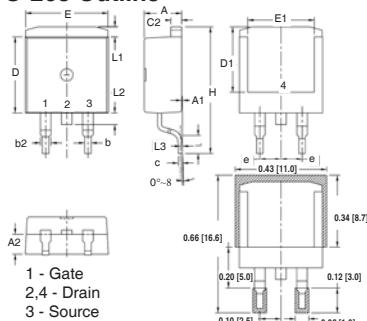
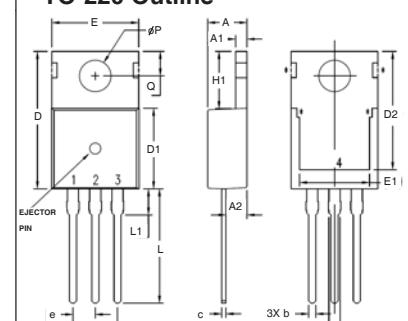
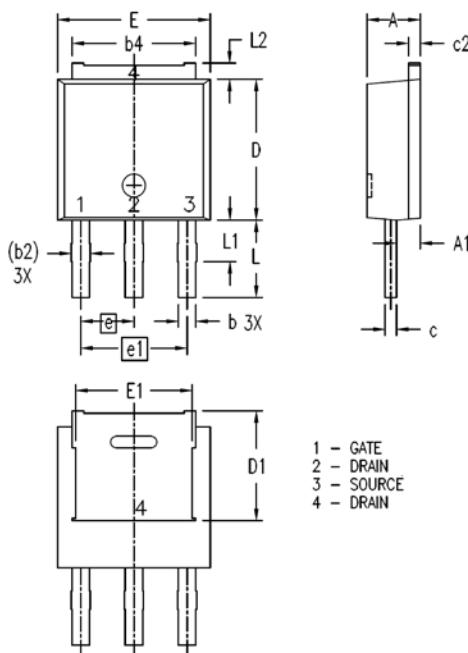


Fig. 15. Maximum Transient Thermal Impedance



TO-252 AA Outline

TO-263 Outline

TO-220 Outline

TO-251 OUTLINE




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