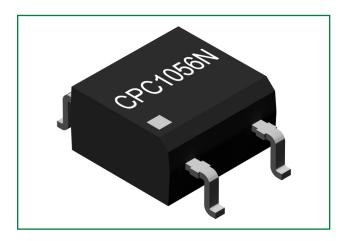
CPC1056N

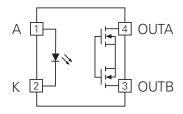
60 V, 75 mA_{RMS}/mA_{DC} Normally Open Relay

Key Attributes

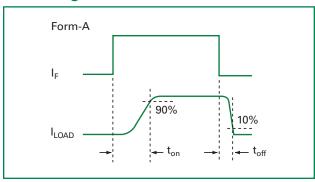
Characteristic	Rating	Unit
Blocking Voltage	60	V _P
Load Current	75	mA _{RMS} /mA _{DC}
On-Resistance (max.)	10	Ω
LED Current to Operate	0.5	mA



Pin Configuration



Switching Characteristics



Description







The CPC1056N is a bi-directional single-pole, normally open (1-Form-A) solid state relay in a 4-Pin SOP package that employs optically coupled MOSFET technology to provide 1500 V_{RMS} of input to output isolation.

The relay outputs are constructed with efficient MOSFET switches and a photovoltaic generator/driver that uses Littelfuse's patented OptoMOS® architecture while the input, a highly efficient infrared LED, provides the optically coupled control.

Using Littelfuse's state of the art double-molded vertical construction packaging, the CPC1056N offers board space savings of at least 20 % over the competitor's larger 4-Pin SOP relay.

Features

- 60 V, 75 mA, output switching capacity
- Input/Output Isolation 1500 V_{RMS}
- Compliant with EN50130-4 standard
- AC or DC load operation
- $t_{on}/t_{off} = 3 \text{ ms}$
- Low LED current of 0.5 mA, low output leakage current
- TTL/CMOS compatible logic inputs
- Solid state design with no moving parts

Applications

- Security
- Passive Infrared Detectors (PIR)
- Data Signalling
- Sensor Circuitry
- Instrumentation

Multiplexers

Data Acquisition

Electronic Switching

I/O Subsystems

- Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Industrial Controls

Approvals

1

- UL 1577 Recognized Component: File E76270
- EN 62368-1: TUV Certificate # B 082667 0008

Ordering Information

Part Number	Description
CPC1056N	4-Pin SOP (100/Tube)
CPC1056NTR	4-Pin SOP (2000/Reel)



Maximum ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data

Specifications

Absolute Maximum Ratings

Parameter	Ratings	Units	
Blocking Voltage	60	V _P	
Reverse Input Voltage	5	V	
Input Control Current			
Continuous	50	mA	
Peak (10 ms)	1	Α	
Power Dissipation			
Input	70	mW	
Package Total ¹	400	Imvv	
Isolation Voltage, Input to Output (60s)	1500	V _{RMS}	
ESD Rating, Human Body Model (HBM)	4	kV	
Operating Temperature, Ambient (T _A)	-40 to +85	°C.	
Storage Temperature (T _{STG})	-40 to +125		

Typical values are characteristics of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

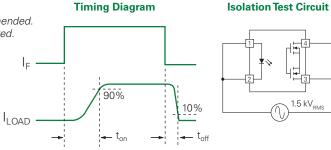
sheet is not implied.

Electrical Characteristics @ 25°C

Da	Conditions	Symbol	Value			11
Parameter			Minimum	Typical	Maximum	Units
Output Characteristics						
Blocking voltage	$I_L = 1 \mu A$	V _{DRM}	60	_		V
Load current						
Continuous 1	$I_F = 0.5 \mathrm{mA}$	IL	_	_	75	mA _{RMS} /mA _{DC}
Peak	t = 10 ms	I _{LPK}	_	_	±350	mA _P
On-resistance ²	$I_F = 0.5 \text{mA}, I_L = 75 \text{mA}$	R _{ON}	_	7	10	Ω
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = 60 \text{ V}$	I _{LK}	_	_	1	μΑ
Switching speeds						
Turn-on	1 2 m 1 1/ 10 1/	t _{on}		_	3	
Turn-off	$I_F = 2 \text{ mA}, V_L = 10 \text{ V}$	t _{off}	_	_	3	- ms
Output capacitance	$I_F = 0 \text{ mA}, V_L = 50 \text{ V},$ f = 1 MHz	C _{OUT}	_	5	_	pF
Input Characteristics						
Input control current to activate ³	$I_L = 75 \mathrm{mA}$	I _F	_	0.1	0.5	mA
Input control to deactivate 4						
Current		_	0.05	_	_	mA
Voltage	_	_	0.8	_	_	V
Input voltage drop	$I_F = 5 \text{mA}$	V _F	0.9	1.36	1.5	V
Reverse input current	$V_R = 5 V$	I _R	_	_	10	μΑ
Input/Output Characteristics	·					•
Capacitance, input to output	$V_{10} = 0 \text{ V, } f = 1 \text{ MHz}$	C _{IO}	_	1	_	pF

 $^{^{1}}$ Load current derates linearly from 75 mA @ 55 °C to 50 mA @ 85 °C.

⁴ Either condition is sufficient to deactivate the relay, both are not required.



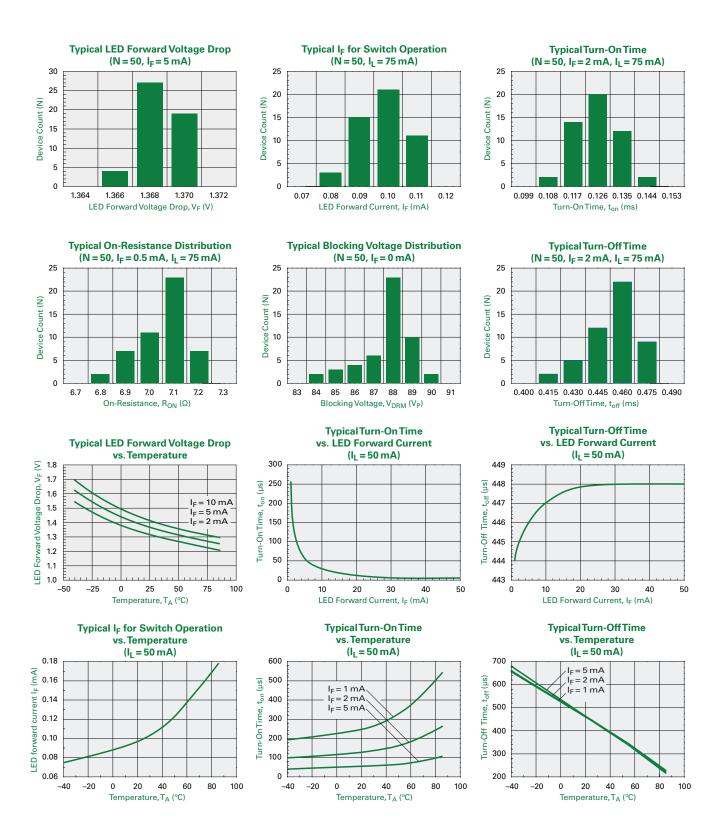


¹ Derate output power linearly 3.3 mW/°C

² Measurement taken within 1 second of on-time.

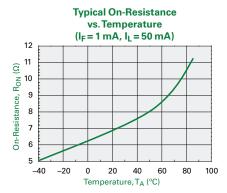
³ For operation above 60 °C a minimum input current of 2 mA is recommended.

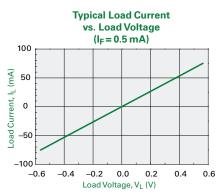
Characteristic Curves

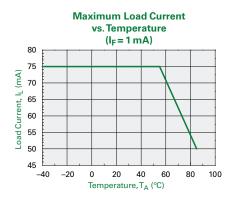


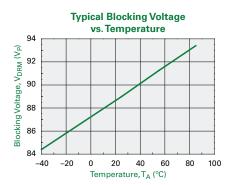
Unless otherwise noted, data presented in these graphs is typical of device operation at $T_A = 25$ °C.

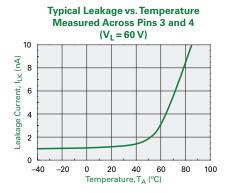


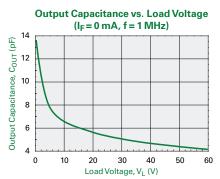


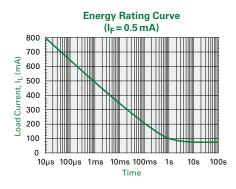












Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. Littelfuse classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard,

IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL)** classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification	
CPC1056N	MSL 3	

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

Soldering Profile

Provided in the table below is the **IPC/JEDEC J-STD-020** Classification Temperature (T_C) and the maximum dwell time ($T_C - 5$ °C). The Classification Temperature sets the Maximum Body Temperature allowed for these devices during reflow soldering processes.

Device	Classification Temperature (T _C)	Dwell Time (T _P)	Max Reflow Cycles
CPC1056N	260°C	30 seconds	3

Board Wash

Littelfuse recommends the use of no-clean flux formulations. Board washing to reduce, or remove flux residue following the solder reflow process is acceptable, provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: Using a low pressure wash and providing a follow-up bake cycle sufficient to remove any moisture trapped within the device, due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning, or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.

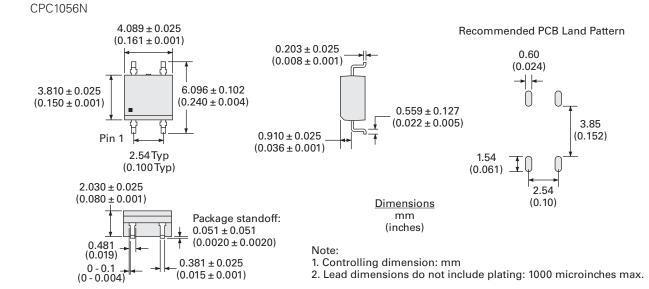








Mechanical Dimensions



CPC1056NTR Tape and Reel Packaging

