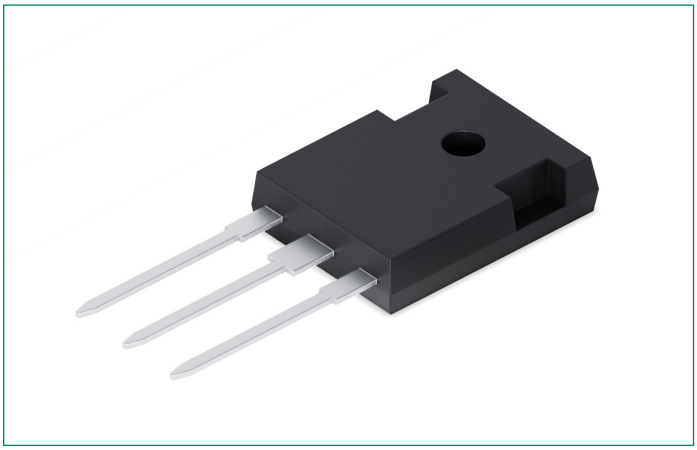
IXFH34N65X2W

650 V, 34 A, 100 mΩ, X2 Class HiPerFETTM Power MOSFET

N-Channel Enhancement Mode, Avalanche Rated



Features

- International standard package
- High power density
- Low $R_{DS(on)}$ and Q_g
- Avalanche rated

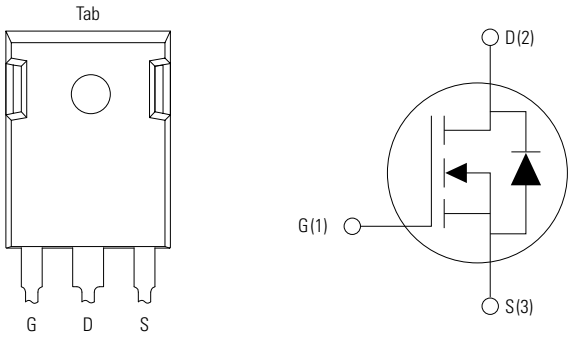
Benefits

- Reduced conduction losses
- Reduced driver power requirements
- Easy to mount
- Space savings

Applications

- Switch-mode and resonant-mode power supplies
- DC-DC converters
- PFC circuits
- AC and DC motor drives
- Robotics and servo controls

Pinout Diagram TO-247 (IXFH)



G: Gate; **D:** Drain; **S:** Source; **Tab:** Drain

Product Summary

Characteristic	Value	Unit
V_{DSS}	650	V
I_{D25}	34	A
$R_{DS(on)}$	≤ 100	mΩ

Revised: AO.06/02/2026 (0.1)

Maximum Ratings

Symbol	Characteristic	Conditions	Value	Unit
V_{DSS}	Drain-source voltage	T_{vj} = 25 °C to 150 °C	650	V
V_{GSS}	Gate-source voltage	Continuous	±30	V
V_{GSM}		Transient	±40	
I_{D25}	Drain current	T_c = 25 °C	34	A
I_{DM}	Peak drain current	T_c = 25 °C, pulse width limited by $T_{vj(max)}$	68	
I_{AS}	Single pulse avalanche current	T_c = 25 °C	7	A
E_{AS}	Single pulse avalanche energy	T_c = 25 °C	1	J
dv/dt	Source-drain diode dv/dt capability	$I_S \le I_{DM}$, $V_{DD} \le V_{DSS}$, $T_{vj} \le 150$ °C	50	V/ns
P_{tot}	Total power dissipation	T_c = 25 °C	540	W
T_{vj}	Virtual junction temperature range	–	−55 to +150	°C
$T_{vj(max)}$	Maximum virtual junction temperature	–	150	
T_{stg}	Storage temperature range	–	−55 to +150	
T_{sld}	Soldering Temperature	Maximum lead temperature for soldering	300	°C
M_d	Mounting torque	–	1.13 / 10	Nm/lb.in
G	Weight	–	6	g

Thermal Characteristics

Symbol	Characteristic	Value			Unit
		Min.	Typ.	Max.	
$R_{th(j-c)}$	Thermal resistance junction to case	–	–	0.23	K/W
$R_{th(c-h)}$	Thermal resistance case to heat sink	–	0.21	–	K/W

Electrical Characteristics – Static (T_{vj} = 25 °C unless otherwise specified)

Symbol	Characteristic	Conditions	Value			Unit
			Min.	Typ.	Max.	
$V_{(BR)DSS}$	Drain-source breakdown voltage	I_D = 1 mA, V_{GS} = 0 V	650	–	–	V
$V_{GS(th)}$	Gate threshold voltage	I_D = 2.5 mA, V_{GS} = V_{DS}	3.5	–	5.0	V
I_{GSS}	Gate-source leakage current	V_{DS} = 0 V, V_{GS} = ±30 V	–	–	±100	nA
I_{DSS}	Drain-source leakage current	V_{DS} = V_{DSS}, V_{GS} = 0 V	–	–	10	µA
		V_{DS} = V_{DSS}, V_{GS} = 0 V, T_{vj} = 125 °C	–	–	1.75	mA
$R_{DS(on)}$	Drain-source on-resistance[1]	V_{GS} = 10 V, I_D = 0.5 x I_{D25}	–	–	100	mΩ

Note 1: Pulse test, t ≤ 300 µs, duty cycle, d ≤ 2 %

Electrical Characteristics – Dynamic (T_{vj} = 25 °C unless otherwise specified)

Symbol	Characteristic	Conditions	Value			Unit
			Min.	Typ.	Max.	
g_{fs}	Transconductance[1]	V_{DS} = 10 V, I_D = 0.5 x I_{D25}	18	29	–	S
$R_{g(int)}$	Gate input resistance	–	–	1.0	–	Ω
C_{iss}	Input capacitance	V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz	–	3200	–	pF
C_{oss}	Output capacitance		–	1560	–	
C_{rss}	Reverse transfer capacitance		–	1.4	–	
$C_{o(er)}$	Effective output capacitance – energy related	V_{GS} = 0 V, V_{DS} = 0.8 x V_{DSS}	–	127	–	pF
$C_{o(tr)}$	Effective output capacitance – time related		–	460	–	
Q_G	Total gate charge	V_{GS} = 10 V, V_{DS} = 0.5 x V_{DSS}, I_D = 0.5 x I_{D25}	–	64	–	nC
Q_{GS}	Gate-source charge		–	22	–	
Q_{GD}	Gate-drain charge		–	22	–	
$t_{d(on)}$	Turn-on delay time	**Resistive Switching** V_{GS} = 10 V, V_{DS} = 0.5 x V_{DSS}, I_D = 0.5 x I_{D25}, $R_{G(ext)}$ = 10 Ω	–	38	–	ns
t_r	Rise time		–	52	–	
$t_{d(off)}$	Turn-off delay time		–	66	–	
t_f	Fall time		–	31	–	

Note 1: Pulse test, t ≤ 300 µs, duty cycle, d ≤ 2 %

Source-Drain Diode Characteristics (T_{vj} = 25°C unless otherwise specified)

Symbol	Characteristic	Conditions	Value			Unit
			Min.	Typ.	Max.	
I_S	Continuous diode forward current	V_{GS} = 0 V	–	–	34	A
I_{SM}	Diode pulse current	Repetitive, pulse width limited by $T_{vj(max)}$	–	–	136	A
V_{SD}	Diode forward voltage[1]	I_F = I_S, V_{GS} = 0 V	–	–	1.4	V
t_{rr}	Reverse recovery time	I_F = 17 A, –di/dt = 100 A/µs, V_R = 100 V, V_{GS} = 0 V	–	180	–	ns
Q_{rr}	Reverse recovery charge		–	1.44	–	µC
I_{rrm}	Peak reverse recovery current		–	16	–	A

Note 1: Pulse test, t ≤ 300 µs, duty cycle, d ≤ 2 %

Characteristic Curves

Fig. 1. Output Characteristics @ T_J = 25ºC

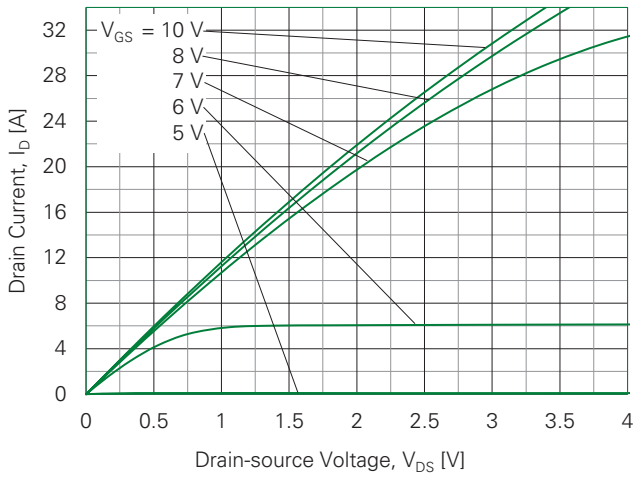


Fig. 2. Extended Output Characteristics @ T_J = 25ºC

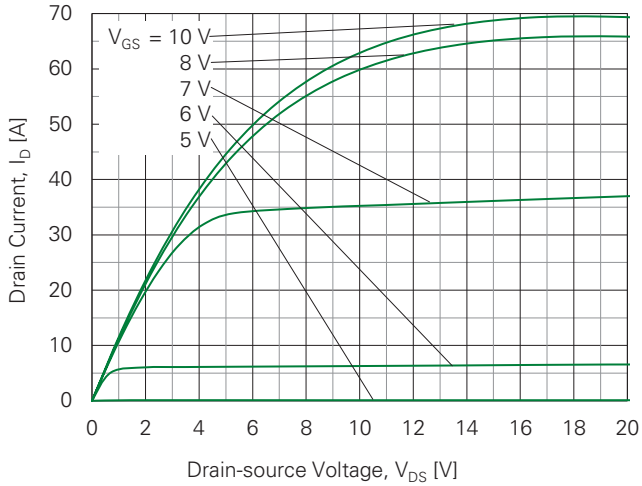


Fig. 3. Output Characteristics @ T_J = 125ºC

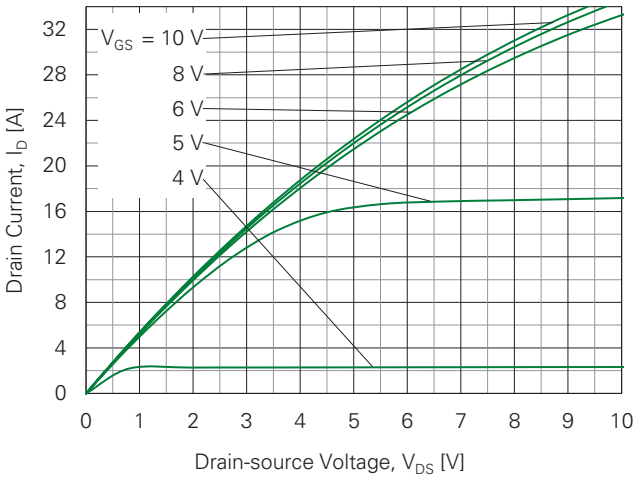


Fig. 4. $R_{DS(on)}$ Normalized to I_D = 17 A Value vs. Junction Temperature

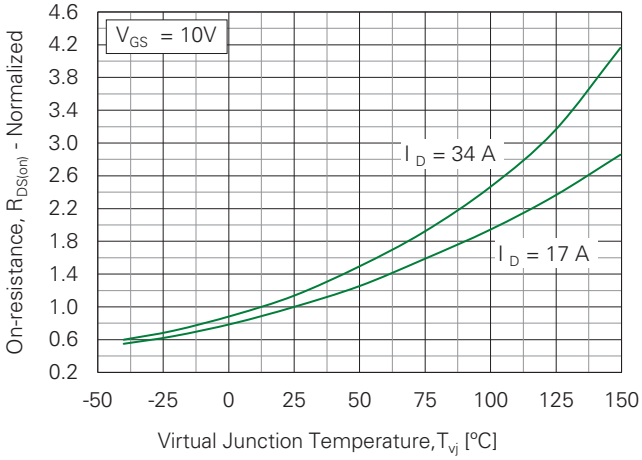


Fig. 5. $R_{DS(on)}$ Normalized to I_D = 17 A Value vs. Drain Current

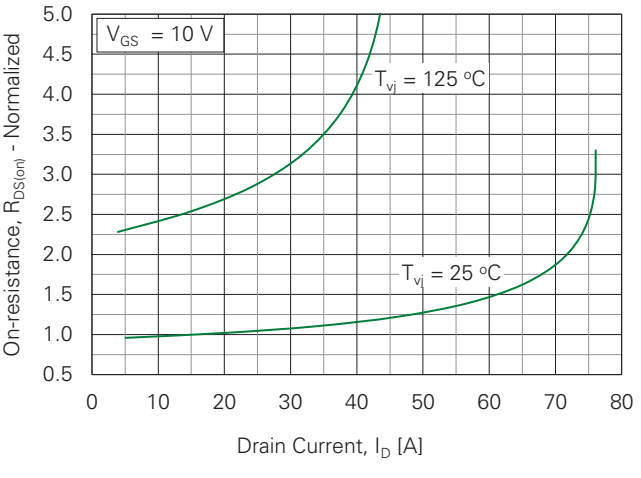


Fig. 6. Normalized Breakdown & Threshold Voltages vs. Junction Temperature

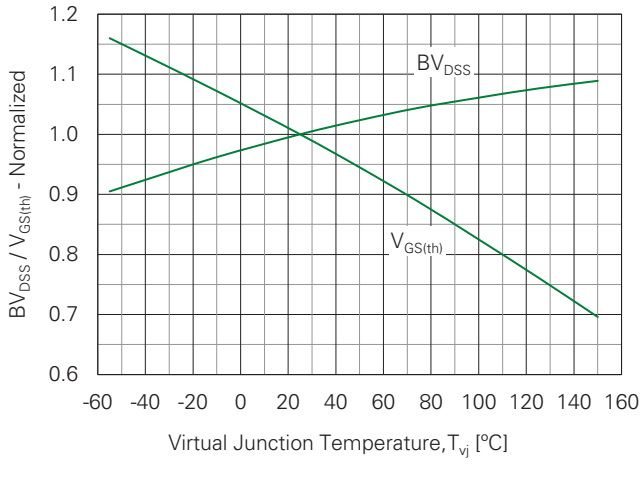


Fig. 7. Maximum Drain Current vs. Case Temperature

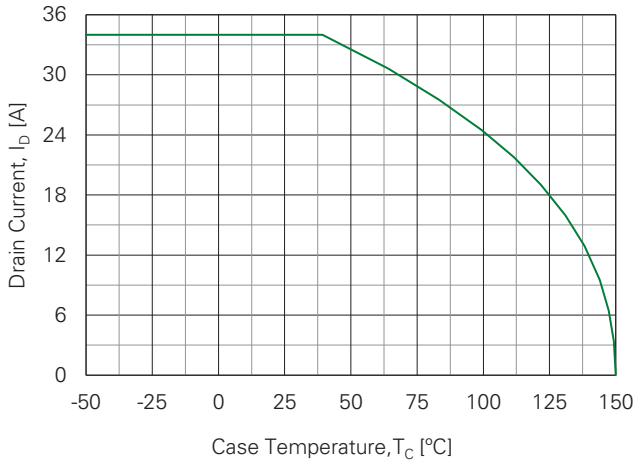


Fig. 8. Input Admittance

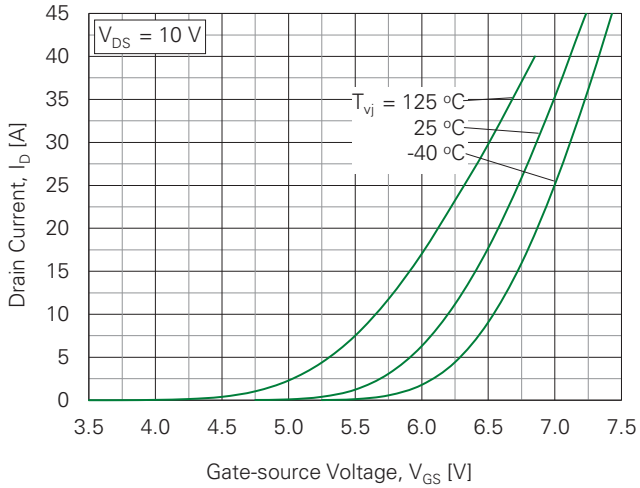


Fig. 9. Transconductance

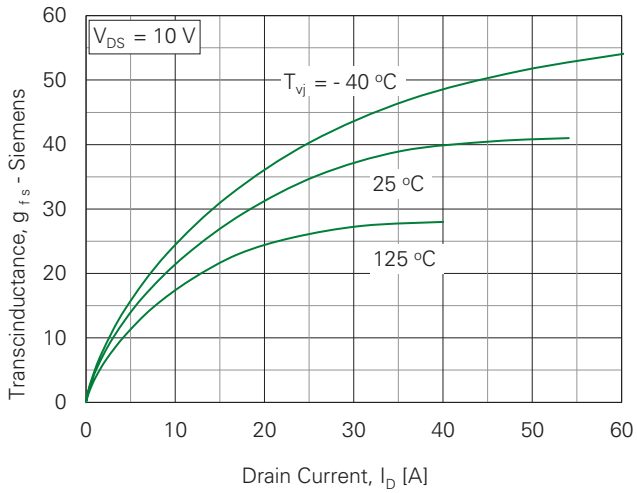


Fig. 10. Forward Voltage Drop of Intrinsic Diode

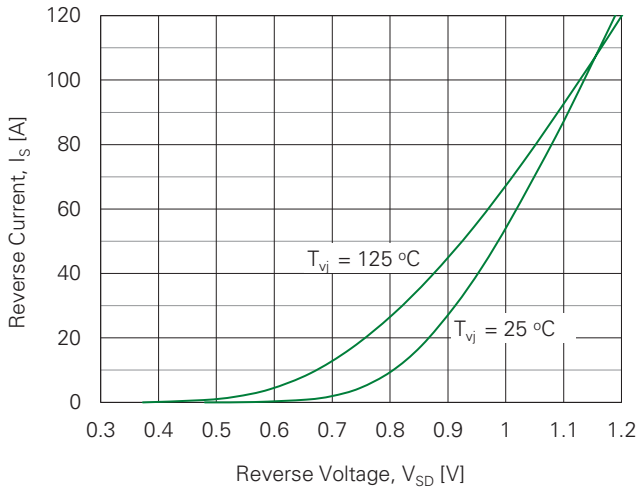


Fig. 11. Gate Charge

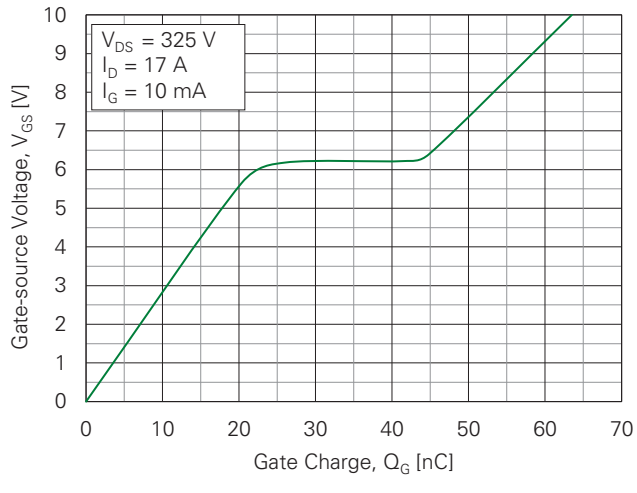


Fig. 12. Capacitance

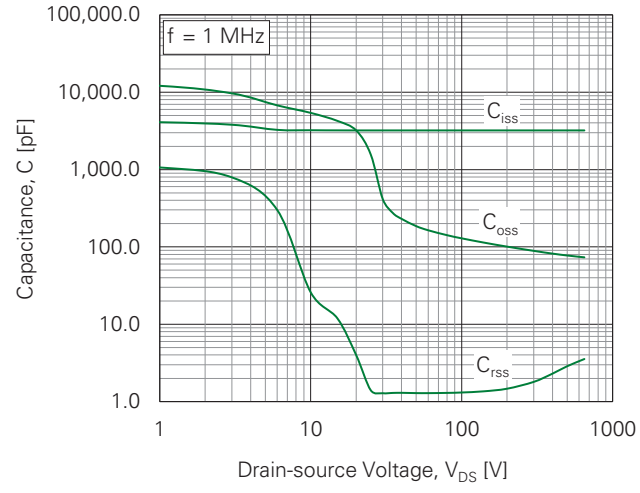


Fig. 13. Output Capacitance Stored Energy

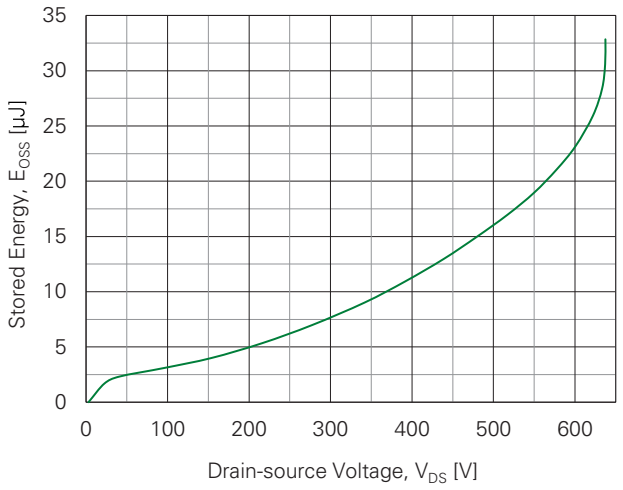


Fig. 14. Forward-Bias Safe Operating Area

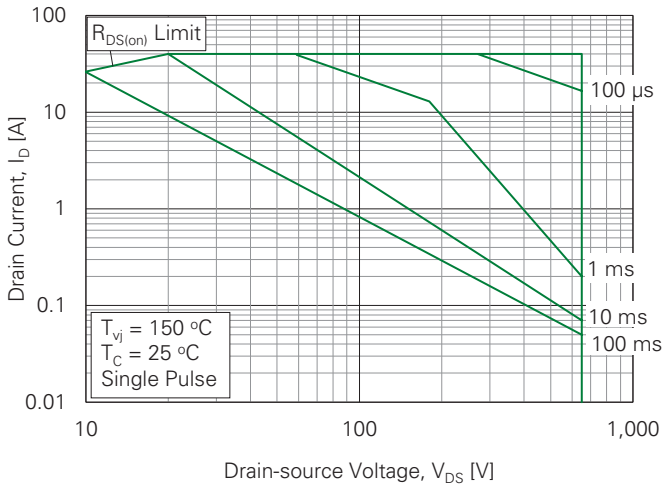
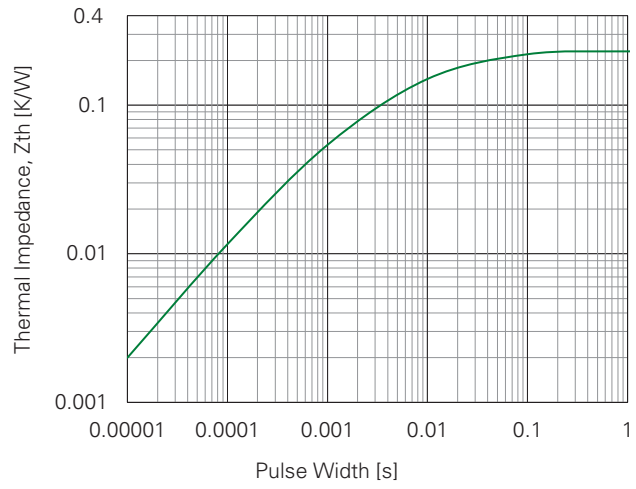
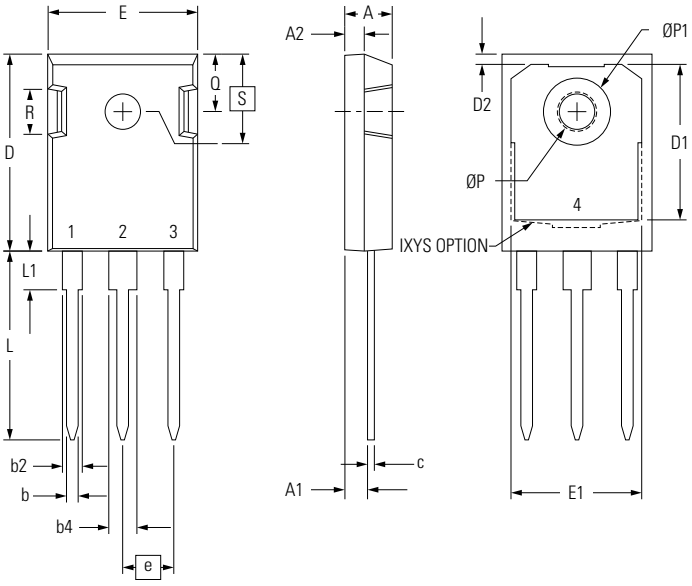


Fig. 15. Maximum Transient Thermal Impedance



Part Outline Drawing TO-247 (IXFH)



Symbol	Inches			Millimeters		
	Min.	Typical	Max.	Min.	Typical	Max
A	0.190	–	0.205	4.83	–	5.21
A1	0.090	–	0.100	2.29	–	2.54
A2	0.075	–	0.085	1.91	–	2.16
b	0.045	–	0.055	1.14	–	1.40
b2	0.075	–	0.087	1.91	–	2.20
b4	0.115	–	0.126	2.92	–	3.20
c	0.024	–	0.031	0.61	–	0.80
D	0.819	–	0.840	20.80	–	21.34
D1	0.650	–	0.690	16.51	–	17.53
D2	0.035	–	0.050	0.89	–	1.27
E	0.620	–	0.635	15.57	–	16.13
E1	0.545	–	0.565	13.84	–	14.35
e	0.215 BSC			5.45 BSC		
L	0.780	–	0.810	19.81	–	20.57
L1	0.150	–	0.170	3.81	–	4.32
ØP	0.140	–	0.144	3.55	–	3.65
ØP1	0.275	–	0.290	6.99	–	7.37
Q	0.220	–	0.244	5.59	–	6.20
R	0.170	–	0.190	4.62	–	4.83
S	0.242 BSC			6.15 BSC		

Revised: A0.06/02/2026 (0.1)