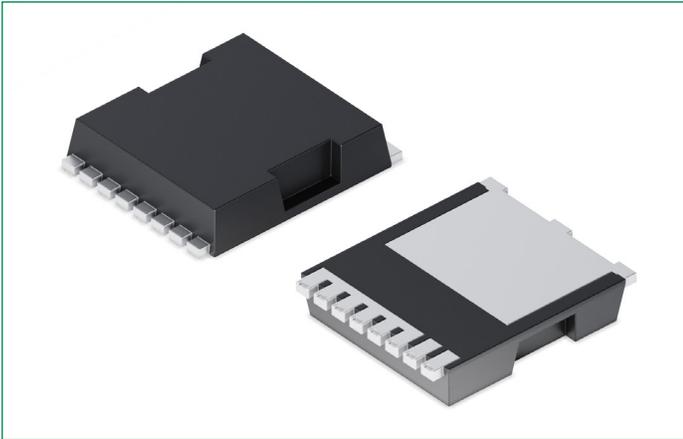
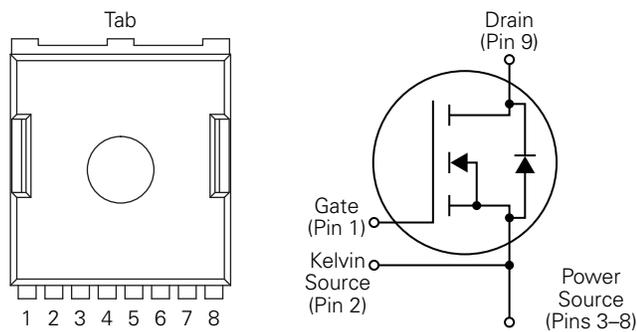


**IXSG110N65L2K**

650 V, 25 mΩ, 111 A SiC MOSFET

RoHS

HF

**Pinout Diagram (TOLL)****G:** Gate; **KS:** Kelvin Source; **S:** Power Source; **Tab:** Drain**Features**

- SiC MOSFET Technology with  $-3/+15 \dots 18$  V gate drive
- High blocking voltage with low on-state resistance
- High-speed switching with low capacitance
- High operating junction temperature capability
- Very fast intrinsic body diode
- Kelvin source contact
- MSL1 rated

**Applications**

- EV charging infrastructure
- Induction heating
- Motor drivers
- Solar inverters
- Switch mode power supplies
- Energy storage system

**Product Summary**

Characteristic	Value	Unit
$V_{DSS}$	650	V
$R_{DS(on)}$	25	mΩ
$I_D @ 25\text{ }^\circ\text{C}$	111	A

**Maximum Ratings** ( $T_c = 25\text{ }^\circ\text{C}$  unless otherwise specified)

Symbol	Characteristic	Conditions	Value	Unit
$V_{DSS}$	Drain-source voltage	$V_{GS} = 0\text{ V}, I_D = 100\text{ }\mu\text{A}$	650	V
$V_{GSM}$	Maximum gate-source voltage	–	–5 to +20	V
	Transient gate-source voltage	$t_{transient} = 200\text{ ns}, D < 1\%$	–10 to +23	
$I_D$	Drain current (continuous) Fig. 23	$V_{GS} = 18\text{ V}, T_c = 25\text{ }^\circ\text{C}$	111	A
		$V_{GS} = 18\text{ V}, T_c = 100\text{ }^\circ\text{C}$	83	A
$I_{DM}$	Peak drain current Fig. 26	Pulse width limited by SOA and dynamic $R_{th(j-c)}$	277	A
$P_{tot}$	Total power dissipation Fig. 24	$T_c = 25\text{ }^\circ\text{C}$	600	W
$T_{vj}$	Virtual junction temperature range	–	–55 to 175	$^\circ\text{C}$
$T_{stg}$	Storage temperature range	–	–55 to 175	$^\circ\text{C}$
$T_{sold}$	Soldering temperature	–	260	$^\circ\text{C}$

**Recommended Values**

Symbol	Characteristic	Value			Unit
		Min.	Typ.	Max.	
$V_{GSon}$	Recommended turn-on voltage	15	–	18	V
$V_{GSoff}$	Recommended turn-off voltage	–5	–3.5	–2	

**Thermal Characteristics**

Symbol	Characteristic	Value			Unit
		Min.	Typ.	Max.	
$R_{th(j-c)}$	Thermal resistance from junction to case Fig. 25	–	0.25	–	K/W

**Electrical Characteristics – Static** ( $T_c = 25\text{ }^\circ\text{C}$  unless otherwise specified)

Symbol	Characteristic	Conditions	Value			Unit
			Min.	Typ.	Max.	
$I_{DSS}$	Drain-source leakage current	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	–	3	100	$\mu\text{A}$
$I_{GSS}$	Gate leakage current	$V_{DS} = 0\text{ V}, V_{GS} = -5 \sim 20\text{ V}$	–	–	$\pm 100$	nA
$V_{GS(th)}$	Gate threshold voltage Fig. 8, 9	$V_{GS} = V_{DS}, I_D = 12\text{ mA}$	1.8	2.8	4.5	V
		$V_{GS} = V_{DS}, I_D = 12\text{ mA}, T_{vj} = 175\text{ }^\circ\text{C}$	–	2.0	–	
$R_{DS(on)}$	Drain-source on-state resistance Fig. 4, 5, 6, 7	$V_{GS} = 18\text{ V}, I_D = 40\text{ A} @ T_{vj} = 25\text{ }^\circ\text{C}$	–	25	33	m $\Omega$
		$V_{GS} = 18\text{ V}, I_D = 40\text{ A} @ T_{vj} = 175\text{ }^\circ\text{C}$	–	38	–	

### Electrical Characteristics – Dynamic ( $T_{vj} = 25\text{ °C}$ unless otherwise specified)

Symbol	Characteristic	Conditions	Value			Unit	
			Min.	Typ.	Max.		
$C_{iss}$	Input capacitance Fig. 16	$V_{DS} = 600\text{ V}, V_{GS} = 0\text{ V},$ $f = 1\text{ MHz}, V_{AC} = 25\text{ mV}$	–	3090	–	pF	
$C_{oss}$	Output capacitance Fig. 16		–	251	–		
$C_{rss}$	Reverse transfer capacitance Fig. 16		–	19	–		
$E_{oss}$	$C_{oss}$ stored energy Fig. 17		–	52	–		$\mu\text{J}$
$Q_G$	Total gate charge Fig. 18	$V_{DS} = 400\text{ V}, I_D = 40\text{ A},$ $V_{GS} = -3\text{ to }+18\text{ V}$	–	125	–	nC	
$Q_{GS}$	Gate-source charge Fig. 18		–	35.7	–		
$Q_{GD}$	Gate-drain charge Fig. 18		–	38.5	–		
$R_{g(int)}$	Gate input resistance	$f = 1\text{ MHz}$	–	1.5	–	$\Omega$	
$E_{on}$	Turn-on switching energy Fig. 19, 20, 22	$V_{DS} = 400\text{ V}, I_D = 40\text{ A},$ $V_{GS} = -3.5\text{ to }+18\text{ V},$ $R_{G(ext)} = 3.3\ \Omega, L = 200\ \mu\text{H}$	$T_{vj} = 25\text{ °C}$	–	271	–	$\mu\text{J}$
			$T_{vj} = 175\text{ °C}$	–	319	–	
$E_{off}$	Turn-off switching energy Fig. 19, 20, 22		$T_{vj} = 25\text{ °C}$	–	75	–	$\mu\text{J}$
			$T_{vj} = 175\text{ °C}$	–	86	–	
$t_{d(on)}$	Turn-on delay time Fig. 19, 20		$T_{vj} = 25\text{ °C}$	–	13.0	–	ns
$t_r$	Rise time Fig. 19, 20		$T_{vj} = 25\text{ °C}$	–	23.4	–	
$t_{d(off)}$	Turn-off delay time Fig. 19, 20		$T_{vj} = 25\text{ °C}$	–	35.1	–	
$t_f$	Fall time Fig. 19, 20		$T_{vj} = 25\text{ °C}$	–	11.5	–	

### Reverse Diode Characteristics ( $T_{vj} = 25\text{ °C}$ unless otherwise specified)

Symbol	Characteristic	Conditions	Value			Unit
			Min.	Typ.	Max.	
$V_{SD}$	Diode forward voltage Fig. 10, 11, 12	$I_{SD} = 20\text{ A}, V_{GS} = 0\text{ V}$	–	3.7	–	V
		$I_{SD} = 20\text{ A}, V_{GS} = 0\text{ V}, T_{vj} = 175\text{ °C}$	–	3.5	–	V
$t_{rr}$	Reverse recovery time	$V_{GS} = -3.5\text{ V}/+18\text{ V}, I_{SD} = 40\text{ A}, V_R = 400\text{ V},$ $R_{G(ext)} = 10\ \Omega, L = 200\ \mu\text{H}, di/dt = 3000\text{ A}/\mu\text{s}$	–	44	–	ns
$Q_{rr}$	Reverse recovery charge		–	187	–	nC
$I_{rrm}$	Peak reverse recovery current		–	19.2	–	A

Characteristic Curves

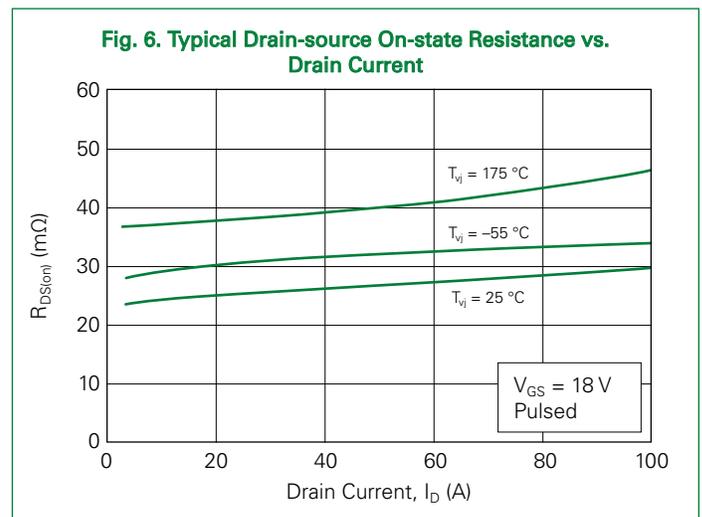
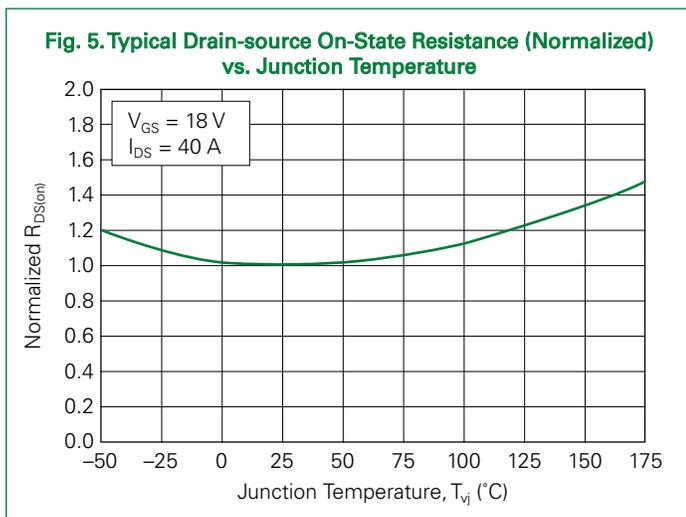
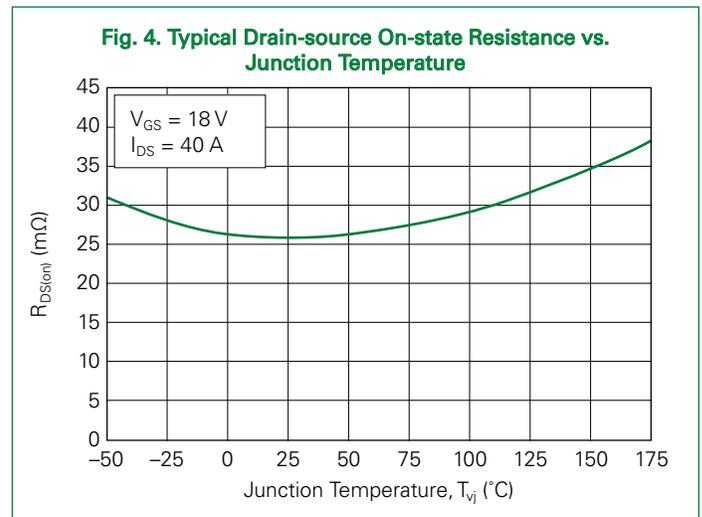
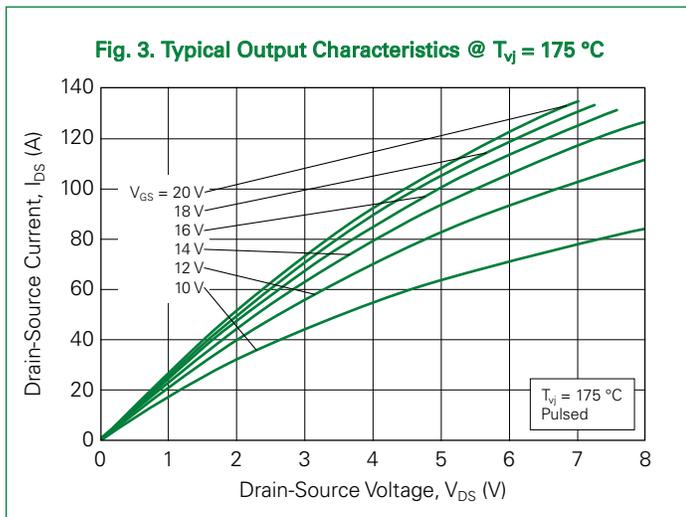
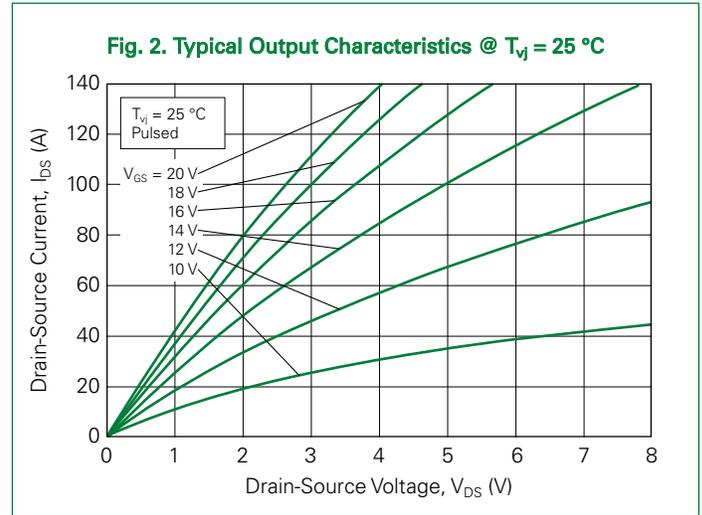
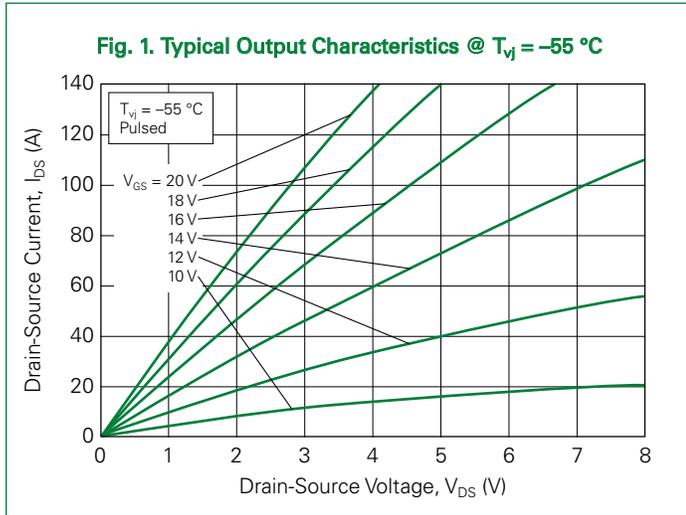


Fig. 7.  $R_{DS(on)}$  vs. Temperature

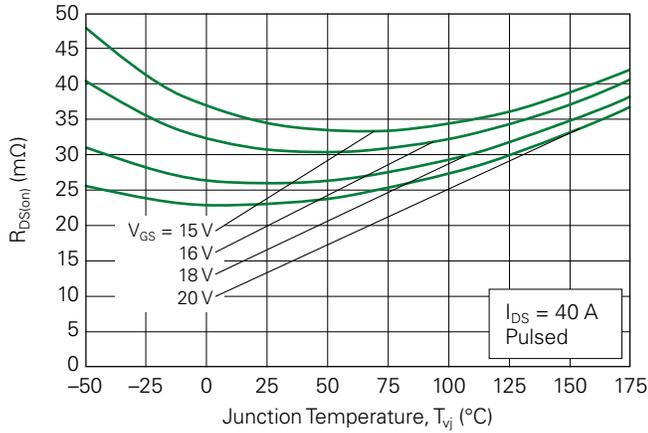


Fig. 8. Transfer Curves

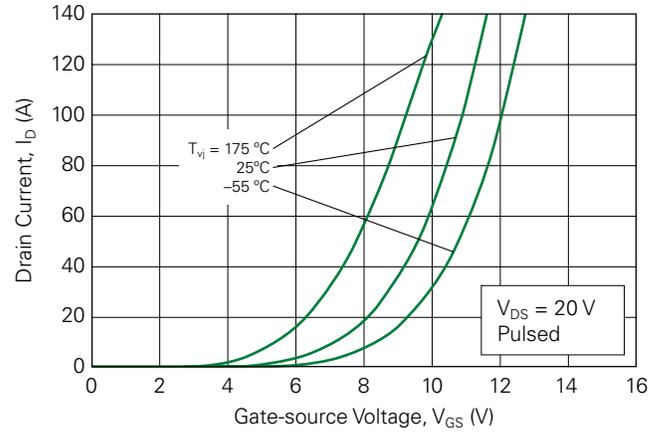


Fig. 9. Threshold Voltage vs. Temperature

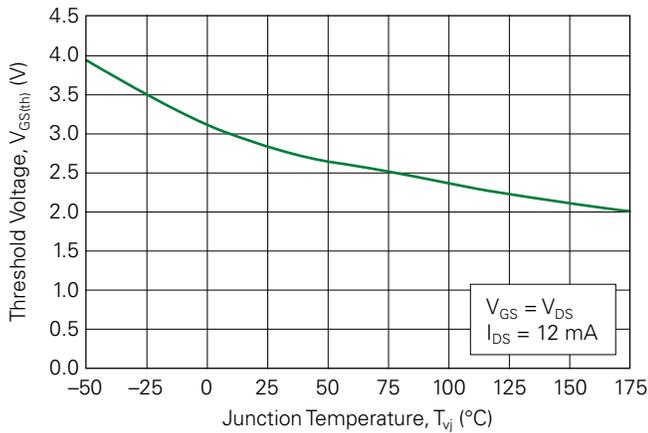


Fig. 10. Body Diode Curves @  $T_{vj} = -55$  °C

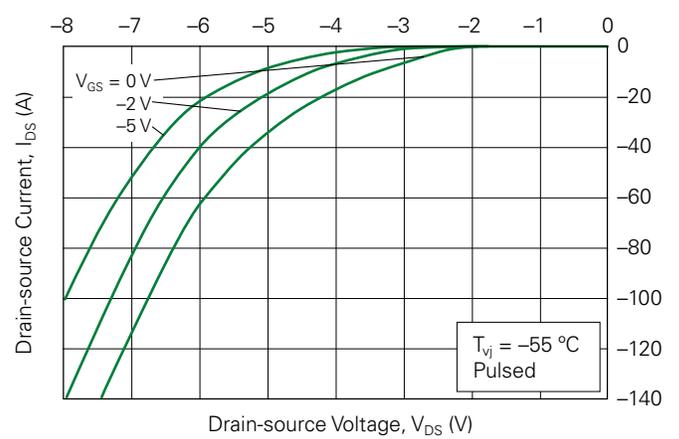


Fig. 11. Body Diode Curves @  $T_{vj} = 25$  °C

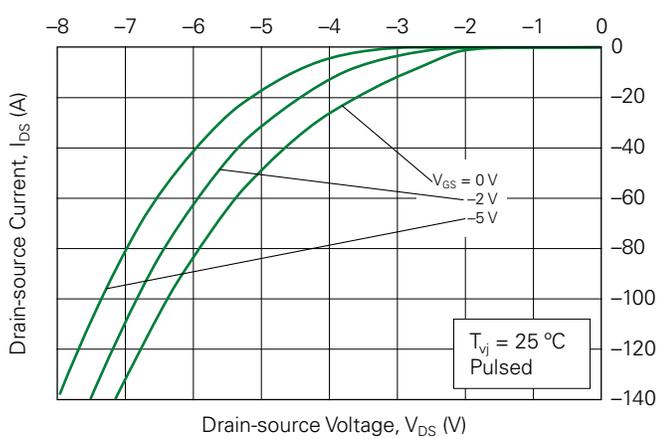


Fig. 12. Body Diode Curves @  $T_{vj} = 175$  °C

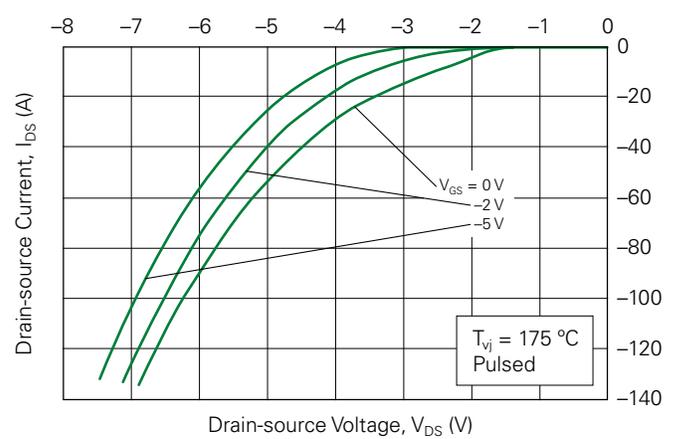


Fig. 13. 3<sup>rd</sup> Quadrant Curves @  $T_{vj} = -55\text{ }^\circ\text{C}$

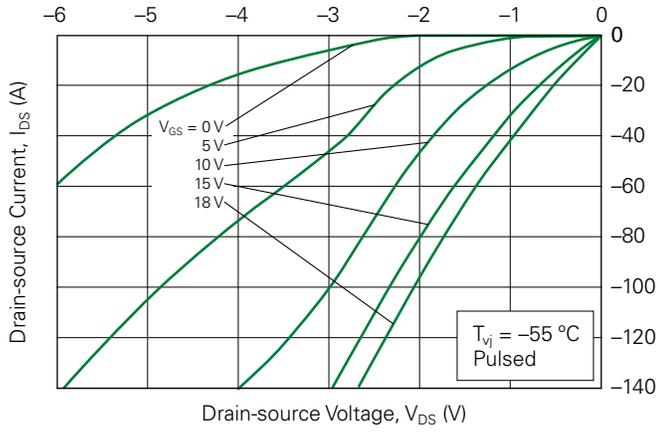


Fig. 14. 3<sup>rd</sup> Quadrant Curves @  $T_{vj} = 25\text{ }^\circ\text{C}$

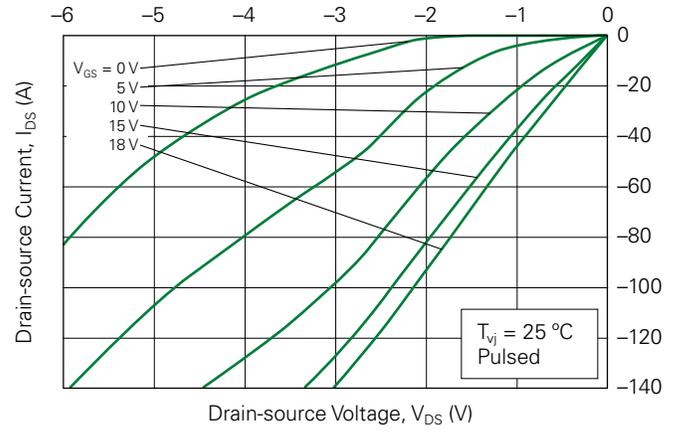


Fig. 15. 3<sup>rd</sup> Quadrant Curves @  $T_{vj} = 175\text{ }^\circ\text{C}$

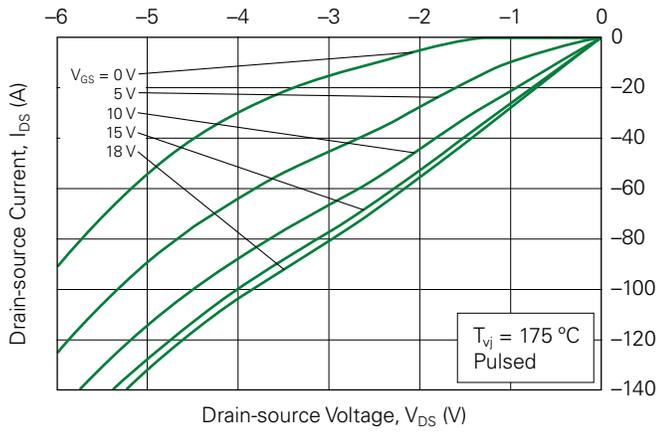


Fig. 16. Capacitance vs.  $V_{DS}$

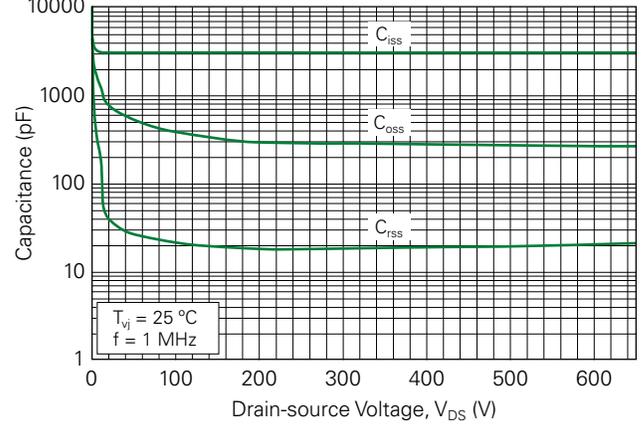


Fig. 17. Output Capacitor Stored Energy

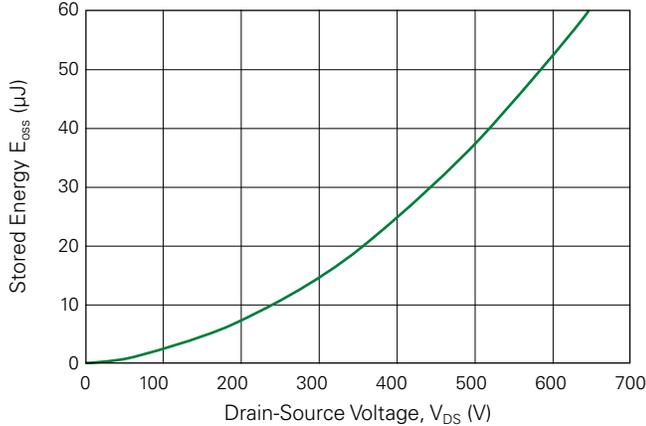


Fig. 18. Gate Charge Characteristics

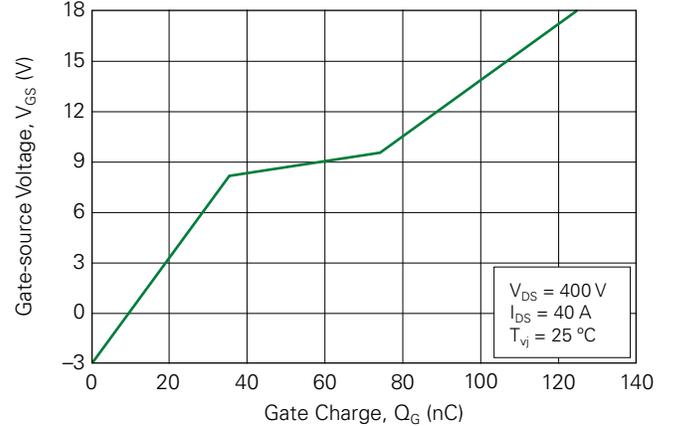


Fig. 19. Switching Energy vs.  $R_{G(ext)}$

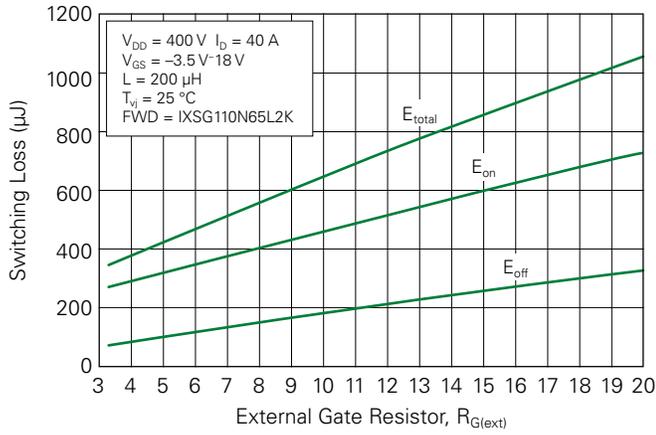


Fig. 20. Switching Times vs.  $R_{G(ext)}$

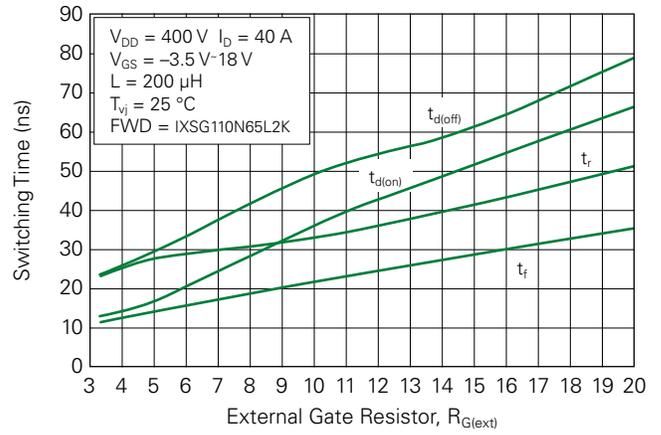


Fig. 21. Switching Energy vs.  $I_D$

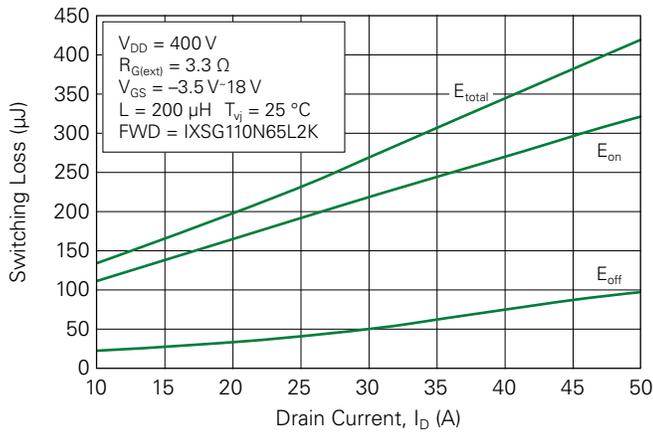


Fig. 22. Switching Energy vs. Temperature

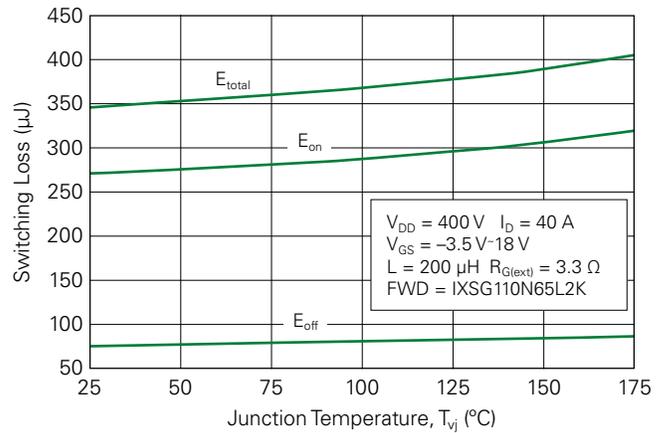


Fig. 23. Continuous Drain Current vs. Case Temperature

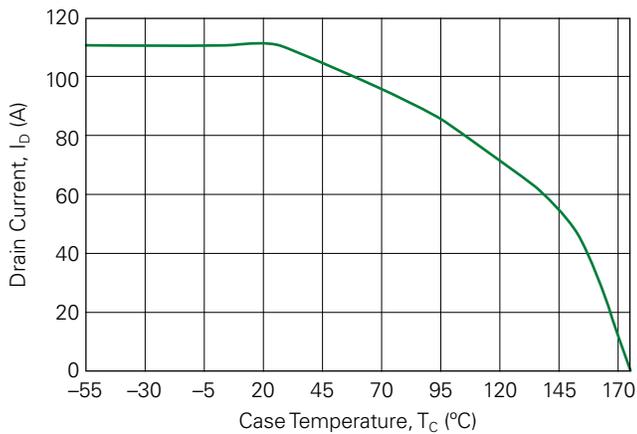


Fig. 24. Max. Power Dissipation Derating vs. Case Temperature

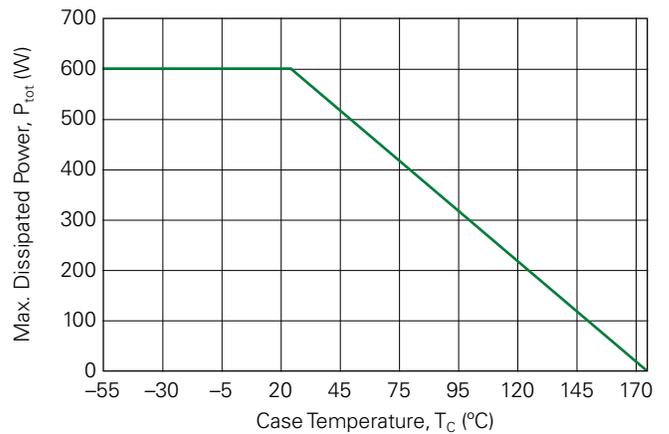


Fig. 25. Thermal Impedance

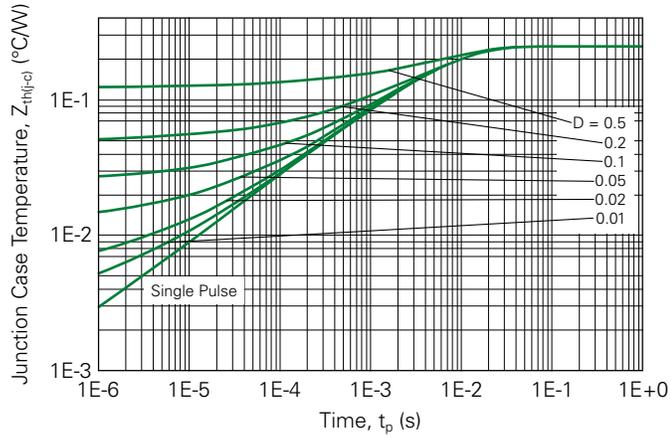
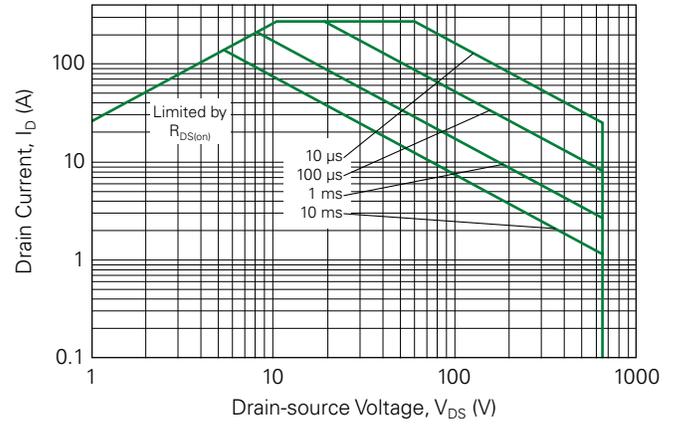
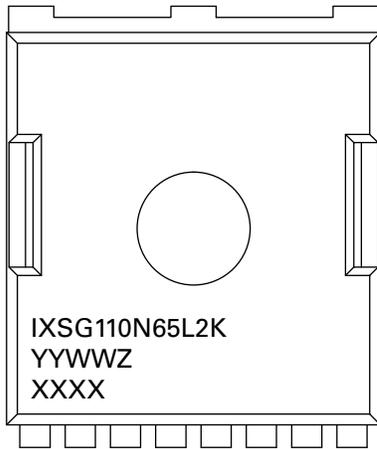


Fig. 26. Safe Operating Area



Part Number and Marking



IXSG110N65L2K = Specific Device Code

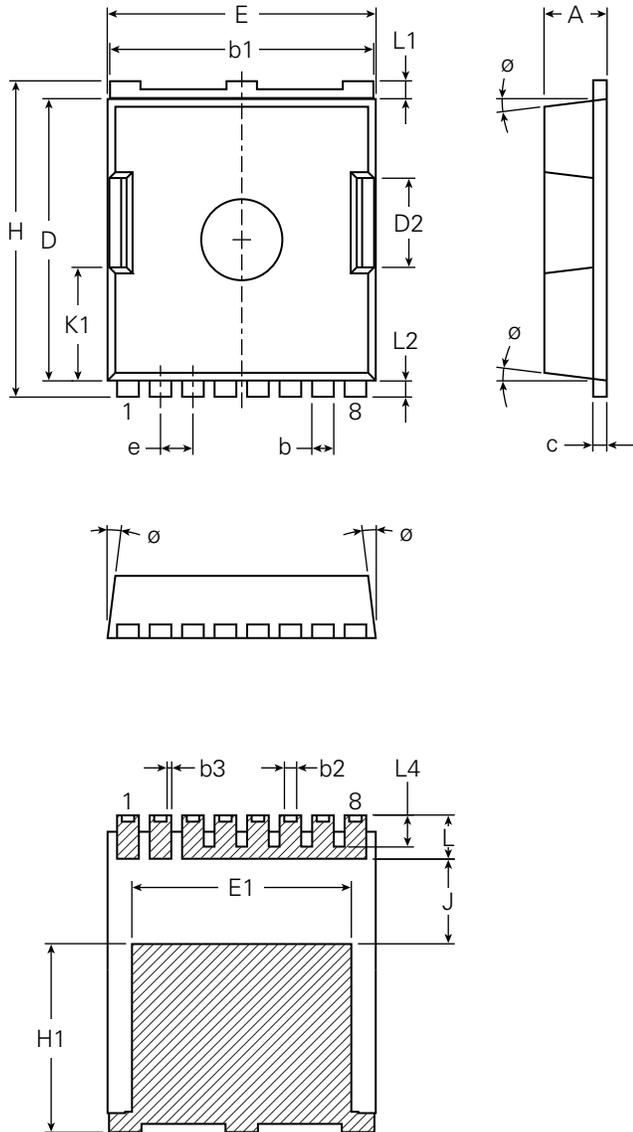
YY = Year

WW = Work Week

Z = Assembly Location

XXXX = Lot Traceability

Part Outline Drawing (TOLL)



Symbol	Inches			Millimeters		
	Min.	Typical	Max.	Min.	Typical	Max
A	0.087	-	0.094	2.20	-	2.40
b	0.028	-	0.035	0.70	-	0.90
b1	0.382	-	0.390	9.70	-	9.90
b2	0.017	-	0.020	0.42	-	0.50
b3	0.003	0.007	0.011	0.07	0.17	0.27
c	0.016	-	0.024	0.40	-	0.60
D	0.405	-	0.417	10.28	-	10.58
D2	0.122	0.130	0.138	3.10	3.30	3.50
E	0.382	0.390	0.398	9.70	9.90	10.10
E1	0.311	0.319	0.327	7.90	8.10	8.30
e	0.047 BSC			1.20 BSC		
H	0.452	0.460	0.468	11.48	11.68	11.88
H1	0.266	0.274	0.281	6.75	6.95	7.15
J	0.118	0.124	0.130	3.00	3.15	3.30
K1	0.157	0.165	0.172	3.98	4.18	4.38
L	0.055	0.063	0.071	1.40	1.60	1.80
L1	0.024	0.028	0.031	0.60	0.70	0.80
L2	0.020	0.024	0.028	0.50	0.60	0.70
L4	0.039	0.045	0.051	1.00	1.15	1.30
ø	4°	7°	10°	4°	7°	10°

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Part of:

