



Phase Control Thyristor

Types N7585FE240 and N7585FE280

Absolute Maximum Ratings

| | VOLTAGE RATINGS | MAXIMUM LIMITS | UNITS |
|-----------|---|----------------|-------|
| V_{DRM} | Repetitive peak off-state voltage, (note 1) | 2400-2800 | V |
| V_{DSM} | Non-repetitive peak off-state voltage, (note 1) | 2400-2800 | V |
| V_{RRM} | Repetitive peak reverse voltage, (note 1) | 2400-2800 | V |
| V_{RSM} | Non-repetitive peak reverse voltage, (note 1) | 2500-2900 | V |

| | OTHER RATINGS | MAXIMUM LIMITS | UNITS |
|----------------|---|---|--------------------------------|
| $I_{T(AV)M}$ | Maximum average on-state current, $T_{sink}=55^{\circ}C$, (note 2) | 7535 | A |
| $I_{T(AV)M}$ | Maximum average on-state current, $T_{sink}=85^{\circ}C$, (note 2) | 5255 | A |
| $I_{T(AV)M}$ | Maximum average on-state current, $T_{sink}=85^{\circ}C$, (note 3) | 2990 | A |
| $I_{T(RMS)M}$ | Nominal RMS on-state current, $T_{sink}=25^{\circ}C$, (note 2) | 14750 | A |
| $I_{T(D.C.)}$ | D.C. on-state current, $T_{sink}=25^{\circ}C$, (note 4) | 13090 | A |
| I_{TSM} | Peak non-repetitive surge $t_p=10ms$, $V_{rm}=60\%V_{RRM}$, (note 5) | 110 | kA |
| I_{TSM2} | Peak non-repetitive surge $t_p=10ms$, $V_{rm}\leq 10V$, (note 5) | 121 | kA |
| I^2t | I^2t capacity for fusing $t_p=10ms$, $V_{rm}=60\%V_{RRM}$, (note 5) | 60.5×10^6 | A^2s |
| I^2t | I^2t capacity for fusing $t_p=10ms$, $V_{rm}\leq 10V$, (note 5) | 73.2×10^6 | A^2s |
| $(di/dt)_{cr}$ | Critical rate of rise of on-state current (note 6) | (continuous, 50Hz) (repetitive, 50Hz, 60s) (non-repetitive) | 75 150 300 A/ μs |
| V_{RGM} | Peak reverse gate voltage | 5 | V |
| $P_{G(AV)}$ | Mean forward gate power | 5 | W |
| P_{GM} | Peak forward gate power | 30 | W |
| $T_{j op}$ | Operating temperature range | -40 to +125 | $^{\circ}C$ |
| T_{stg} | Storage temperature range | -40 to +150 | $^{\circ}C$ |

Notes:-

- De-rating factor of 0.13% per $^{\circ}C$ is applicable for T_j below $25^{\circ}C$.
- Double side cooled, single phase; 50Hz, 180° half-sinewave.
- Cathode side cooled, single phase; 50Hz, 180° half-sinewave.
- Double side cooled.
- Half-sinewave, $125^{\circ}C$ T_j initial.
- $V_D=67\% V_{DRM}$, $I_{TM}=4000A$, $I_{FG}=2A$, $t_r\leq 0.5\mu s$, $T_{case}=125^{\circ}C$.

Characteristics

| | PARAMETER | MIN. | TYP. | MAX. | TEST CONDITIONS (Note 1) | UNITS |
|-----------------------|--|------|------|--------|---|-------|
| V _{TM} | Maximum peak on-state voltage | - | - | 1.15 | I _{TM} =6000A | V |
| V _{TM} | Maximum peak on-state voltage | - | - | 1.25 | I _{TM} =8000A | V |
| V _{T0} | Threshold voltage | - | - | 0.780 | | V |
| r _T | Slope resistance | - | - | 0.062 | | mΩ |
| (dv/dt) _{cr} | Critical rate of rise of off-state voltage | 1000 | - | - | V _D =80% V _{DRM} , linear ramp, gate o/c | V/μs |
| I _{DRM} | Peak off-state current | - | - | 200 | Rated V _{DRM} | mA |
| I _{RRM} | Peak reverse current | - | - | 200 | Rated V _{RRM} | mA |
| V _{GT} | Gate trigger voltage | - | - | 3.0 | T _J =25°C V _D =10V, I _T =3A | V |
| I _{GT} | Gate trigger current | - | - | 300 | | mA |
| V _{GD} | Gate non-trigger voltage | - | - | 0.25 | | V |
| I _H | Holding current | - | - | 1000 | T _J =25°C | mA |
| t _{gd} | Gate-controlled turn-on delay time | - | 1.0 | 2.0 | V _D =67% V _{DRM} , I _T =3000A, di/dt=10A/μs, I _{FG} =2A, t _r =0.5μs, T _J =25°C | μs |
| t _{gt} | Turn-on time | - | 1.5 | 3.0 | | μs |
| Q _{rr} | Recovered charge | - | 12.6 | 14 | I _{TM} =4000A, t _p =2000μs, di/dt=10A/μs, V _r =50V | mC |
| Q _{ra} | Recovered charge, 50% Chord | - | 8.4 | - | | mC |
| I _{rr} | Reverse recovery current | - | 305 | - | | A |
| t _{rr} | Reverse recovery time | - | 55 | - | | μs |
| t _q | Turn-off time | - | 550 | - | I _{TM} =4000A, t _p =2000μs, di/dt=10A/μs, V _r =50V, V _{dr} =67%V _{DRM} , dV _{dr} /dt=20V/μs | μs |
| | | - | 950 | - | I _{TM} =4000A, t _p =2000μs, di/dt=10A/μs, V _r =50V, V _{dr} =67%V _{DRM} , dV _{dr} /dt=200V/μs | |
| R _{thJK} | Thermal resistance, junction to heatsink | - | - | 0.0048 | Double side cooled | K/W |
| | | - | - | 0.0086 | Anode side cooled | K/W |
| | | - | - | 0.0108 | Cathode side cooled | K/W |
| F | Mounting force | 80 | - | 100 | Note 2. | kN |
| W _t | Weight | - | 2.9 | - | | kg |

Notes:-

- 1) Unless otherwise indicated T_J=125°C.
- 2) For other clamp forces, please consult factory.

Notes on Ratings and Characteristics

1.0 Voltage Grade Table

| Voltage Grade | V_{DRM} V_{DSM} V_{RRM} V | V_{RSM} V | V_D V_R DC V |
|---------------|------------------------------------|----------------|---------------------|
| 24 | 2400 | 2500 | 1440 |
| 28 | 2800 | 2900 | 1680 |

2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_j below 25°C.

4.0 Repetitive dv/dt

Standard dv/dt is 1000V/μs.

5.0 Snubber Components

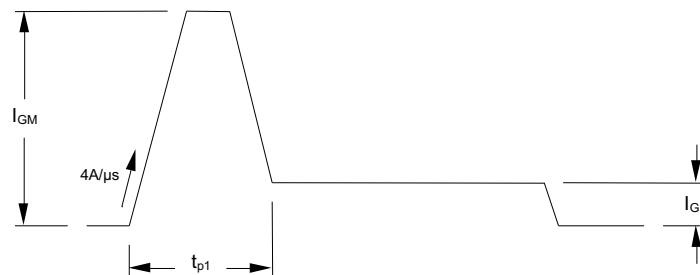
When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

6.0 Rate of rise of on-state current

The maximum un-primed rate of rise of on-state current must not exceed 300A/μs at any time during turn-on on a non-repetitive basis. For repetitive performance, the on-state rate of rise of current must not exceed 150A/μs at any time during turn-on. Note that these values of rate of rise of current apply to the total device current including that from any local snubber network.

7.0 Gate Drive

The nominal requirement for a typical gate drive is illustrated below. An open circuit voltage of at least 30V is assumed. This gate drive must be applied when using the full di/dt capability of the device.



The magnitude of I_{GM} should be between five and ten times I_{GT} , which is shown on page 2. Its duration (t_{p1}) should be 20μs or sufficient to allow the anode current to reach ten times I_L , whichever is greater. Otherwise, an increase in pulse current could be needed to supply the necessary charge to trigger. The 'back-porch' current I_G should remain flowing for the same duration as the anode current and have a magnitude in the order of 1.5 times I_{GT} .

8.0 Computer Modelling Parameters

8.1 Device Dissipation Calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T} \quad \text{and:} \quad W_{AV} = \frac{\Delta T}{R_{th}}$$

$$\Delta T = T_{j \max} - T_K$$

Where $V_{T0}=0.78V$, $r_T=0.062m\Omega$,

R_{th} = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

| Supplementary Thermal Impedance | | | | | | | |
|---------------------------------|--------|--------|--------|--------|--------|--------|--------|
| Conduction Angle | 30° | 60° | 90° | 120° | 180° | 270° | d.c. |
| Square wave Double Side Cooled | 0.0056 | 0.0054 | 0.0053 | 0.0052 | 0.0050 | 0.0049 | 0.0048 |
| Square wave Anode Side Cooled | 0.0093 | 0.0092 | 0.0091 | 0.0090 | 0.0089 | 0.0087 | 0.0086 |
| Square wave Cathode Side Cooled | 0.0116 | 0.0114 | 0.0113 | 0.0112 | 0.0111 | 0.0109 | 0.0108 |
| Sine wave Double Side Cooled | 0.0054 | 0.0052 | 0.0051 | 0.0050 | 0.0049 | | |
| Sine wave Anode Side Cooled | 0.0092 | 0.0091 | 0.0090 | 0.0089 | 0.0087 | | |
| Sine wave Cathode Side Cooled | 0.0114 | 0.0113 | 0.0112 | 0.0111 | 0.0109 | | |

| Form Factors | | | | | | | |
|------------------|------|------|------|------|------|------|------|
| Conduction Angle | 30° | 60° | 90° | 120° | 180° | 270° | d.c. |
| Square wave | 3.46 | 2.45 | 2 | 1.73 | 1.41 | 1.15 | 1 |
| Sine wave | 3.98 | 2.78 | 2.22 | 1.88 | 1.57 | | |

8.2 Calculating V_T using ABCD Coefficients

The on-state characteristic I_T vs. V_T , on page 6 is represented in two ways;

- the well established V_{T0} and r_T tangent used for rating purposes and
- a set of constants A, B, C, D, forming the coefficients of the representative equation for V_T in terms of I_T given below:

$$V_T = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V_T agree with the true device characteristic over a current range, which is limited to that plotted.

| 25°C Coefficients | | 125°C Coefficients | |
|-------------------|----------------------------|--------------------|---------------------------|
| A | 0.9379402 | A | 0.7428896 |
| B | -6.822715×10^{-3} | B | -0.02430125 |
| C | 2.04909×10^{-5} | C | 9.54751×10^{-6} |
| D | 2.566803×10^{-3} | D | 7.254107×10^{-3} |

8.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}} \right)$$

Where $p = 1$ to n , n is the number of terms in the series and:

t = Duration of heating pulse in seconds.

r_t = Thermal resistance at time t .

r_p = Amplitude of p_{th} term.

τ_p = Time Constant of r_{th} term.

The coefficients for this device are shown in the tables below:

| D.C. Double Side Cooled | | | | |
|-------------------------|---------------------------|---------------------------|---------------------------|---------------------------|
| Term | 1 | 2 | 3 | 4 |
| r_p | 2.598885×10^{-3} | 1.739334×10^{-3} | 2.818348×10^{-4} | 1.411440×10^{-4} |
| τ_p | 1.677142 | 0.1686846 | 0.01827976 | 3.390535×10^{-3} |

| D.C. Anode Side Cooled | | | | |
|------------------------|---------------------------|---------------------------|---------------------------|---------------------------|
| Term | 1 | 2 | 3 | 4 |
| r_p | 5.606120×10^{-3} | 2.068096×10^{-3} | 6.757121×10^{-4} | 2.516057×10^{-4} |
| τ_p | 7.879130 | 0.3731131 | 0.06743285 | 6.052540×10^{-3} |

| D.C. Cathode Side Cooled | | | | |
|--------------------------|---------------------------|---------------------------|---------------------------|---------------------------|
| Term | 1 | 2 | 3 | 4 |
| r_p | 8.204382×10^{-3} | 1.589943×10^{-3} | 8.331906×10^{-4} | 2.029845×10^{-4} |
| τ_p | 9.180430 | 0.3102548 | 0.07884356 | 4.323303×10^{-3} |

9.0 Reverse recovery ratings

(i) Q_{rr} is based on 50% I_{RM} chord as shown in Fig. 1

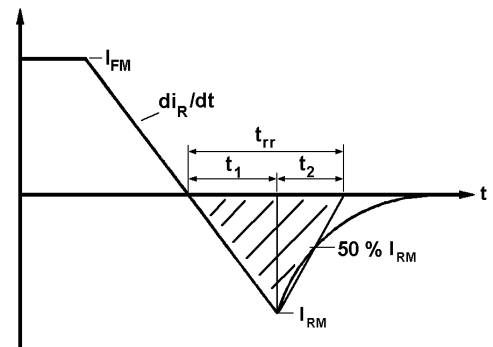


Fig. 1

(ii) Q_{rr} is based on a $150\mu s$ integration time i.e.

$$Q_{rr} = \int_0^{150\mu s} i_{rr} \cdot dt$$

(iii)

$$K \text{ Factor} = \frac{t_1}{t_2}$$

Curves

Figure 1 – On-state characteristics of Limit device

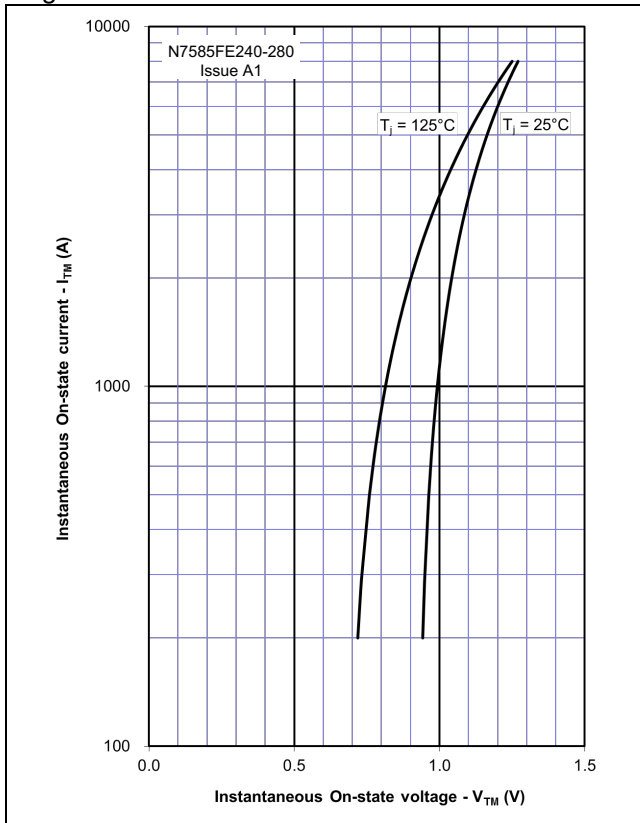


Figure 2 – Transient thermal impedance

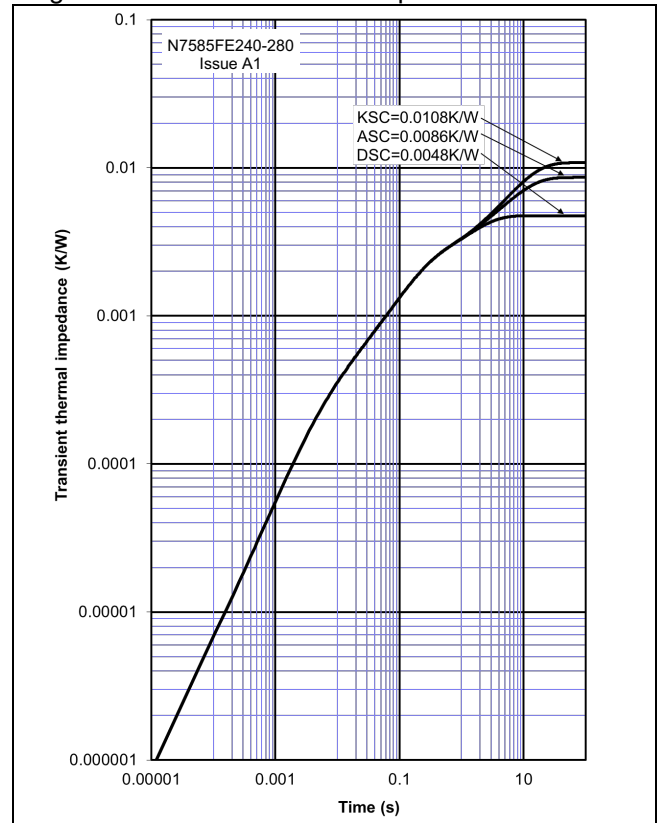


Figure 3 – Gate Characteristics – Trigger limits

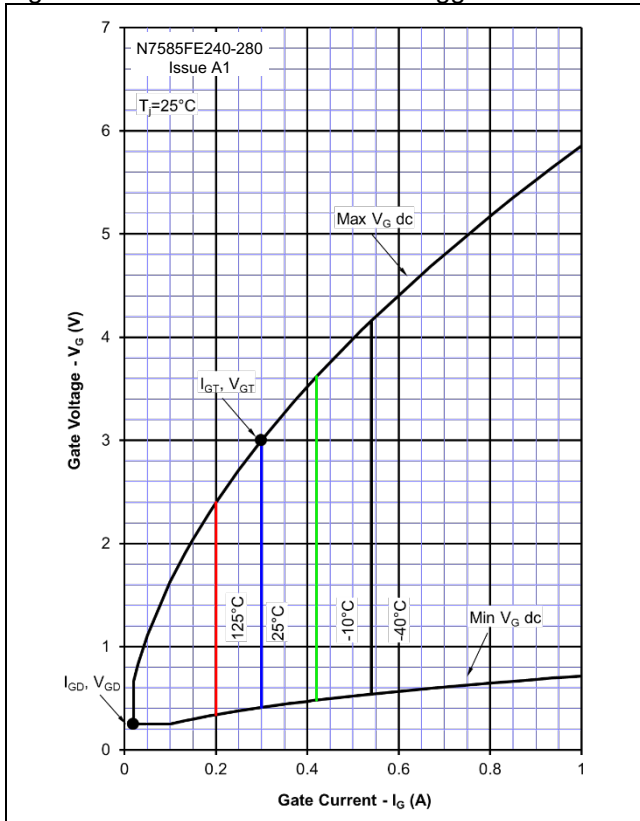


Figure 4 – Gate Characteristics – Power Curves

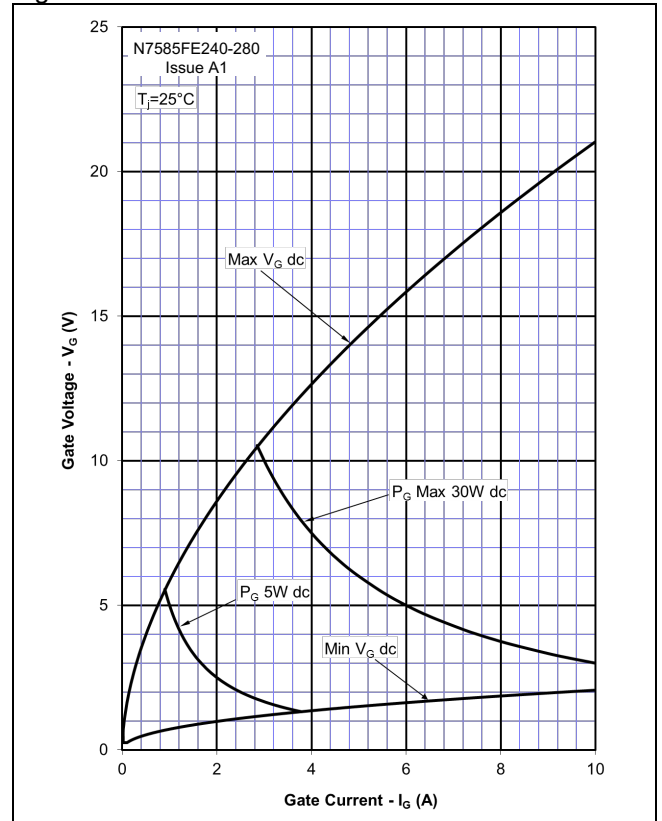


Figure 5 – Total Recovered Charge, Q_{rr}

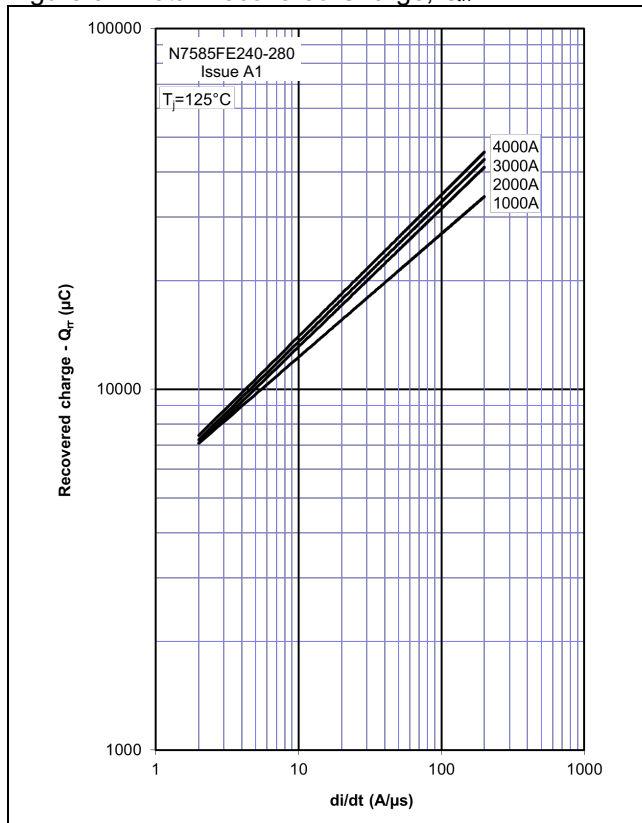


Figure 6 – Recovered Charge, Q_{ra} (50% chord)

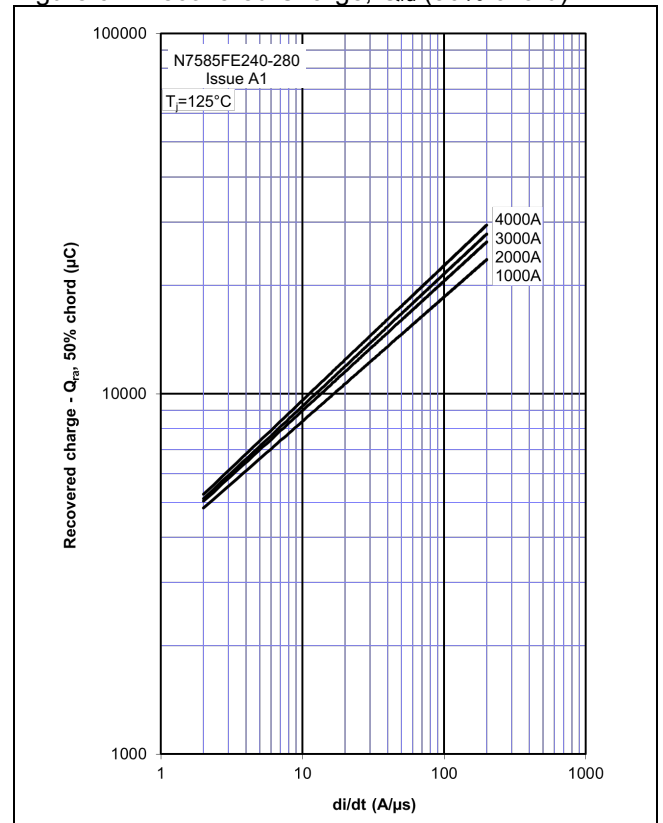


Figure 7 – Peak Reverse Recovery Current, I_{rm}

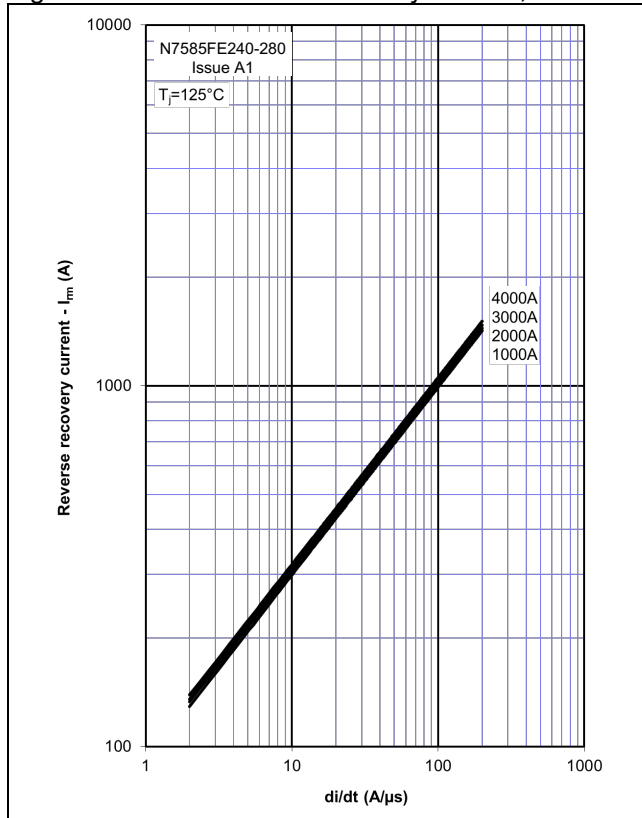


Figure 8 – Maximum Recovery Time, t_{rr} (50% chord)

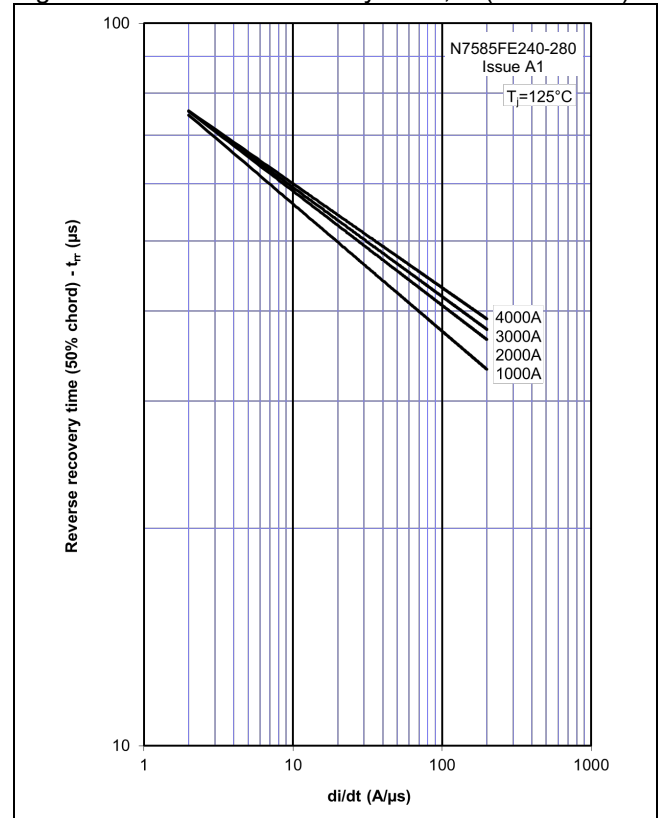


Figure 9 – On-state current vs. Power dissipation – Double Side Cooled (Sine wave)

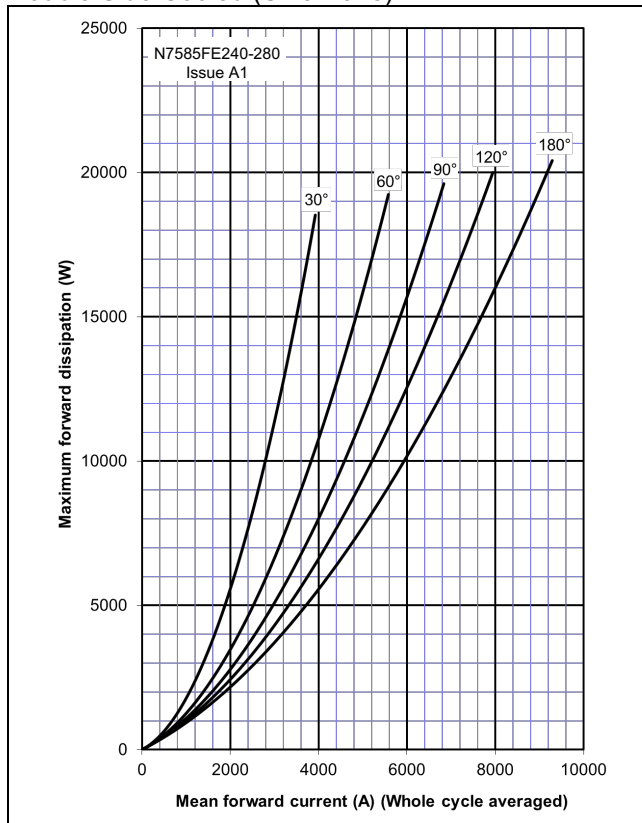


Figure 10 – On-state current vs. Heatsink temperature – Double Side Cooled (Sine wave)

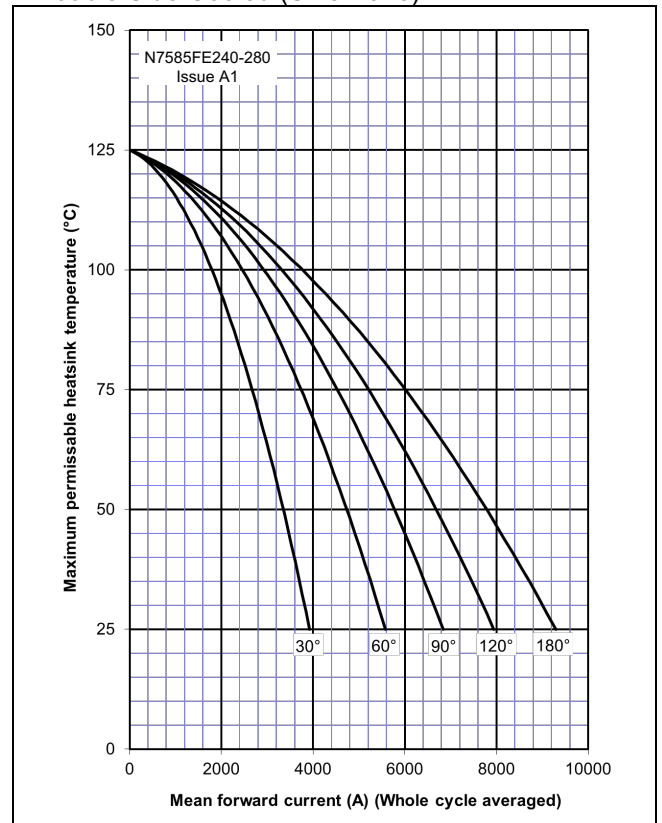


Figure 11 – On-state current vs. Power dissipation – Double Side Cooled (Square wave)

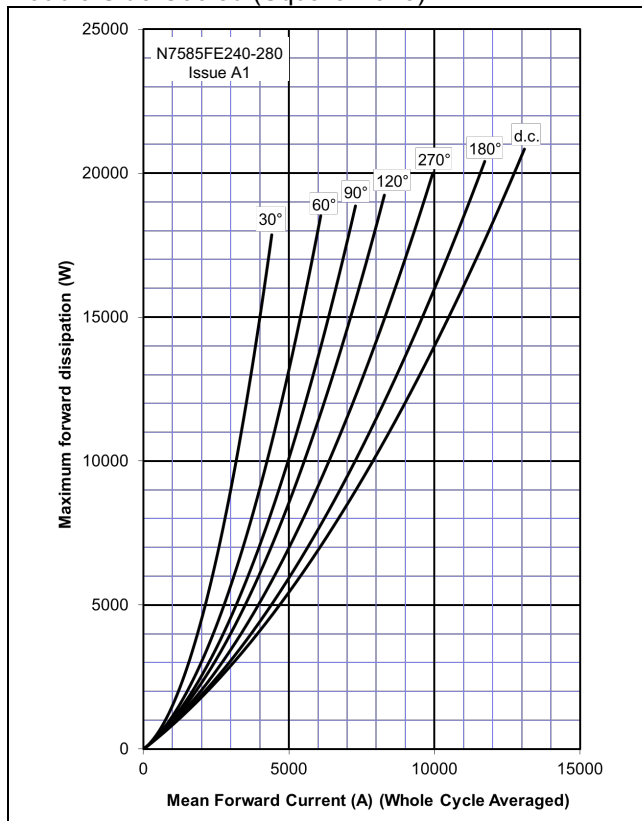


Figure 12 – On-state current vs. Heatsink temperature – Double Side Cooled (Square wave)

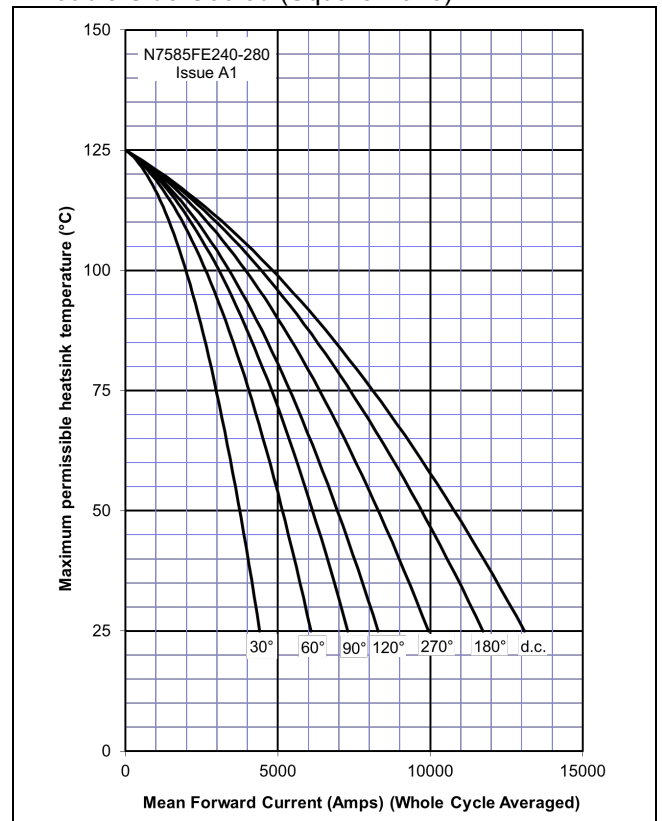


Figure 13 – On-state current vs. Power dissipation – Cathode Side Cooled (Sine wave)

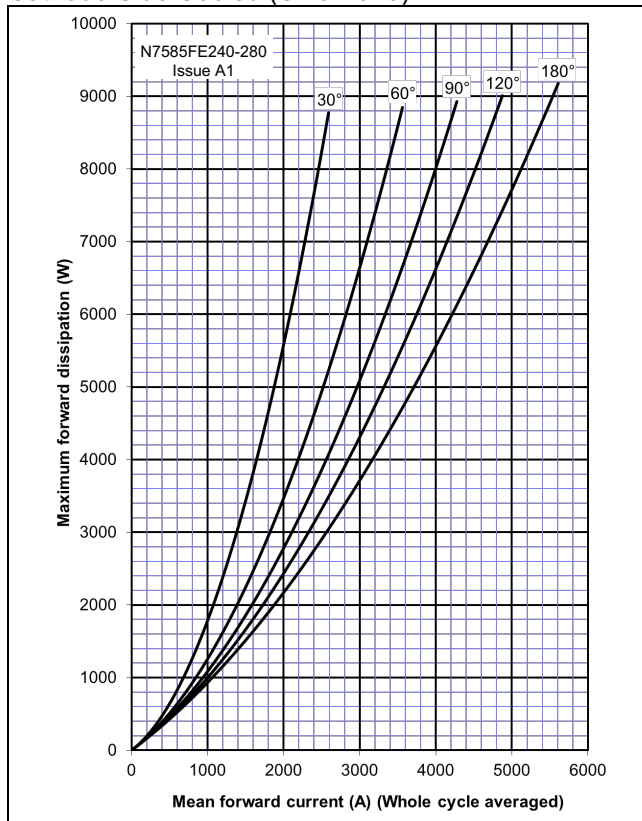


Figure 14 – On-state current vs. Heatsink temperature – Cathode Side Cooled (Sine wave)

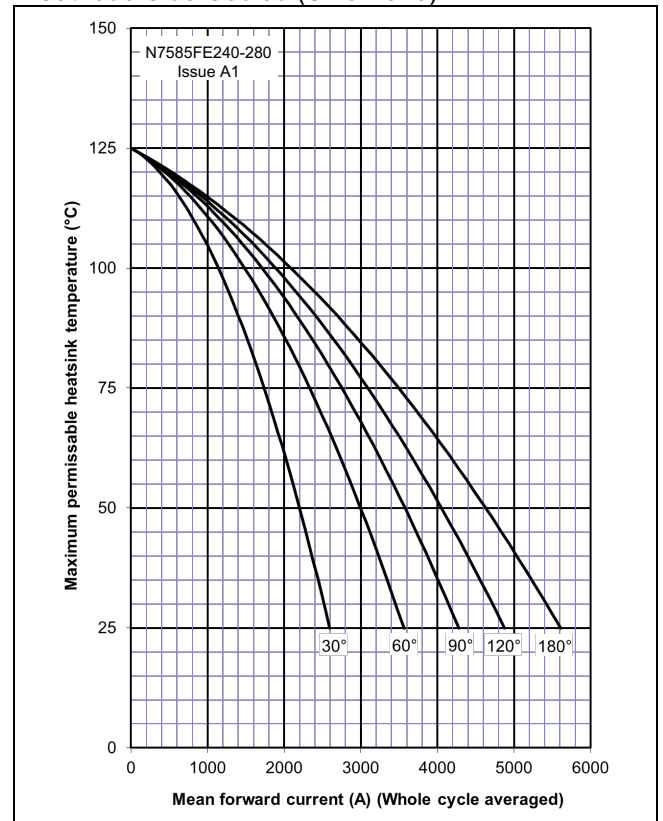


Figure 15 – On-state current vs. Power dissipation – Cathode Side Cooled (Square wave)

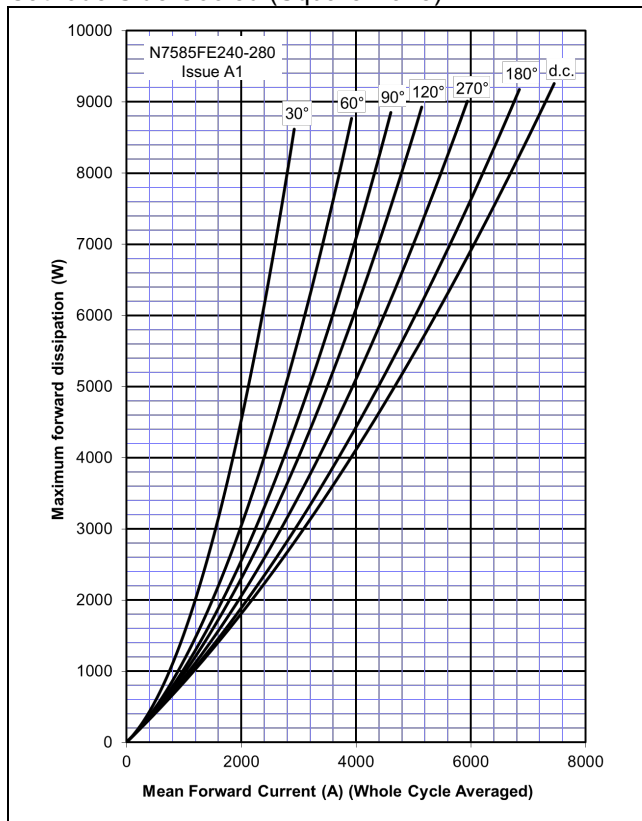


Figure 16 – On-state current vs. Heatsink temperature – Cathode Side Cooled (Square wave)

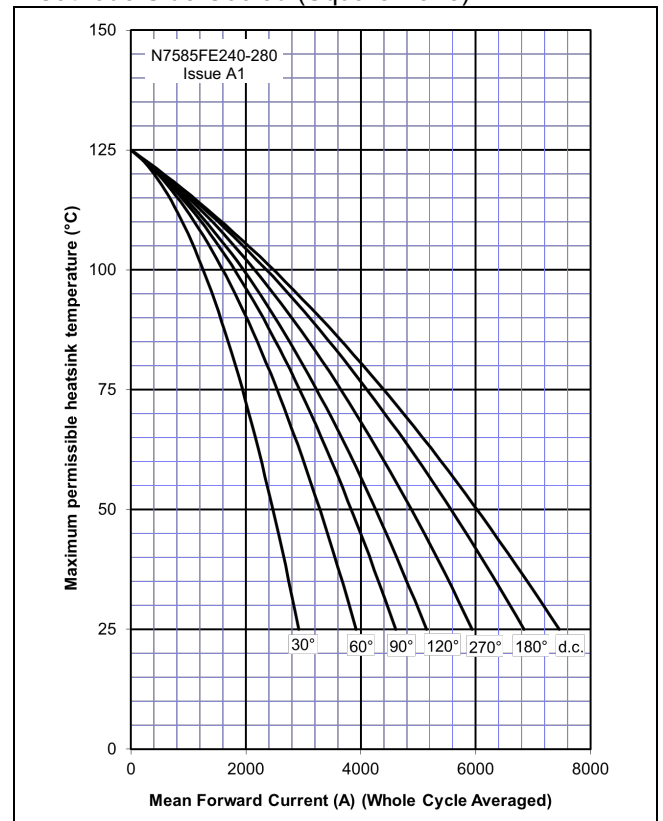
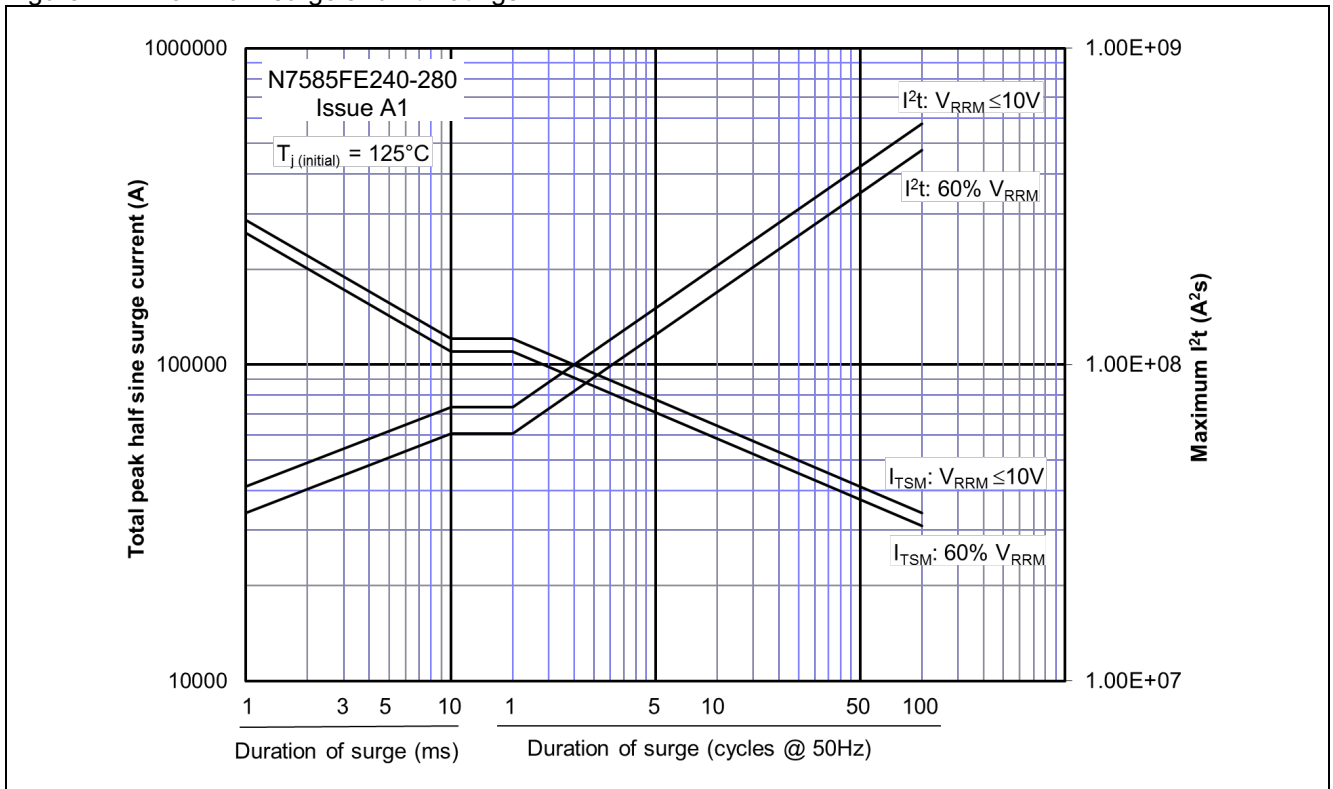
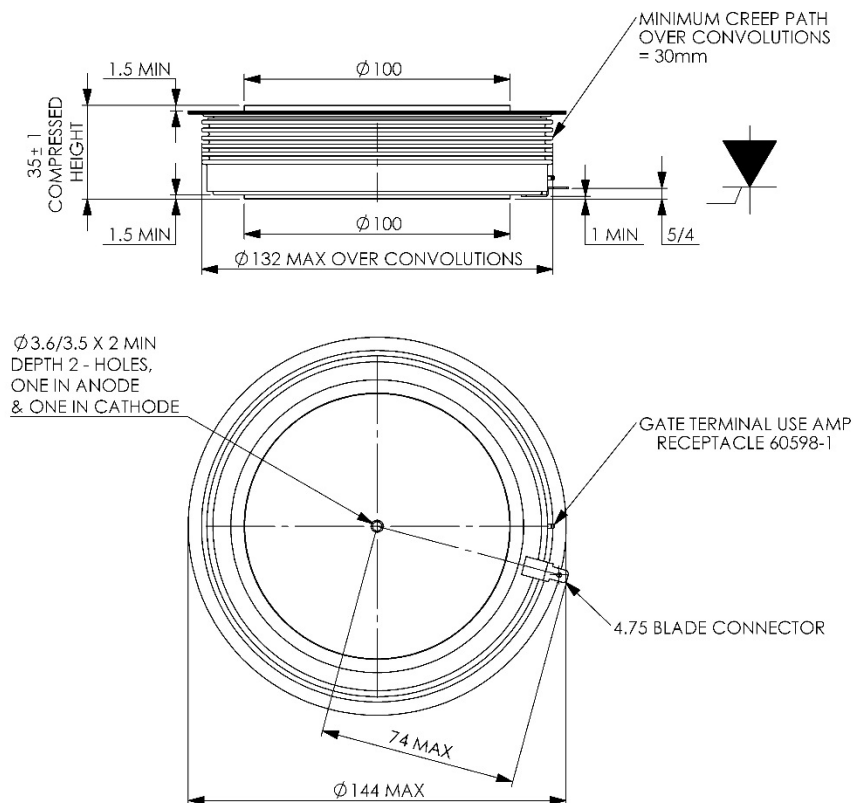


Figure 17 – Maximum surge and I^2t Ratings



Outline Drawing & Ordering Information



101A421

ORDERING INFORMATION

(Please quote 10 digit code as below)

| N7585 | FE | ◆◆ | 0 |
|--------------------|----------------------------|--|-----------------------------|
| Fixed Type Code | Fixed 35mm clamp height | Voltage code $V_{DRM}/100$ 24 and 28 | Fixed turn-off time code |

 Order code: N7585FE280 – 2800V V_{DRM} , V_{RRM} , 35mm clamp height capsule.

IXYS Semiconductor GmbH

 Edisonstraße 15
 D-68623 Lampertheim
 Tel: +49 6206 503-0
 Fax: +49 6206 503-627
 E-mail: marcom@ixys.de

IXYS UK Westcode Ltd
 Langley Park Way, Langley Park,
 Chippenham, Wiltshire, SN15 1GE.
 Tel: +44 (0)1249 444524
 E-mail: sales@ixysuk.com

IXYS Corporation

 3540 Bassett Street
 Santa Clara CA 95054 USA
 Tel: +1 (408) 982 0700
 Fax: +1 (408) 496 0670
 E-mail: sales@ixys.net
www.littelfuse.com
www.ixysuk.com
www.ixys.com
IXYS Long Beach Inc
 2500 Mira Mar Ave,
 Long Beach CA 90815
 Tel: +1 (562) 296 6584
 Fax: +1 (562) 296 6585
 E-mail: service@ixyslongbeach.com

The information contained herein is confidential and is protected by Copyright. The information may not be used or disclosed except with the written permission of and in the manner permitted by the proprietors IXYS UK Westcode Ltd.

© IXYS UK Westcode Ltd.

In the interest of product improvement, IXYS UK Westcode reserves the right to change specifications at any time without prior notice.

Devices with a suffix code (2-letter, 3-letter or letter/digit/letter combination) added to their generic code are not necessarily subject to the conditions and limits contained in this report.



Disclaimer Notice - Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littelfuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at www.littelfuse.com/disclaimer-electronics.