Not For New Designs

IXYS
INTEGRATED CIRCUITS DIVISION



CPC1333
Single-Pole Normally Closed 350V, 130mA OptoMOS® Relay

Parameters	Ratings	Units
Peak Blocking Voltage	350	V_P
Load Current	130	mA_{rms} / mA_{DC}
On-Resistance (max)	30	Ω
Isolation Voltage, Input to Output	5000	V_{rms}

Features
- 5000V_{rms} Input/Output Isolation
- 350V_P Blocking Voltage
- Low Drive Power Requirements
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Small 4-Pin Package

Applications
- Telephony Switching
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description
The CPC1333 is a single-pole, normally closed (1-Form-B) Solid State Relay with an enhanced input to output isolation barrier of 5000V_{rms}.

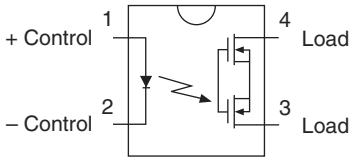
The relay output is constructed with efficient MOSFET switches that use IXYS Integrated Circuits' patented OptoMOS architecture. The input, a highly efficient infrared LED, controls the optically coupled output.

Approvals
- UL Recognized Component: File E76270
- IEC EN 62368-1: TUV Certificate B 082667 0008

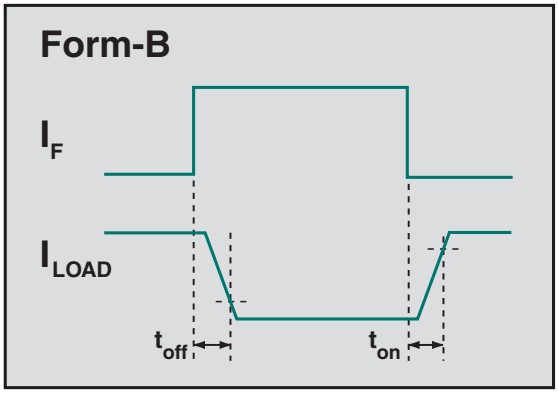
Ordering Information

Part Number	Description
CPC1333G	4-Pin DIP (100/Tube)
CPC1333GR	4-Pin Surface Mount (100/Tube)
CPC1333GRTR	4-Pin Surface Mount (1000/Reel)

Pin Configuration



Switching Characteristics of Normally Closed Devices



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Absolute Maximum Ratings @ 25ºC

Parameter	Ratings	Units
Peak Blocking Voltage	350	V_P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation [1]	100	mW
Total Package Dissipation [2]	550	mW
Isolation Voltage, Input to Output	5000	V_{rms}
ESD Rating, Human Body Model	8	kV
Operational Temperature	-40 to +85	ºC
Storage Temperature	-40 to +125	ºC

[1] Derate linearly 1.33mW / ºC

[2] Derate output power linearly 3mW / ºC

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

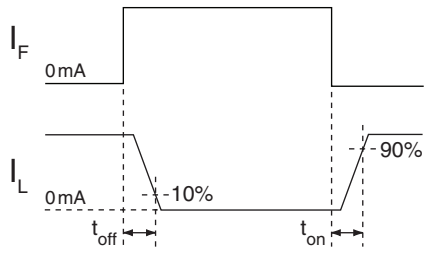
Electrical Characteristics @ 25ºC

Parameters	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Blocking Voltage	I_L=1µA	V_{DRM}	350	-	-	V_P
Load Current						
Continuous	-	I_L	-	-	130	mA_{rms} / mA_{DC}
Peak	t=10ms	I_{LPK}	-	-	±350	mA_P
On-Resistance [1]	I_L=130mA	R_{ON}	-	25	30	Ω
Off-State Leakage Current	I_F=2mA, V_L=350V	I_{LEAK}	-	-	1	µA
Switching Speeds						
Turn-On Output (Deactivate)	I_F=5mA, V_L=10V	t_{on}	-	-	2	ms
Turn-Off Output (Activate)		t_{off}	-	-	3	
Output Capacitance	I_F=2mA, V_L=50V, f=1MHz	C_{OUT}	-	6	-	pF
Input Characteristics						
Input Control Current to Activate [2]	-	I_F	-	0.18	2	mA
Input Control Current to Deactivate	I_L=130mA	I_F	0.1	-	-	mA
Input Voltage Drop	I_F=5mA	V_F	0.9	1.26	1.5	V
Reverse Input Current	V_R=5V	I_R	-	-	10	µA
Common Characteristics						
Capacitance, Input to Output	V_{IO}=0V, f=1MHz	C_{IO}	-	3	-	pF

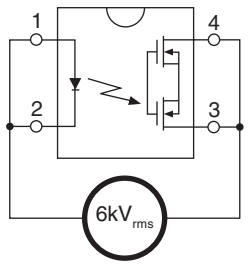
[1] Measurement taken within one second of on-time.

[2] For high temperature operation (>60ºC), IXYS Integrated Circuits recommends a minimum LED drive current of 5mA.

Timing Diagram



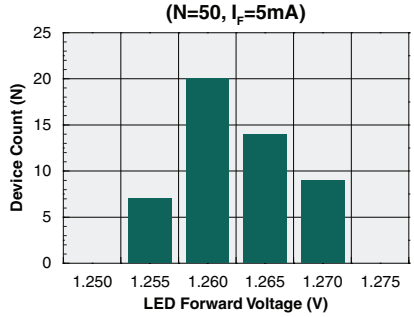
CPC1333G Isolation Test Circuit



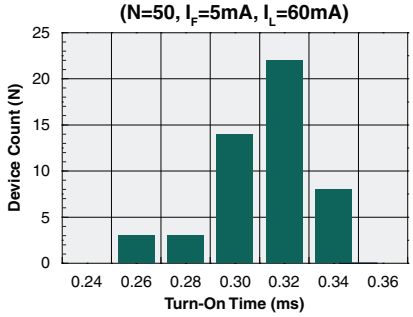
Test Conditions:
Voltage Ramp: 2V/µs
Test Time: 1 Second
I_{LEAK} Threshold: 50µA
Test Voltage: $6kV_{rms}$

PERFORMANCE DATA*

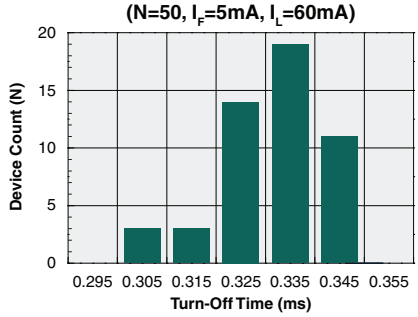
Typical LED Forward Voltage Drop
(N=50, I_F=5mA)



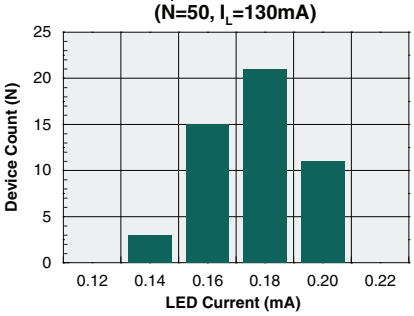
Typical Turn-On Time
(N=50, I_F=5mA, I_L=60mA)



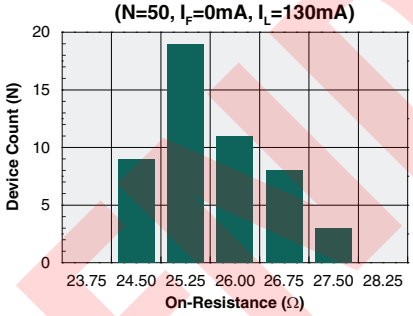
Typical Turn-Off Time
(N=50, I_F=5mA, I_L=60mA)



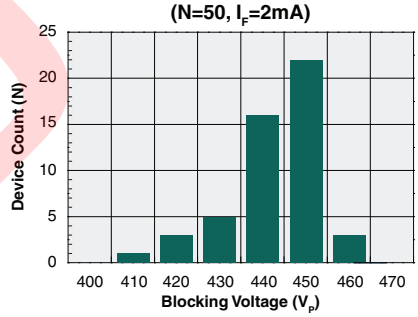
Typical I_F for Switch Operation
(N=50, I_L=130mA)



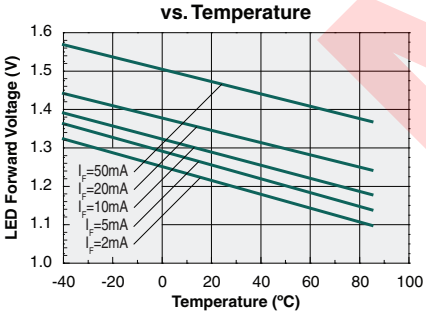
Typical On-Resistance Distribution
(N=50, I_F=0mA, I_L=130mA)



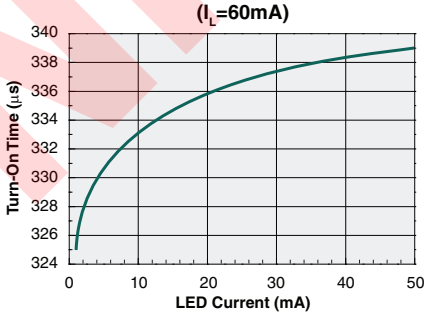
Typical Blocking Voltage Distribution
(N=50, I_F=2mA)



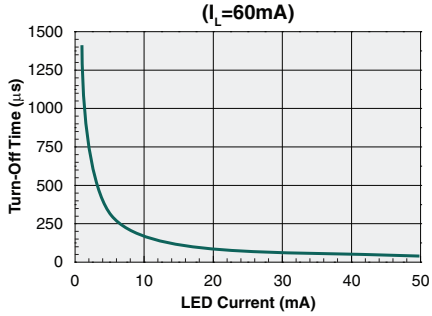
Typical LED Forward Voltage Drop
vs. Temperature



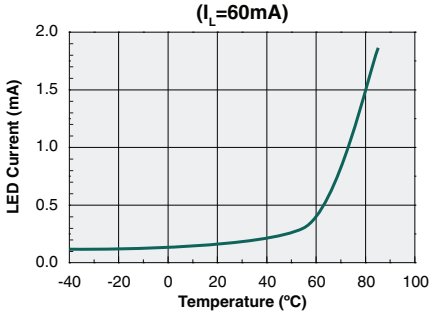
Typical Turn-On Time
vs. LED Forward Current
(I_L=60mA)



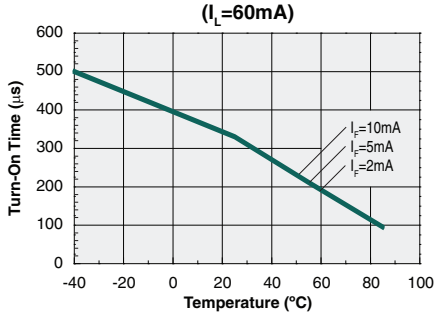
Typical Turn-Off Time
vs. LED Forward Current
(I_L=60mA)



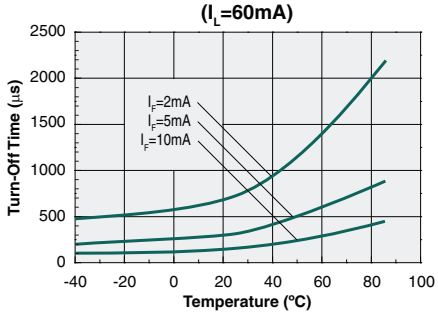
Typical I_F for Switch Operation
vs. Temperature
(I_L=60mA)



Typical Turn-On Time
vs. Temperature
(I_L=60mA)



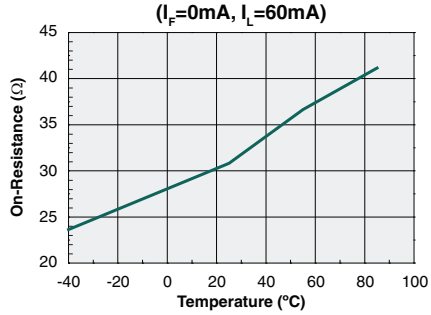
Typical Turn-Off Time
vs. Temperature
(I_L=60mA)



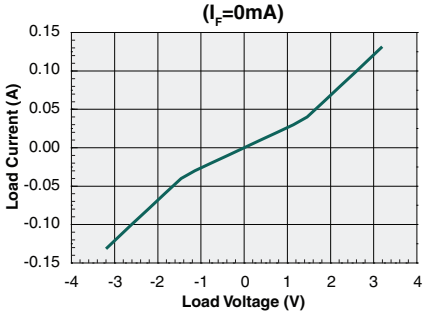
*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA*

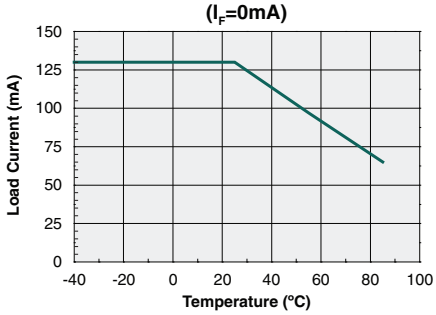
Typical On-Resistance vs. Temperature (I_F=0mA, I_L=60mA)



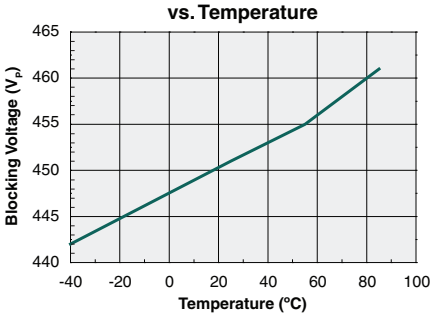
Typical Load Current vs. Load Voltage (I_F=0mA)



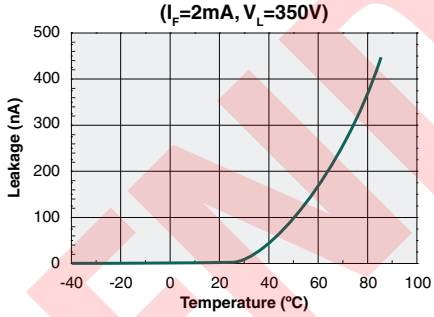
Maximum Load Current vs. Temperature (I_F=0mA)



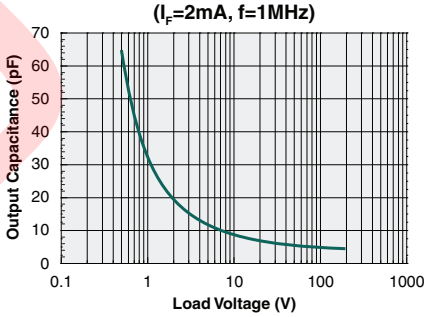
Typical Blocking Voltage vs. Temperature



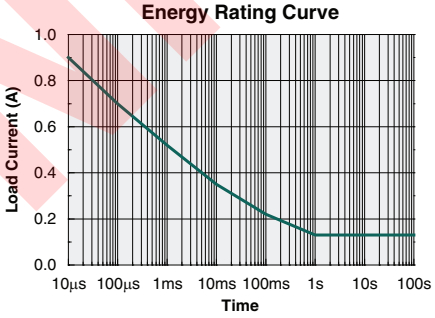
Typical Leakage vs. Temperature Measured Between Pins 3 & 4 (I_F=2mA, V_L=350V)



Output Capacitance vs. Load Voltage (I_F=2mA, f=1MHz)



Energy Rating Curve



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
CPC1333G	MSL 1
CPC1333GR	MSL 3

ESD Sensitivity

This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the Classification Temperature (T_C) of this product and the maximum dwell time the body temperature of this device may be (T_C - 5)°C or greater. The classification temperature sets the Maximum Body Temperature allowed for this device during lead-free reflow processes. Additionally, for the CPC1333GR, the solder reflow profile given in Technical Brief TB-200 **"Pb-Free Solder Reflow Profile for Select Devices"** must be followed. For the through-hole device, CPC1333G, and any other processes, the guidelines of **J-STD-020** must be observed.

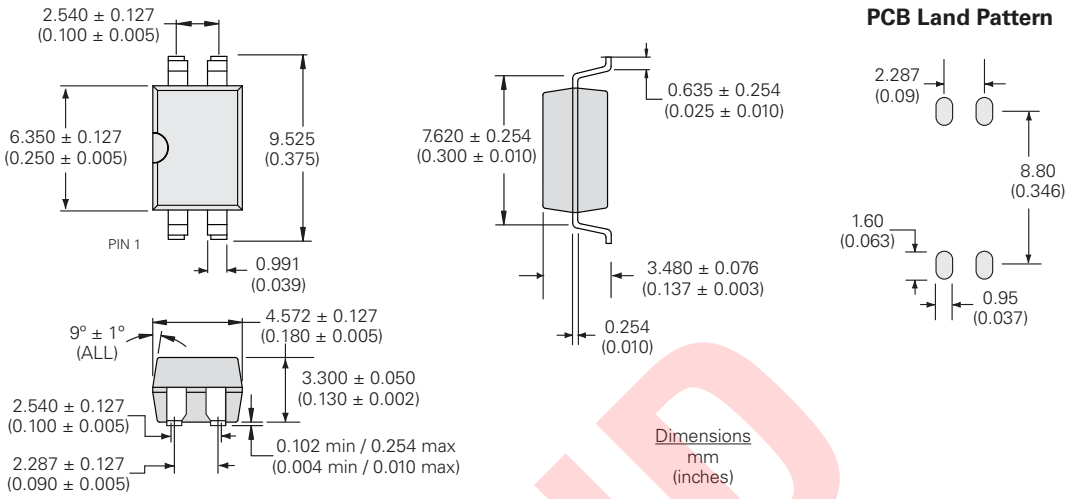
Device	Classification Temperature (T_c)	Dwell Time (t_p)	Max Reflow Cycles
CPC1333G	250°C	15 seconds	1
CPC1333GR	250°C	15 seconds	3

Board Wash

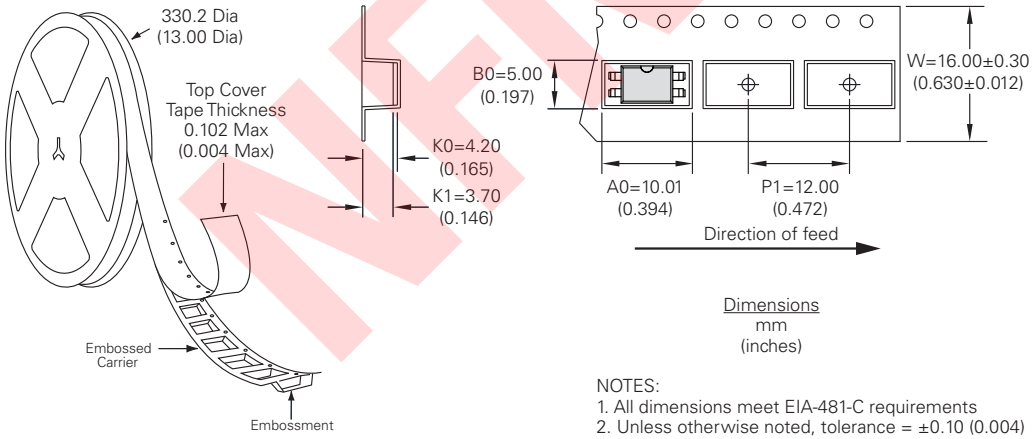
IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to flux or solvents that are Chlorine- or Fluorine-based.

Mechanical Dimensions

CPC1333GR



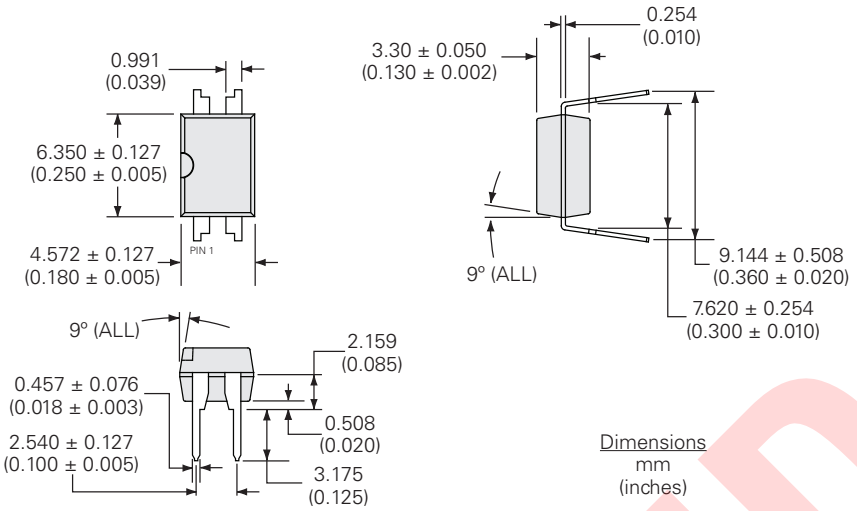
CPC1333GRTR Tape & Reel



NOTES:
1. All dimensions meet EIA-481-C requirements
2. Unless otherwise noted, tolerance = ±0.10 (0.004)

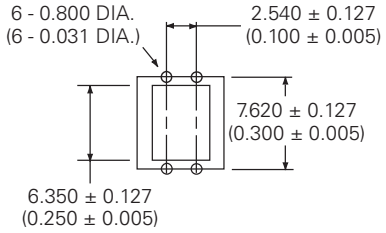
NFND

CPC1333G



Dimensions
mm
(inches)

PC Board Pattern (Top View)



NFND

For additional information please visit our website at: www.ixysic.com

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Specification: DS-CPC1333-R05
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11/18/2025