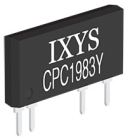
Not For New Designs

IXYS
INTEGRATED CIRCUITS DIVISION



CPC1983Y
600V, 0.5A_{DC} Single-Pole Normally Open Power SIP Relay

Parameter	Rating	Units
Blocking Voltage	600	V_P
Load Current	0.5	A_{DC}
On-Resistance (max)	6	Ω

Features

- Handle Load Currents Up to 0.5A
- 600V_P Blocking Voltage
- 2500V_{rms} Input/Output Isolation
- Power SIP Package
- High Reliability
- Low Drive Power Requirements
- No EMI/RFI Generation

Applications

- Industrial Controls
- Motor Control
- Robotics
- Medical Equipment—Patient/Equipment Isolation
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- IC Equipment
- Home Appliances

Description

IXYS Integrated Circuits brings OptoMOS technology, reliability, and compact size to a new family of high power Solid State Relays.

As part of this family, the CPC1983Y single-pole, normally open (1-Form-A) Solid State Power Relay is rated for up to 0.5A_{DC} continuous load current.

The CPC1983Y employs optically coupled MOSFET technology to provide 2500V_{rms} of input to output isolation. The optically coupled outputs, that use patented OptoMOS architecture, are controlled by a highly efficient infrared LED.

This combination of low on-resistance and high load current handling capability makes this relay suitable for a variety of high performance switching applications.

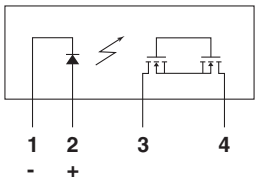
Approvals

- UL 508 Recognized Component: File E69938
- CSA Certified Component: Certificate 1172007
- EN IEC 62368-1: TUV Certificate B 082667 0008

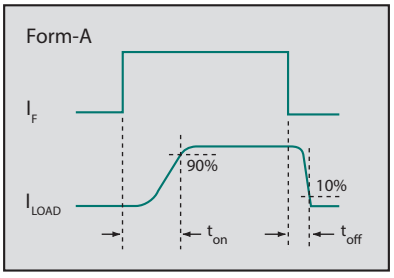
Ordering Information

Part #	Description
CPC1983Y	4-Pin (8-Pin Body) Power SIP Package (25 per tube)

Pin Configuration



Switching Characteristics of Normally Open Devices



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Not For New Designs

Absolute Maximum Ratings @ 25°C

Parameter	Symbol	Rating	Unit
Blocking Voltage	V_L	600	V_P
Reverse Input Voltage	V_R	5	V
Input control Current	I_F	50	mA
Peak (10ms)		1	A
Input Power Dissipation [1]	P_{IN}	150	mW
Total Power Dissipation [2]	P_T	2400	mW
Isolation Voltage, Input to Output	V_{ISO}	2500	V_{rms}
Operational Temperature, Ambient	T_A	-40 to +85	°C
Storage Temperature	T_{STG}	-40 to +125	°C

[1] Derate linearly 1.33 mW / °C
[2] Derate output power linearly 20 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Blocking Voltage	I_L=1µA	V_{DRM}	600	-	-	V
Load Current	I_F=5mA, Free air					
Continuous	-	I_L	-	-	0.5	A_{DC}
Peak	t=10ms	I_{LPK}	-	-	3	A
On-Resistance[1]	I_F=5mA, I_L=0.5A	R_{ON}	-	3.5	6	Ω
Off-State Leakage Current	I_F=0mA, V_L=600V_P	I_{LEAK}	-	-	1	µA
Switching Speeds						
Turn-On	I_F=5mA, V_L=10V	t_{on}	-	2.2	5	ms
Turn-Off		t_{off}	-	0.15	2	
Output Capacitance	I_F=0mA, V_L=50V, f=1MHz	C_{OUT}	-	41	-	pF
Input Characteristics						
Input Control Current to Activate	I_L=0.5A	I_F	-	1.3	5	mA
Input Control Current to Deactivate	-	I_F	0.5	-	-	mA
Input Voltage Drop	I_F=5mA	V_F	0.9	1.36	1.5	V
Reverse Input Current	V_R=5V	I_R	-	-	10	µA
Input/Output Characteristics						
Capacitance, Input to Output	V_{IO}=0V, f=1MHz	C_{IO}	-	2	-	pF

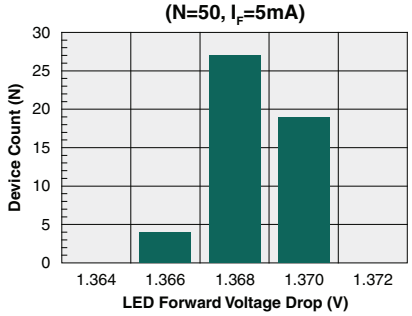
[1] Measurement taken within 1 second of on-time.

Thermal Characteristics

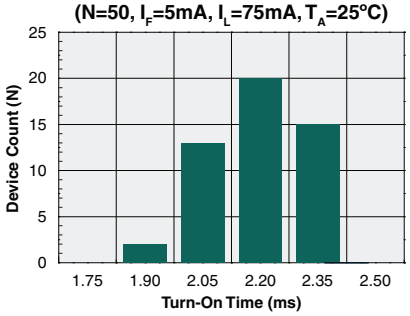
Parameter	Conditions	Symbol	Rating	Units
Thermal Impedance (junction to case)	-	θ_{JC}	1.5	°C/W

PERFORMANCE DATA*

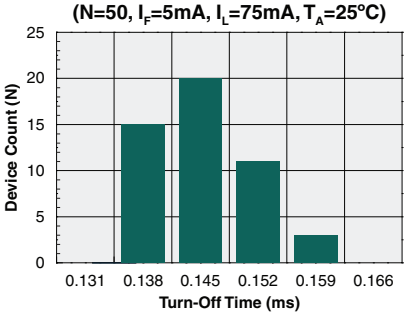
Typical LED Forward Voltage Drop
(N=50, I_F=5mA)



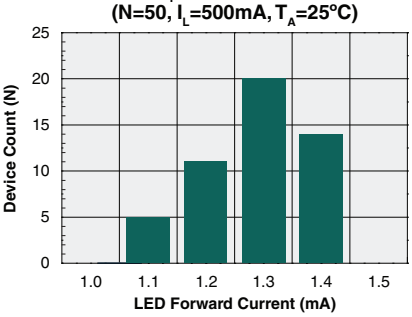
Typical Turn-On Time
(N=50, I_F=5mA, I_L=75mA, T_A=25°C)



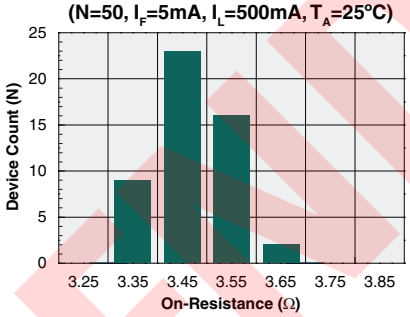
Typical Turn-Off Time
(N=50, I_F=5mA, I_L=75mA, T_A=25°C)



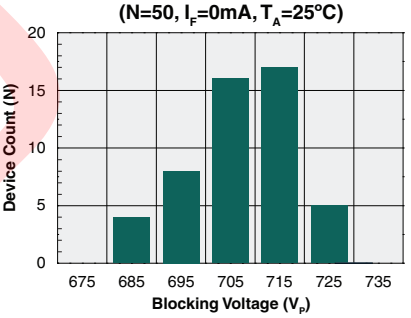
Typical I_F for Switch Operation
(N=50, I_L=500mA, T_A=25°C)



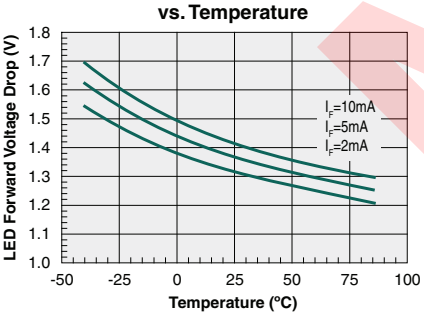
Typical On-Resistance Distribution
(N=50, I_F=5mA, I_L=500mA, T_A=25°C)



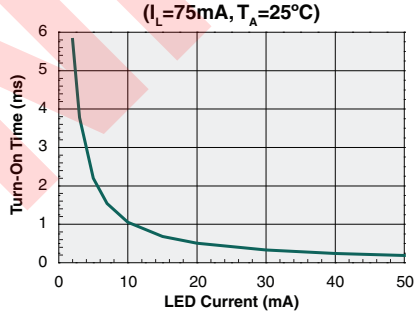
Typical Blocking Voltage Distribution
(N=50, I_F=0mA, T_A=25°C)



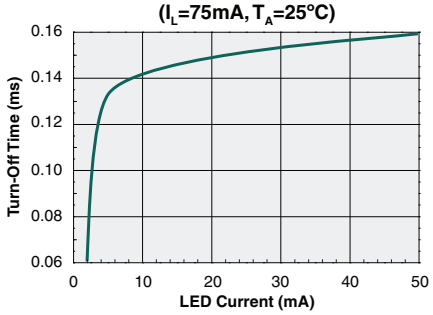
Typical LED Forward Voltage Drop
vs. Temperature



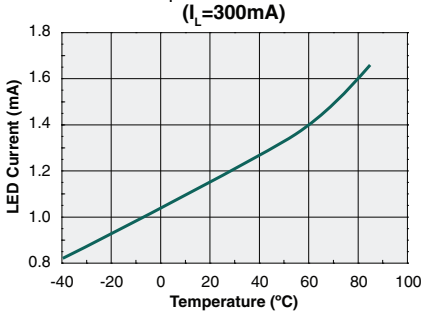
Typical Turn-On Time
vs. LED Forward Current
(I_L=75mA, T_A=25°C)



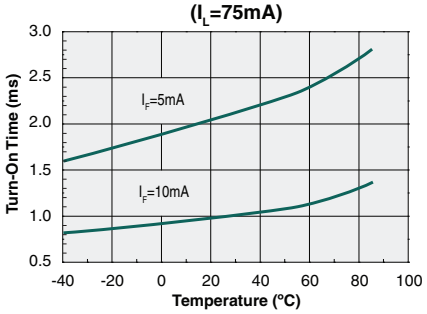
Typical Turn-Off Time
vs. LED Forward Current
(I_L=75mA, T_A=25°C)



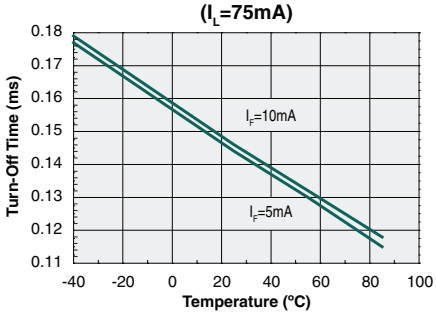
Typical I_F for Switch Operation
(I_L=300mA)



Typical Turn-On Time
vs. Temperature
(I_L=75mA)



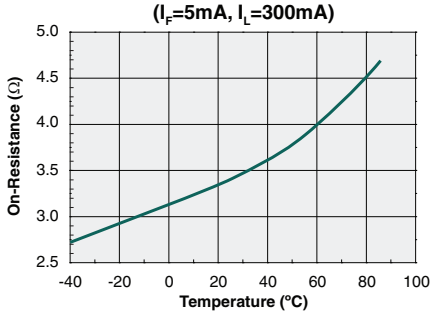
Typical Turn-Off Time
vs. Temperature
(I_L=75mA)



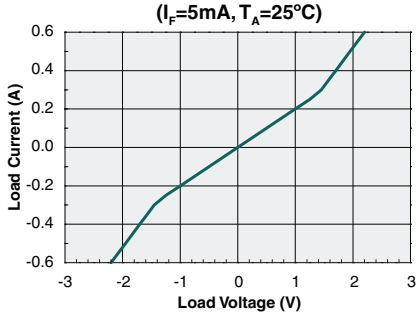
*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

PERFORMANCE DATA*

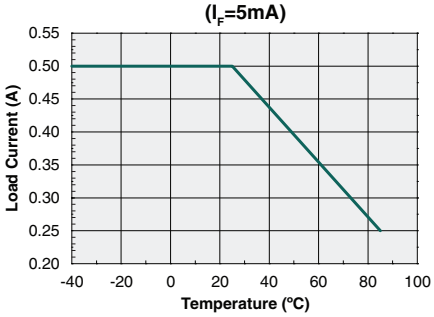
Typical On-Resistance vs. Temperature (I_F=5mA, I_L=300mA)



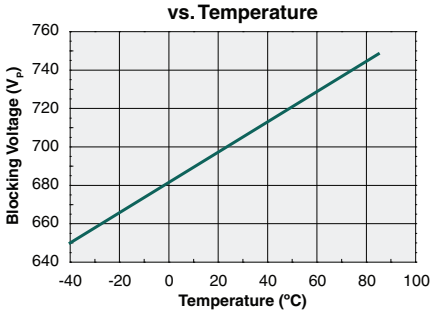
Typical Load Current vs. Load Voltage (I_F=5mA, T_A=25°C)



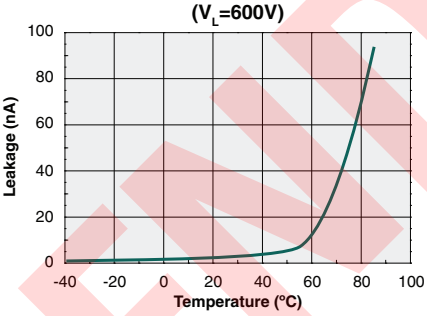
Maximum Load Current vs. Temperature (I_F=5mA)



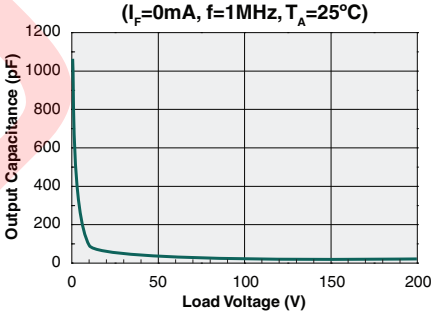
Typical Blocking Voltage vs. Temperature



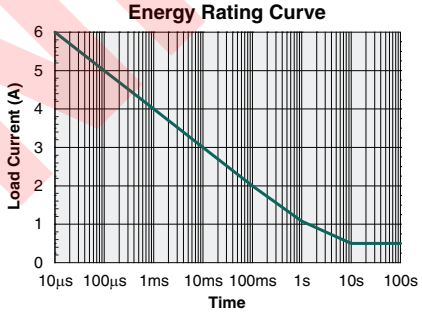
Typical Leakage Current vs. Temperature (V_L=600V)



Output Capacitance vs. Load Voltage (I_F=0mA, f=1MHz, T_A=25°C)



Energy Rating Curve



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*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

Manufacturing Information

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

The Maximum Solder Temperature and the Maximum Total Dwell Time in all solder waves the device pins (leads) may be at the Maximum Solder Temperature is given in the table below. The body temperature of the device must not exceed the Maximum Body Temperature shown below at any time during the soldering process.

Device	Maximum Solder Temperature	Maximum Body Temperature	Maximum Total Dwell Time	Wave Cycles
CPC1983Y	260°C	245°C	10 seconds	1

Board Wash

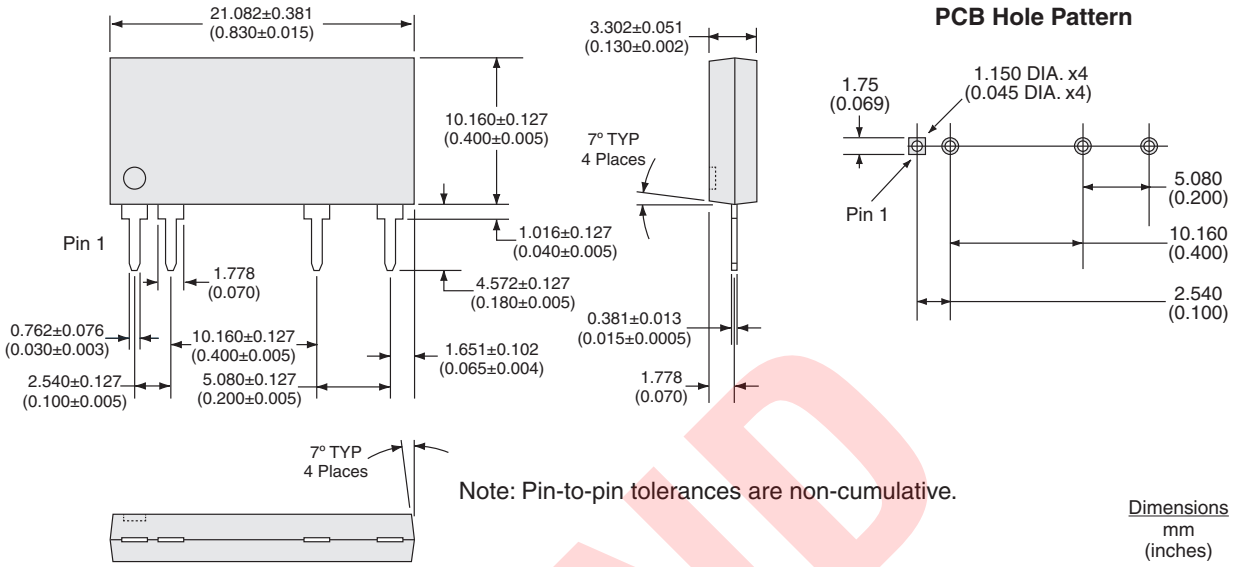
IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.

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MECHANICAL DIMENSIONS

CPC1983Y



Note: Pin-to-pin tolerances are non-cumulative.

Dimensions
mm
(inches)

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For additional information please visit our website at: https://www.ixysic.com

IXYS
A Littelfuse Technology

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Specification: DS-CPC1983Y-R07

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2/10/2026