



Parameter	Ratings	Units
Blocking Voltage	350	V _P
Load Current	120	mA _{rms} / mA _{DC}
On-Resistance (max)	35	Ω

Features

- 3750V_{rms} Input/Output Isolation
- Low Drive Power Requirements
- Greater Reliability than Electromechanical Relays
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Small 8-Pin Package
- Surface Mount, Tape & Reel Versions Available

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hook Switch
 - Dial Pulsing
 - Ground Start
 - Ringing Injection
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Industrial Controls

Description

LBB110 is a dual normally closed (1-Form-B) solid state relay that has two independently controlled, optically coupled MOSFET switches in an 8-pin SOIC package that employs optically coupled MOSFET technology to provide 3750V_{rms} of input to output isolation.

Its optically coupled outputs, which use patented OptoMOS architecture, are controlled by a highly efficient infrared LED.

Dual single-pole OptoMOS relays provide a more compact design solution than discrete single-pole relays in a variety of applications by incorporating both relays in a single 8-pin package.

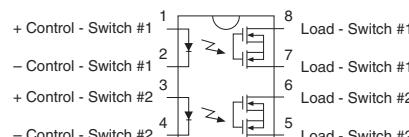
Approvals

- UL Recognized Component: File # E76270
- CSA Certified Component: Certificate # 1175739
- TUV EN 62368-1: Certificate # B 082667 0008

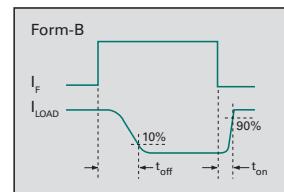
Ordering Information

Part #	Description
LBB110	8-Pin DIP (50/Tube)
LBB110P	8-Pin Flatpack (50/Tube)
LBB110PTR	8-Pin Flatpack (1000/Reel)
LBB110S	8-Pin Surface Mount (50/Tube)
LBB110STR	8-Pin Surface Mount (1000/Reel)

Pin Configuration



Switching Characteristics of Normally Closed Devices



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	350	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
	1	A
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
Operational Temperature, Ambient	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 1.33 mW / °C

² Derate output power linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

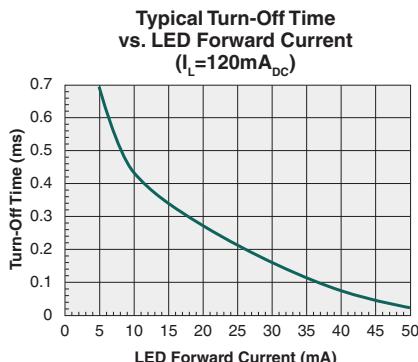
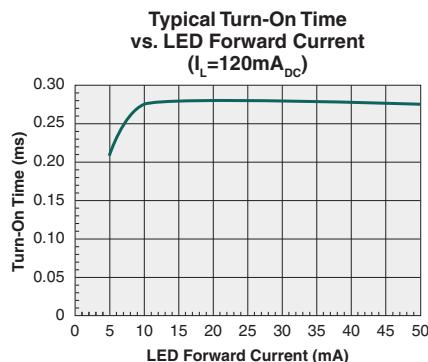
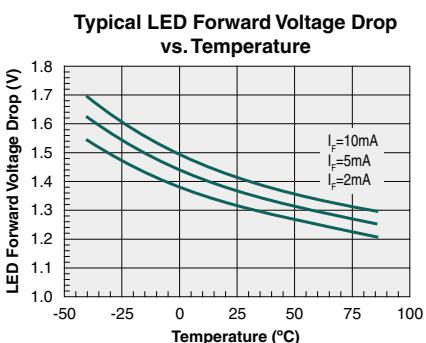
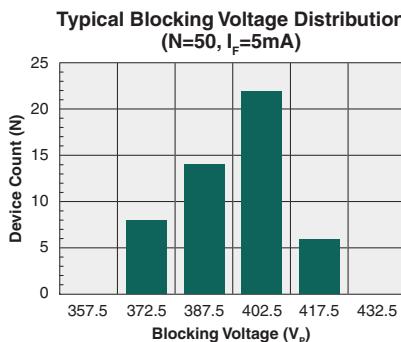
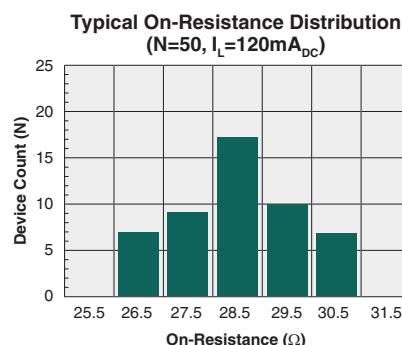
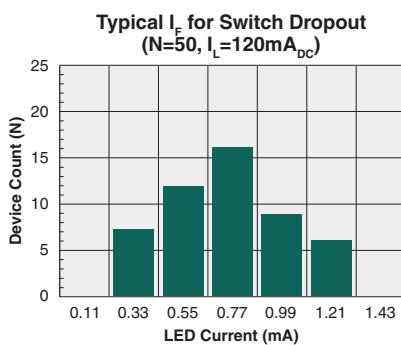
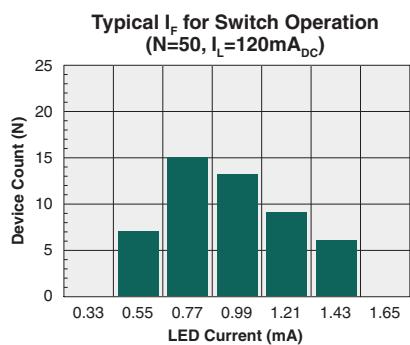
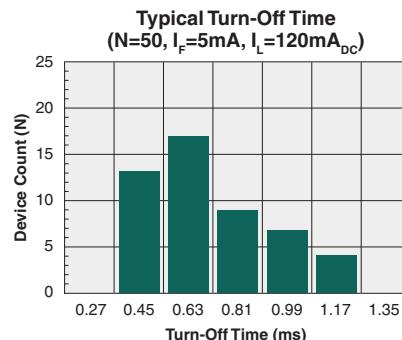
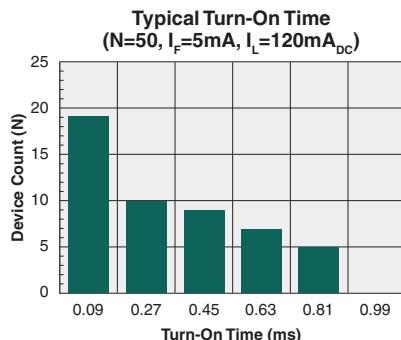
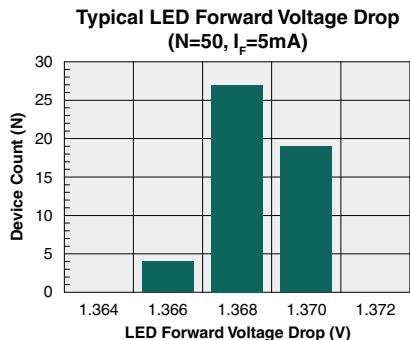
Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Blocking Voltage	I _L =1μA	V _{DRM}	350	-	-	V _P
Load Current Continuous ¹	-	I _L	-	-	120	mA _{rms} / mA _{DC}
	t=10ms	I _{LPK}	-	-	±350	A _P
On-Resistance ²	I _L =120mA	R _{ON}	-	25	35	Ω
Off-State Leakage Current	V _L =350V _P	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F =5mA, V _L =10V	t _{on}	-	-	3	ms
Turn-Off		t _{off}	-	-	3	
Output Capacitance	I _F =5mA, V _L =50V, f=1MHz	C _{OUT}	-	25	-	pF
Input Characteristics						
Input Control Current to Activate	I _L =120mA	I _F	-	-	5	mA
Input Control Current to Deactivate	-	I _F	0.4	0.7	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.36	1.5	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics						
Input to Output Capacitance	V _{IO} =50V, f=1MHz	C _{IO}	-	3	-	pF

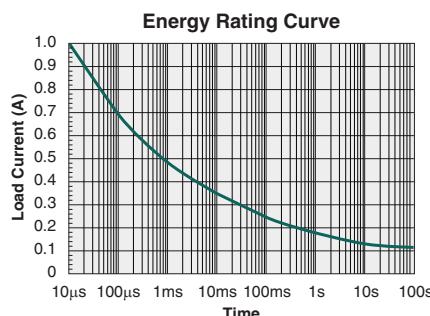
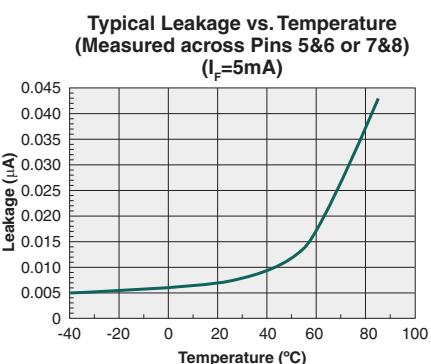
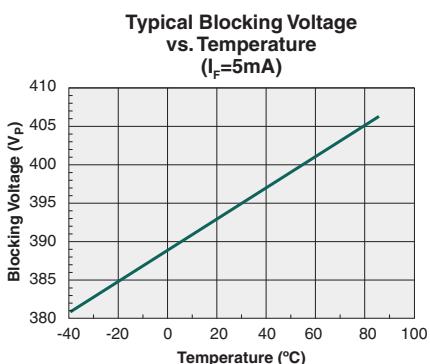
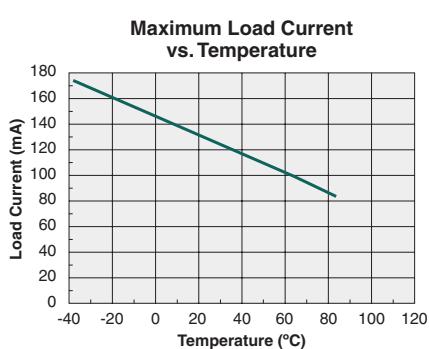
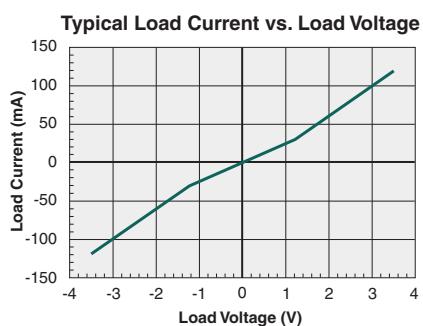
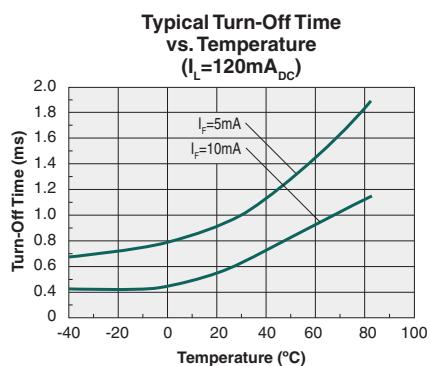
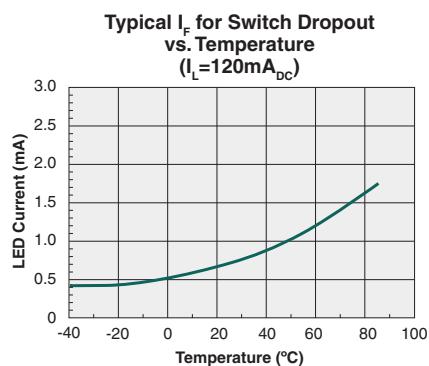
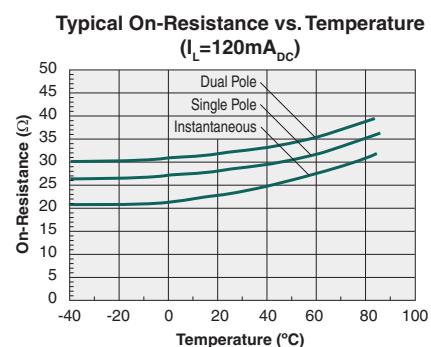
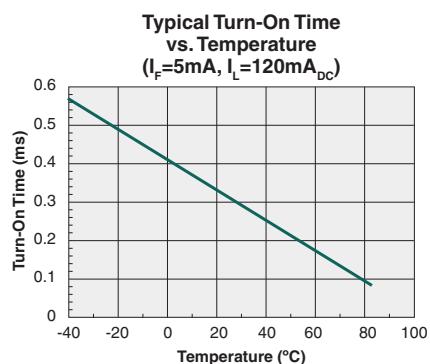
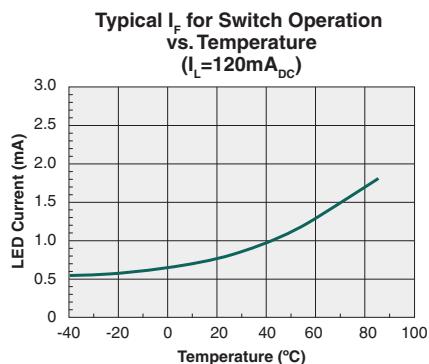
¹ If both poles operate simultaneously, then the load current must be derated so as not to exceed the package power dissipation value.

² Measurement taken within 1 second of on-time.

PERFORMANCE DATA*


*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

PERFORMANCE DATA*



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL)** classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
LBB110P / LBB110S	MSL 1

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the **IPC/JEDEC J-STD-020** Classification Temperature (T_c) and the maximum dwell time the body temperature of these surface mount devices may be ($T_c - 5$)°C or greater. The Classification Temperature sets the Maximum Body Temperature allowed for these devices during reflow soldering processes.

Device	Classification Temperature (T_c)	Dwell Time (t_p)	Max Reflow Cycles
LBB110S	250°C	30 Seconds	3
LBB110P	260°C	30 seconds	3

For through-hole devices, the maximum pin temperature and maximum dwell time through all solder waves is provided in the table below. Dwell time is the interval beginning when the pins are initially immersed into the solder wave until they exit the solder wave. For multiple waves, the dwell time is from entering the first wave until exiting the last wave. During this time, pin temperatures must not exceed the maximum temperature given in the table below. Body temperature of the device must not exceed the limit shown in the table below at any time during the soldering process.

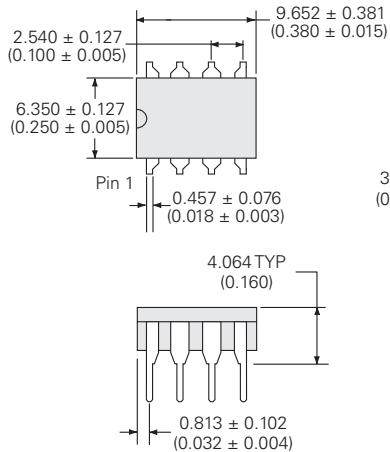
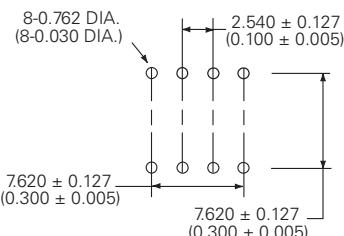
Device	Maximum Pin Temperature	Maximum Body Temperature	Maximum Dwell Time	Wave Cycles
LBB110	260°C	250°C	10 seconds*	1

*Total cumulative duration of all waves.

Board Wash

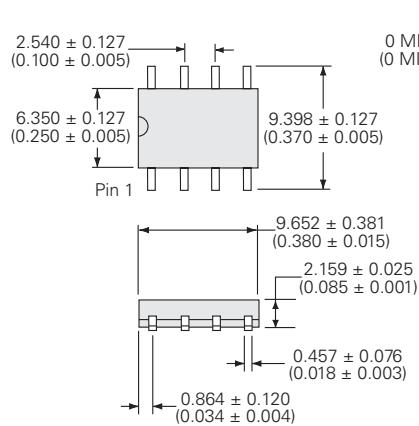
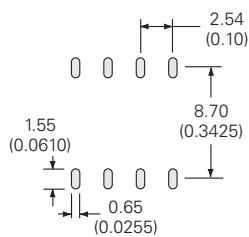
IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.



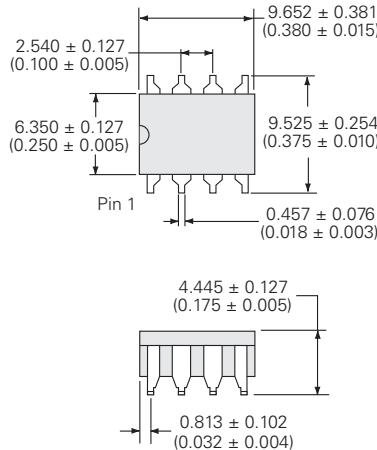
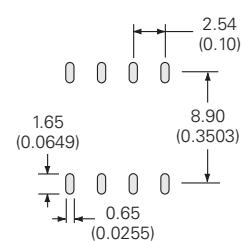
MECHANICAL DIMENSIONS
LBB110

PCB Hole Pattern

Notes:

1. Controlling dimension: Inches
2. Leadframe thickness does not include solder plating (1000 μ inches max)

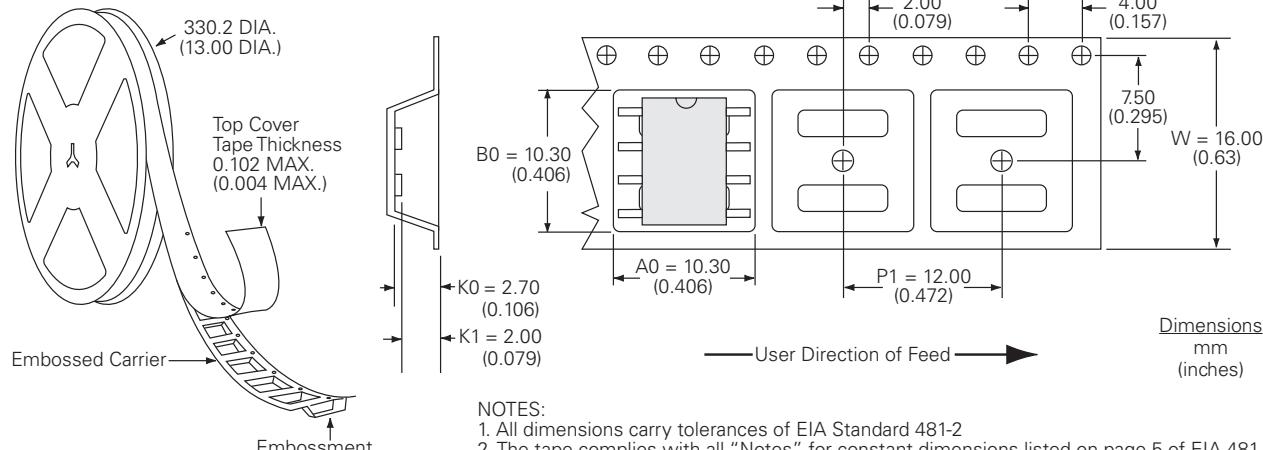
Dimensions
mm
(inches)

LBB110P

PCB Land Pattern

Notes:

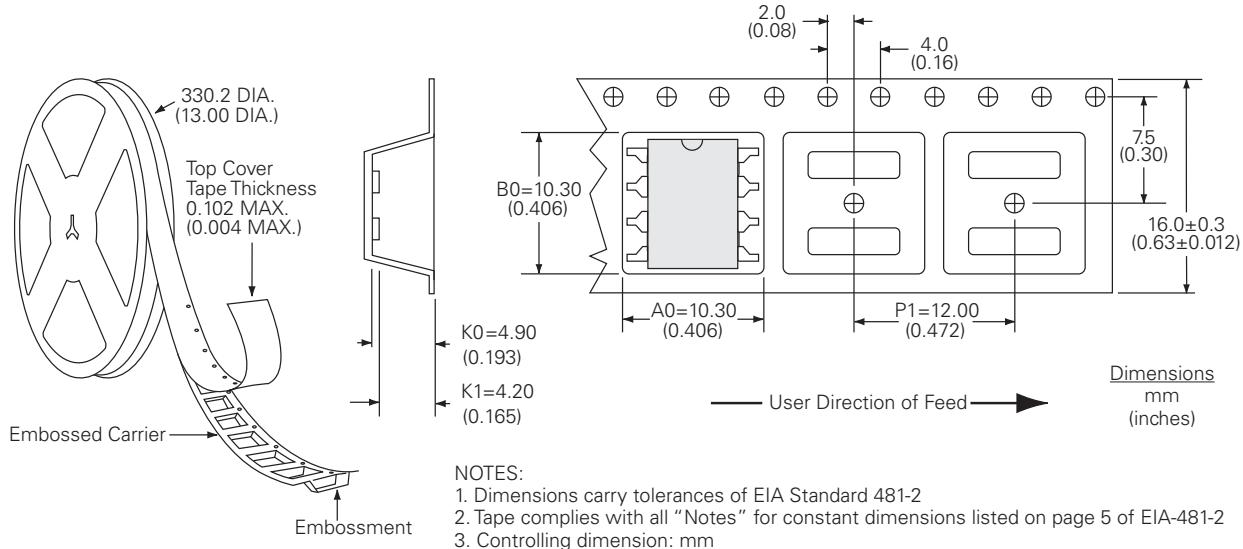
1. Controlling dimension: Inches
2. Coplanarity = 0.004" (0.102 mm) max
3. Leadframe thickness does not include solder plating (1000 μ inches max)
3. Sum of package height, standoff, and coplanarity shall not exceed 0.090" (2.286 mm)

LBB110S

PCB Land Pattern

Notes:

1. Controlling dimension: Inches
2. Coplanarity = 0.004" (0.102 mm) max
3. Leadframe thickness does not include solder plating (1000 μ inches max)

LBB110PTR Tape & Reel


NOTES:
1. All dimensions carry tolerances of EIA Standard 481-2
2. The tape complies with all "Notes" for constant dimensions listed on page 5 of EIA-481-2
3. Controlling dimension: mm

LBB110STR Tape & Reel


NOTES:
1. Dimensions carry tolerances of EIA Standard 481-2
2. Tape complies with all "Notes" for constant dimensions listed on page 5 of EIA-481-2
3. Controlling dimension: mm

For additional information please visit our website at: <https://www.ixysic.com>



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