

Parameter	Rating	Units
Blocking Voltage	600	V _P
Load Current	100	mA _{rms} / mA _{DC}
On-Resistance (max)	50	Ω

Features

- 5000V_{rms} Input/Output Isolation
- Low Drive Power Requirements
- Greater Reliability than Electromechanical Relays
- No EMI/RFI Generation
- Small 8-Pin Package
- Surface Mount Tape & Reel Version Available

Applications

- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Industrial Controls

Description

The PAA193 is a dual normally open (1-Form-A) solid state relay that uses optically coupled relay technology to provide an enhanced 5000V_{rms} input to output isolation barrier. The efficient MOSFET switches use IXYS Integrated Circuits' patented OptoMOS architecture. Highly efficient infrared LEDs provide the optically coupled control.

Dual OptoMOS relays provide a more compact design solution than discrete single-pole relays in a variety of applications. The dual relays save board space by incorporating two relays in a single 8-pin package.

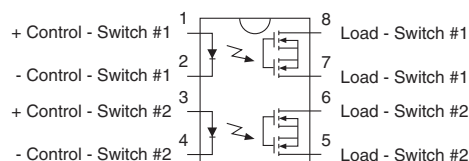
Approvals

- UL Recognized Component: File E76270
- TUV EN 62368-1: Certificate # B 082667 0008

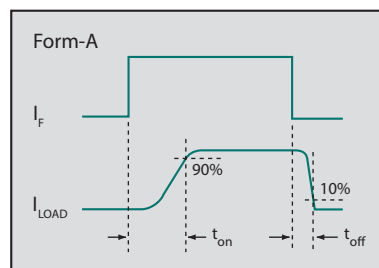
Ordering Information

Part #	Description
PAA193	8-Lead DIP (50/tube)
PAA193S	8-Lead Surface Mount (50/tube)
PAA193STR	8-Lead Surface Mount (1000/Reel)

Pin Configuration



Switching Characteristics of Normally Open Devices



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	600	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	5000	V _{rms}
Operational Temperature, Ambient	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 1.33 mW / °C

² Derate output power linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

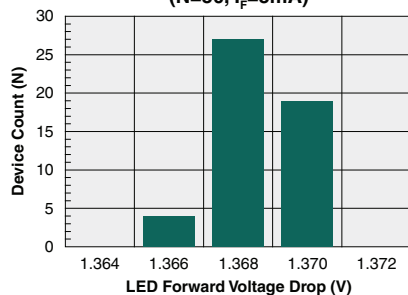
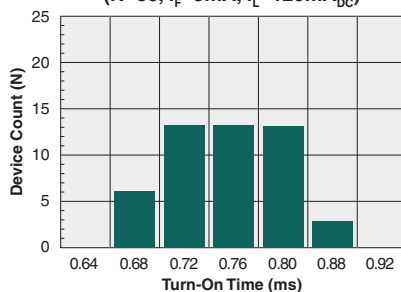
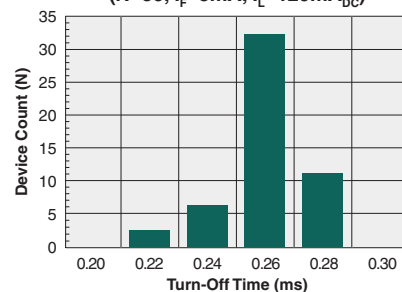
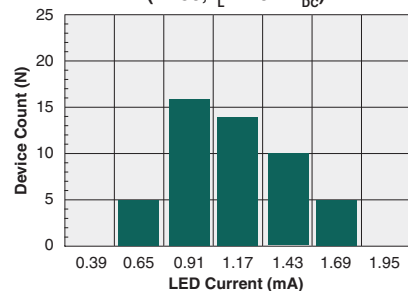
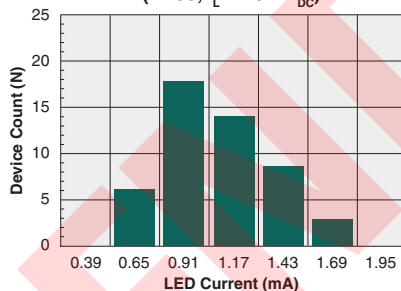
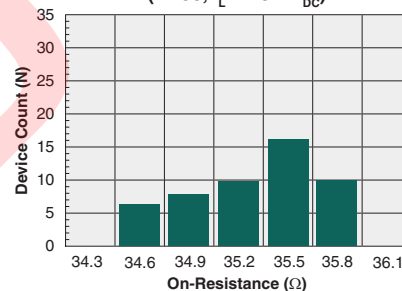
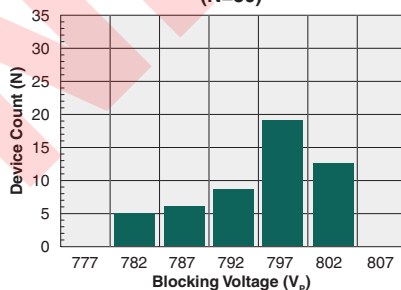
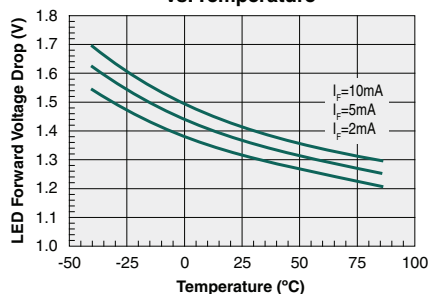
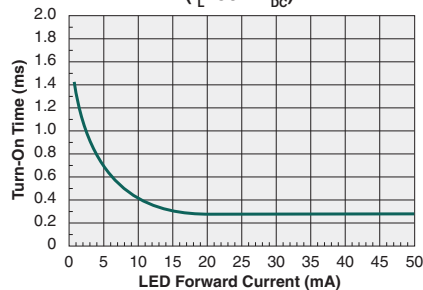
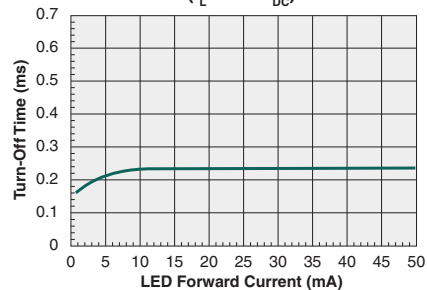
Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Blocking Voltage	I _L =1μA	V _{DRM}	600	-	-	V _P
Load Current						
Continuous ¹	-	I _L	-	-	100	mA _{rms} / mA _{DC}
Peak	t=10ms	I _{LPK}	-	-	±350	mA _P
On-Resistance ²	I _L =100mA	R _{ON}	-	-	50	Ω
Off-State Leakage Current	V _L =600V _P	I _{LEAK}	-	-	10	μA
Switching Speeds						
Turn-On	I _F =5mA, V _L =10V	t _{on}	-	-	5	ms
Turn-Off		t _{off}	-	-	5	
Output Capacitance	I _F =0mA, V _L =50V, f=1MHz	C _{OUT}	-	50	-	pF
Input Characteristics						
Input Control Current to Activate	I _L =100mA	I _F	-	-	5	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.36	1.5	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics						
Capacitance, Input to Output	V _{IO} =0V, f=1MHz	C _{IO}	-	3	-	pF

¹ If both poles operate simultaneously, then load current must be derated in order not to exceed the package power dissipation value.

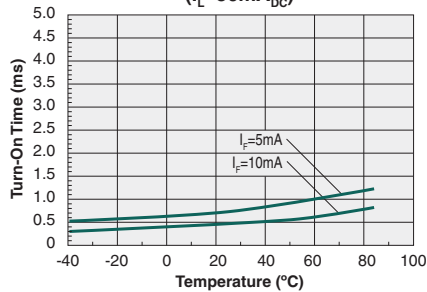
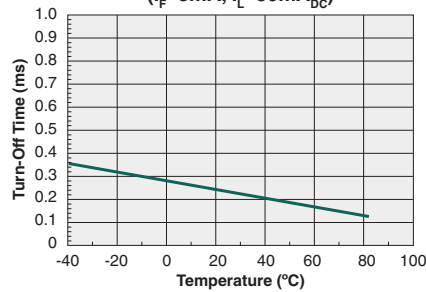
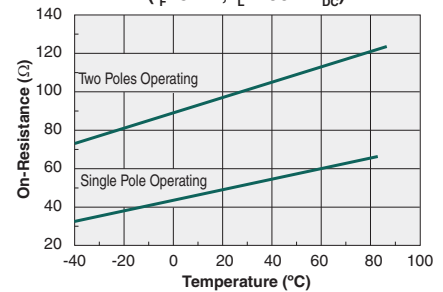
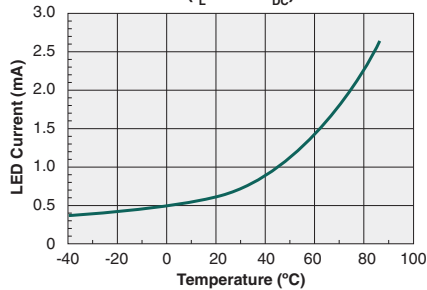
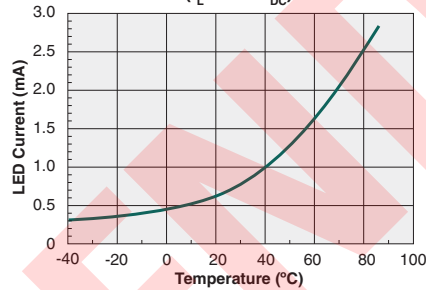
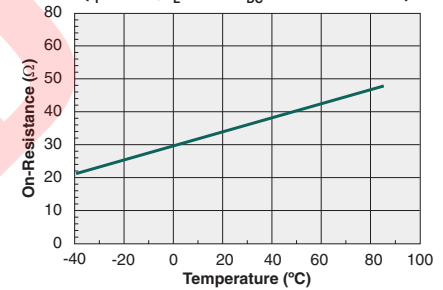
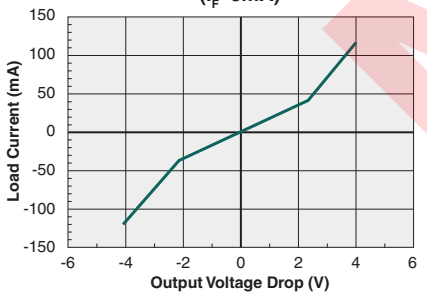
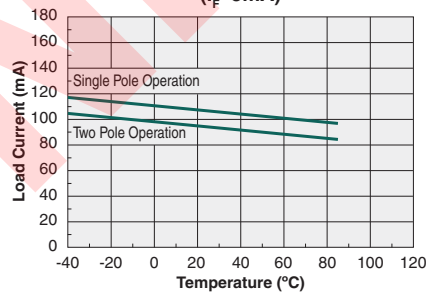
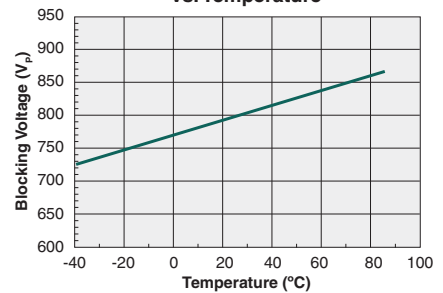
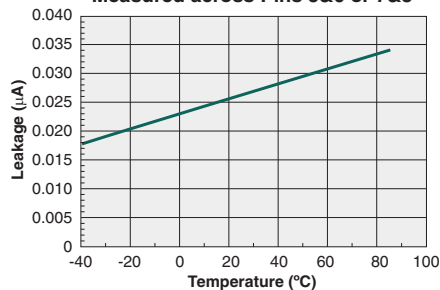
² Measurement taken within one second of on-time.

PERFORMANCE DATA*

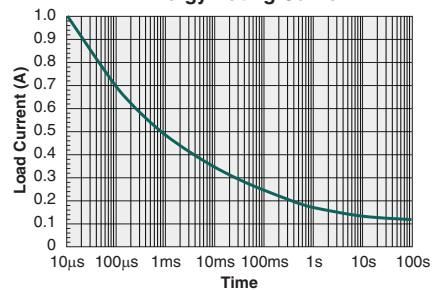
 Typical LED Forward Voltage Drop
 (N=50, $I_F=5\text{mA}$)

 Typical Turn-On Time
 (N=50, $I_F=5\text{mA}$, $I_L=120\text{mA}_{DC}$)

 Typical Turn-Off Time
 (N=50, $I_F=5\text{mA}$, $I_L=120\text{mA}_{DC}$)

 Typical I_F for Switch Operation
 (N=50, $I_L=120\text{mA}_{DC}$)

 Typical I_F for Switch Dropout
 (N=50, $I_L=120\text{mA}_{DC}$)

 Typical On-Resistance Distribution
 (N=50, $I_L=120\text{mA}_{DC}$)

 Typical Blocking Voltage Distribution
 (N=50)

 Typical LED Forward Voltage Drop
 vs. Temperature

 Typical Turn-On Time
 vs. LED Forward Current
 ($I_L=50\text{mA}_{DC}$)

 Typical Turn-Off Time
 vs. LED Forward Current
 ($I_L=50\text{mA}_{DC}$)


*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C .

PERFORMANCE DATA*

 Typical Turn-On Time
 vs. Temperature
 ($I_L = 50\text{mA}_{\text{DC}}$)

 Typical Turn-Off Time
 vs. Temperature
 ($I_F = 5\text{mA}$, $I_L = 50\text{mA}_{\text{DC}}$)

 Typical On-Resistance
 vs. Temperature
 ($I_F = 5\text{mA}$, $I_L = 100\text{mA}_{\text{DC}}$)

 Typical I_F for Switch Operation
 vs. Temperature
 ($I_L = 50\text{mA}_{\text{DC}}$)

 Typical I_F for Switch Dropout
 vs. Temperature
 ($I_L = 50\text{mA}_{\text{DC}}$)

 Typical On-Resistance
 vs. Temperature
 ($I_F = 5\text{mA}$, $I_L = 50\text{mA}_{\text{DC}}$ Instantaneous)

 Typical Load Current
 vs. Load Voltage
 ($I_F = 5\text{mA}$)

 Maximum Load Current
 vs. Temperature
 ($I_F = 5\text{mA}$)

 Typical Blocking Voltage
 vs. Temperature

 Typical Leakage vs. Temperature
 Measured across Pins 5&6 or 7&8


Energy Rating Curve



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL)** classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
PAA193S	MSL 1

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the **IPC/JEDEC J-STD-020** Classification Temperature (T_C) and the maximum total dwell time (t_p) in all reflow processes that the body temperature of these surface mount devices may be ($T_C - 5$)°C or greater. The device's body temperature must not exceed the Classification Temperature at any time during reflow soldering processes.

Device	Classification Temperature (T_C)	Dwell Time (t_p)	Max Reflow Cycles
PAA193S	250°C	30 seconds	3

For through-hole devices, the maximum pin temperature and maximum dwell time through all solder waves is provided in the table below. Dwell time is the interval beginning when the pins are initially immersed into the solder wave until they exit the solder wave. For multiple waves, the dwell time is from entering the first wave until exiting the last wave. During this time, pin temperatures must not exceed the maximum temperature given in the table below. Body temperature of the device must not exceed the limit shown in the table below at any time during the soldering process.

Device	Maximum Pin Temperature	Maximum Body Temperature	Maximum Dwell Time	Wave Cycles
PAA193	260°C	250°C	10 seconds*	1

*Total cumulative duration of all waves.

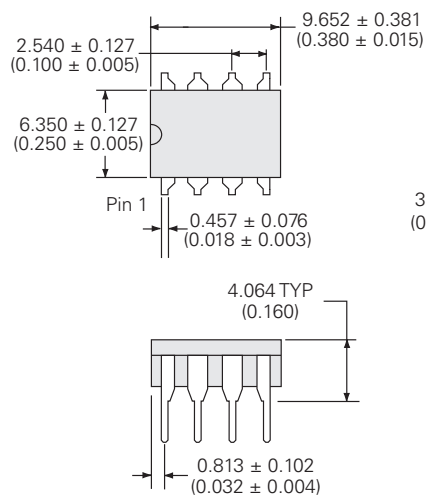
Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.

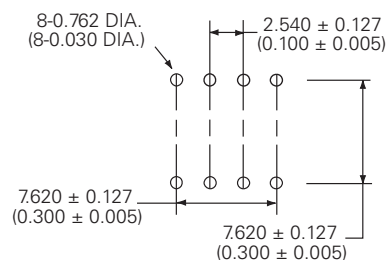


Mechanical Dimensions

PAA193



PCB Hole Pattern

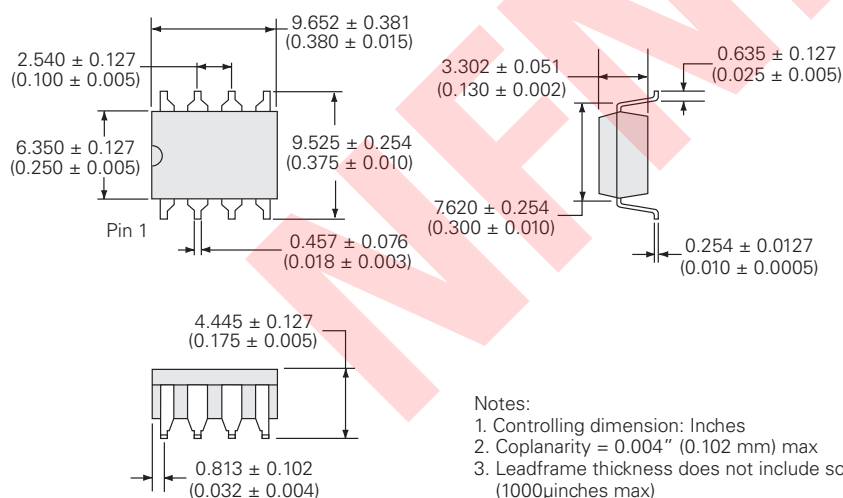


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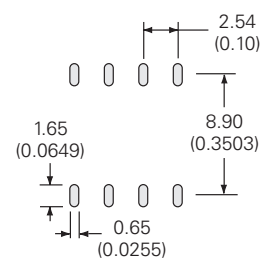
1. Controlling dimension: Inches
2. Leadframe thickness does not include solder plating (1000μinches max)

Dimensions
mm
(inches)

PAA193S



PCB Land Pattern

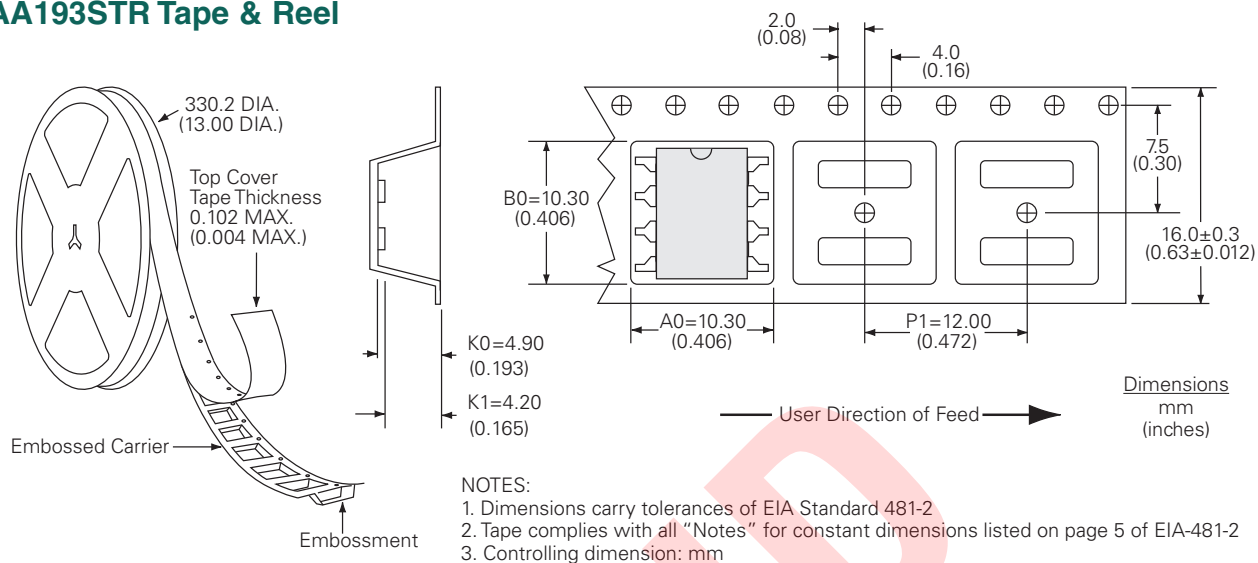


Notes:

1. Controlling dimension: Inches
2. Coplanarity = 0.004" (0.102 mm) max
3. Leadframe thickness does not include solder plating (1000μinches max)

Dimensions
mm
(inches)

PAA193STR Tape & Reel



For additional information please visit our website at: <https://www.ixysic.com>