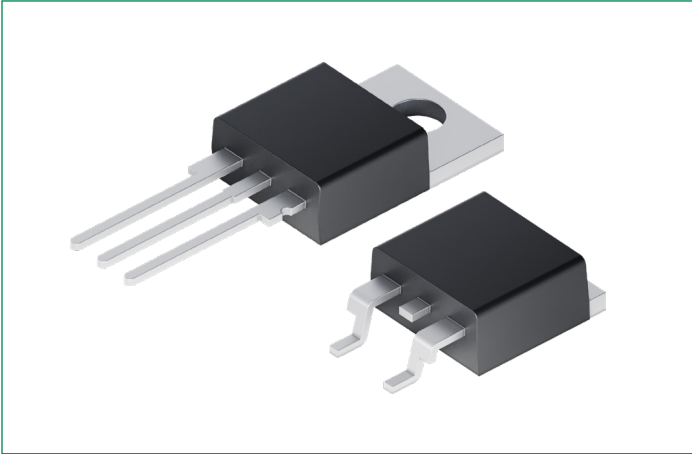


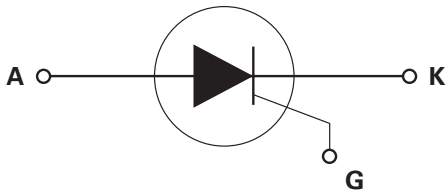
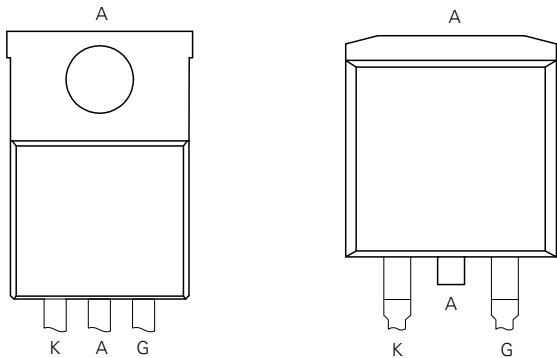
Sxx30x Series

30 A Standard SCRs

RoHS



Pinout Diagram TO-220AB and TO-263AB



K: Cathode; **A:** Anode; **G:** Gate

Description

This 30A SCR series in TO-220AB and TO-263 packages, offers design flexibility and optimized thermal management. Its clip-attach assembly, 290 A surge current capability, and high noise immunity simplify input protection and filtering.

Features

- Rated blocking voltage of 1200 V
- Halogen-free and RoHS-compliant
- Glass-passivated junctions
- Mechanically and thermally robust TO-220 clip-attach assembly
- Surge current capability up to 290 A

Applications

Typical applications include AC solid-state switches, industrial power tools, softstarter, DC motor control, and power converters.

Product Summary

Characteristic	Value	Unit
$I_{T(RMS)}$	30	A
V_{DRM}/V_{RRM}	1200	V
I_{GT}	30	mA

Maximum Ratings

Symbol	Characteristics	Conditions	Value	Units
$I_{T(RMS)}$	RMS On-state Current	$T_c = 100\text{ }^{\circ}\text{C}$	30	A
$I_{T(AV)}$	Average On-state Current	$T_c = 100\text{ }^{\circ}\text{C}$	19	A
I_{TSM}	Non-repetitive Surge Peak On-state Current (Single Half-cycle; T_{vj} (initial) = $25\text{ }^{\circ}\text{C}$)	$f = 50\text{ Hz}$	240	A
		$f = 60\text{ Hz}$	290	
I^2t	I^2t Value for Fusing	$t_p = 8.3\text{ ms}$	346	A^2s
di/dt	Critical Rate of Rise of On-state Current	$f = 60\text{ Hz}; T_{vj} = 125\text{ }^{\circ}\text{C}$	150	$\text{A}/\mu\text{s}$
I_{GM}	Peak Gate Current	$T_{vj} = 125\text{ }^{\circ}\text{C}$	4	A
$P_{G(AV)}$	Average Gate Power Dissipation	$T_{vj} = 125\text{ }^{\circ}\text{C}$	0.5	W
T_{stg}	Storage Temperature Range	–	–40 to 150	$^{\circ}\text{C}$
T_{vj}	Operating Junction Temperature Range	–	–40 to 125	$^{\circ}\text{C}$
V_{DSM}/V_{RSM}	Non-repetitive Peak Off-state Voltage	Pulse width = $100\text{ }\mu\text{s}$	1400	V
V_{DRM}/V_{RRM}	Repetitive Peak Off-state Voltage	–	1200	V

Thermal Characteristics

Symbol	Characteristics	Value	Units
$R_{th(j-c)}$	Thermal Resistance, Junction to Case (AC)	Sxx30R/ Sxx30N	0.6 K/W

Electrical Characteristics ($T_{vj} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

Symbol	Characteristics	Conditions	Value			Units
			Min.	Typ.	Max.	
I_{GT}	DC Gate Trigger Current	$V_D = 12\text{ V}, R_L = 30\text{ }\Omega$	5	–	30	mA
V_{GT}	DC Gate Trigger Voltage	$V_D = 12\text{ V}, R_L = 30\text{ }\Omega$	–	0.75	1.3	V
V_{GD}	Gate Non-trigger Voltage	$V_D = V_{DRM}, R_L = 3.3\text{ k}\Omega, T_{vj} = 125\text{ }^{\circ}\text{C}$	0.2	–	–	V
I_H	Holding Current	$I_T = 400\text{ mA}$ (initial)	–	–	70	mA
$dv/dt_{(cr)}$	Critical Rate-of-rise of Off-stage Voltage	$V_D = 2/3 V_{DRM}$, Gate Open, $T_{vj} = 125\text{ }^{\circ}\text{C}$	1000	1500	–	$\text{V}/\mu\text{s}$
		$V_D = V_{DRM}$, Gate Open, $T_{vj} = 125\text{ }^{\circ}\text{C}$	500	1000	–	
t_q	Turn-off Time	$I_T = 2\text{ A}; t_p = 50\text{ }\mu\text{s}; dv/dt = 5\text{ V}/\mu\text{s}; di/dt = 30\text{ A}/\mu\text{s}$	–	25	35	μs
t_{gt}	Turn-on Time	$I_G = 2 \cdot I_{GT}, t_p = 15\text{ }\mu\text{s}, I_T = 60\text{ A}$	–	2.5	–	μs

Static Characteristics

Symbol	Characteristics	Conditions	Maximum Value	Units
V_{TM}	Peak On-state Voltage	$I_T = 60\text{ A}; t_p = 380\text{ }\mu\text{s}$	1.85	V
I_{DRM}/I_{RRM}	Off-state Current, Peak Repetitive	$T_{vj} = 25\text{ }^{\circ}\text{C}, V_D = V_{DRM}/V_{RRM}$	5	μA
		$T_{vj} = 125\text{ }^{\circ}\text{C}, V_D = V_{DRM}/V_{RRM}$	3	mA
$V_{(TO)}$	Threshold Voltage	$T_{vj} = 125\text{ }^{\circ}\text{C}$	0.98	V
r_T	Slope Resistance	$T_{vj} = 125\text{ }^{\circ}\text{C}$	18	$\text{m}\Omega$

Characteristic Curves

Fig. 1. Normalized DC Gate Trigger Current vs. Junction Temperature

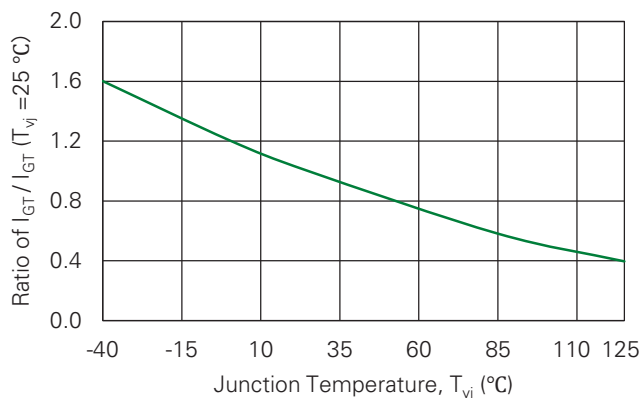


Fig. 2. Normalized DC Gate Trigger Voltage vs. Junction Temperature

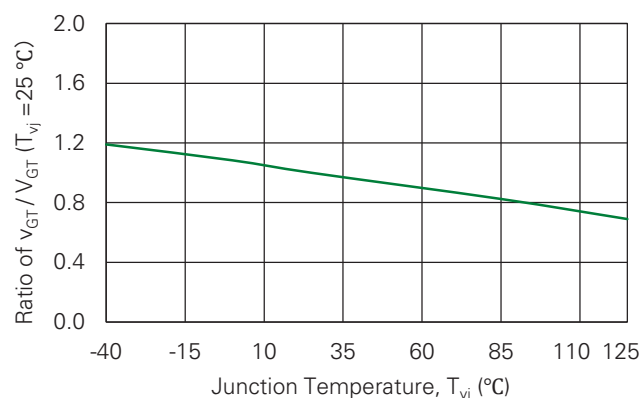


Fig. 3. Normalized DC Holding Current vs. Junction Temperature

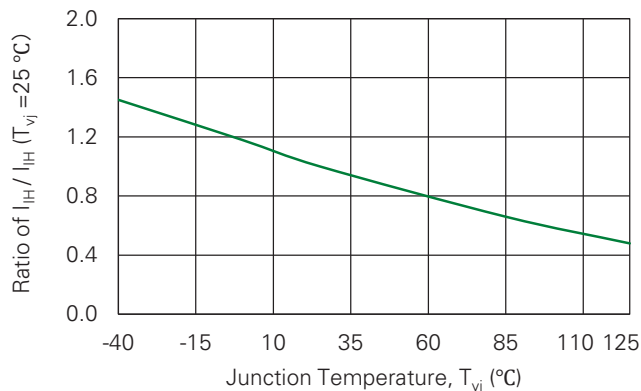


Fig. 4. Typical On-state Current vs. On-state Voltage

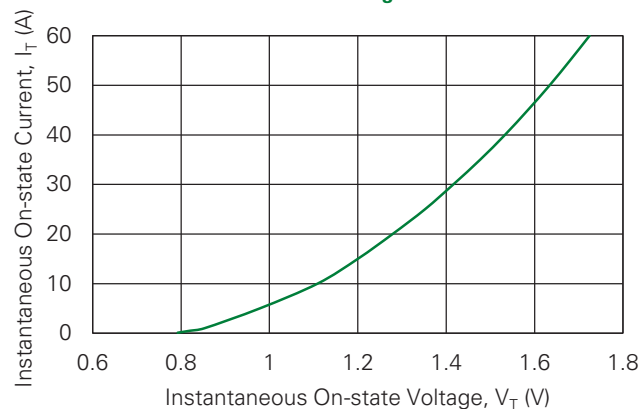


Fig. 5. Typical Power Dissipation vs. RMS On-state Current

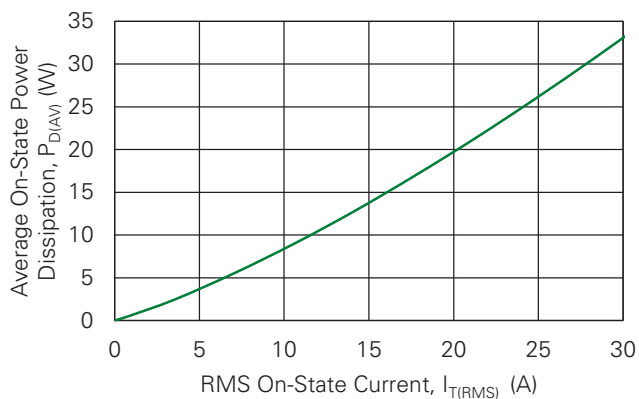


Fig. 6. Maximum Allowable Case Temperature vs. RMS On-state Current

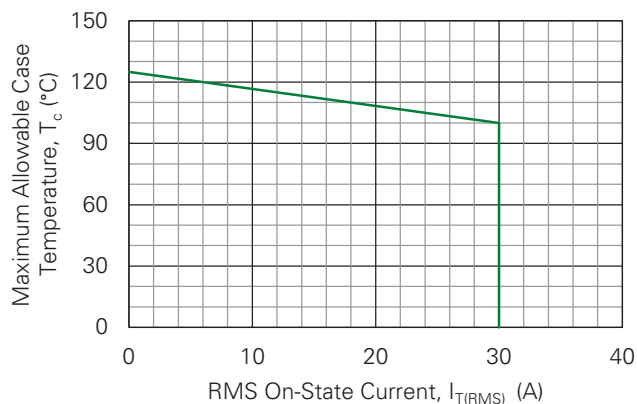


Fig. 7. Maximum Allowable Case Temperature vs. Average On-state Current

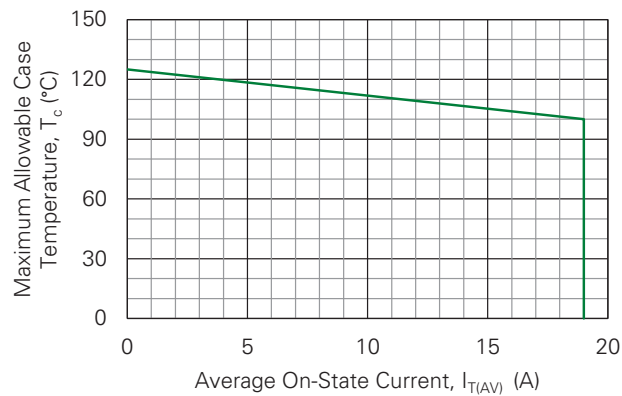
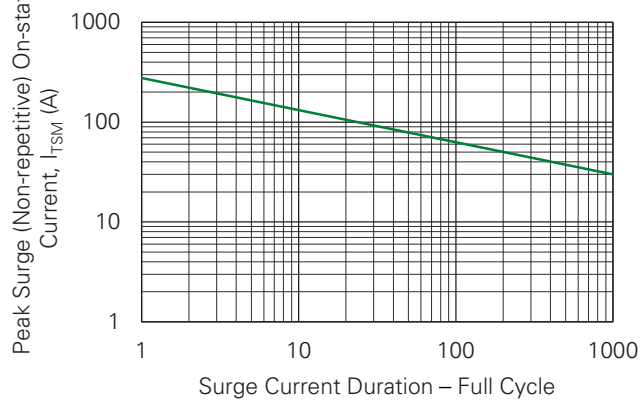


Fig. 8. Surge Peak On-state Current vs. Number of Cycles



Supply Frequency: 60 Hz Sinusoidal
Load: Resistive
RMS On-state, $I_{T(RMS)}$: Max. rated value at Specific Case Temperature

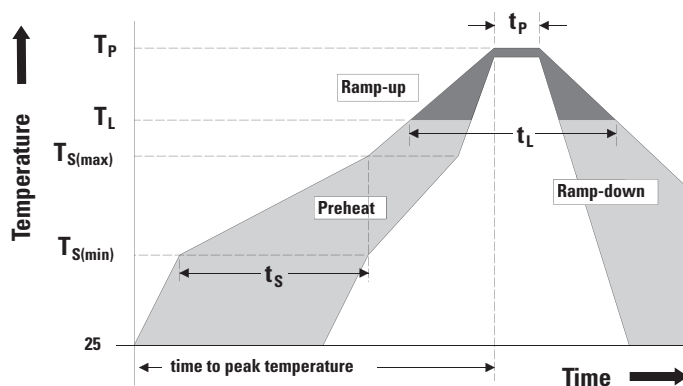
- Notes:
1. Gate control may be lost during and immediately following surge current interval.
 2. Overload may not be repeated until junction temperature has returned to steady-state rated value.

Soldering Parameters

Characteristic		Value
Reflow Condition		Pb – Free assembly
Pre-heat	Temperature Min ($T_{s(min)}$)	150 °C
	Temperature Max ($T_{s(max)}$)	200 °C
	Time (min to max) (t_s)	60–180 seconds
Average ramp up rate (Liquidus Temp) (T_L) to peak		5 K/second max.
$T_{s(max)}$ to T_L - Ramp-up Rate		5 K/second max.
Reflow	Temperature (T_L) (Liquidus)	217 °C
	Time (t_L)	60–150 seconds
Peak Temperature (T_P)		260 ^{+0/-5} °C
Time within 5 °C of actual peak Temperature (t_p)		20 – 40 seconds
Ramp-down Rate		5 K/second max.
Time 25 °C to peak Temperature (T_P)		8 minutes max.

Physical Specifications

Characteristic	Value
Terminal Finish	100% Matte Tin-plated
Body Material	UL Recognized Compound Meeting Flammability Rating V-0
Lead Material	Copper Alloy



Environmental Specifications

Test	Specifications and Conditions
AC Blocking	MIL-STD-750, M-1040, Cond A Applied Peak AC voltage @ 125°C for 1008 hours
Temperature/Humidity	EIA / JEDEC, JESD22-A101, 1008 hours; 320 V - DC: 85°C; 85% relative humidity
Temperature Cycling	MIL-STD-750, M-1051, 1000 cycles; -55°C to +150°C; 15-min dwell-time
Resistance to Solder Heat	MIL-STD-750: Method 2031
Solderability	ANSI/J-STD-002: category 3, Test A
Lead Bend	MIL-STD-750, M-2036 Cond E
High Temp Storage	MIL-STD-750, M-1031, 1008 hours, 150°C
Low Temp Storage	1008 hours, -40°C

Technical drawing of a 2N4350 JFET showing three views: front, side, and top.

Front View Dimensions:

- A: Anode width
- B: Anode height
- C: Anode radius
- D: Anode thickness
- E: Anode top edge
- F: Cathode height
- G: Cathode length
- H: Cathode width
- J: Anode to gate distance
- K: Cathode to gate distance
- L: Cathode to anode distance
- R: Gate width

Side View Dimensions:

- M: Gate lead length
- N: Gate lead width
- O: Anode lead length
- P: Anode lead width
- T_c MEASURING POINT: Anode lead centerline

Top View Dimensions:

- [8.13] .320: Anode lead width
- [13.36] .526: Anode lead length
- [7.01] .276: Cathode lead width
- AREA (REF) 0.17 IN²: Anode lead area

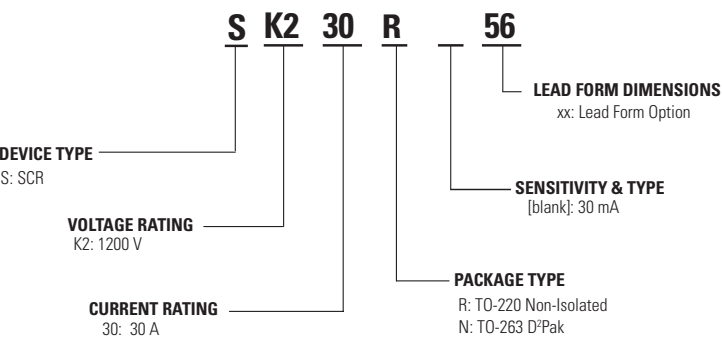
Labels:

- ANODE
- CATHODE
- GATE
- NOTCH IN GATE LEAD TO ID. NON-ISOLATED TAB

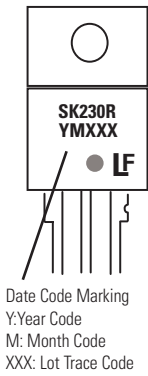
Symbol	Millimeters		Inches	
	Min.	Max.	Min.	Max
A	9.65	10.67	0.380	0.420
B	2.67	2.92	0.105	0.115
C	5.84	6.35	0.230	0.250
D	14.99	15.75	0.590	0.620
E	3.61	3.73	0.142	0.147
F	2.79	3.30	0.110	0.130
G	13.72	14.60	0.540	0.575
H	0.64	0.89	0.025	0.035
J	4.95	5.21	0.195	0.205
K	2.41	2.67	0.095	0.105
L	1.52	1.91	0.060	0.075
M	2.16	2.41	0.085	0.095
N	0.46	0.61	0.018	0.024
O	4.52	4.78	0.178	0.188
P	1.14	1.52	0.045	0.060
R	0.97	1.22	0.038	0.048

Symbol	Millimeters		Inches	
	Min.	Max.	Min.	Max
A	9.14	9.40	0.360	0.370
B	9.65	10.67	0.380	0.420
C	4.52	4.78	0.178	0.188
D	0.64	0.89	0.025	0.035
E	1.14	1.52	0.045	0.060
F	1.52	1.91	0.060	0.075
G	2.41	2.67	0.095	0.105
H	2.34	2.59	0.092	0.102
J	0.46	0.61	0.018	0.024
K	2.29	2.79	0.090	0.110
S	14.99	15.88	0.590	0.625
V	0.89	1.14	0.035	0.045
U	0.05	0.25	0.002	0.010
W	1.02	1.78	0.040	0.070

Part Numbering and Marking



TO-220 AB - (R Package)
TO-263 AB - (N Package)



Product Selector

Part Number	Voltage (1200 V)	Gate Sensitivity	Type	Package
Sxx30R	X	30 mA	Standard SCR	TO-220R
Sxx30N	X	30 mA	Standard SCR	TO-263

Note: xx = Voltage

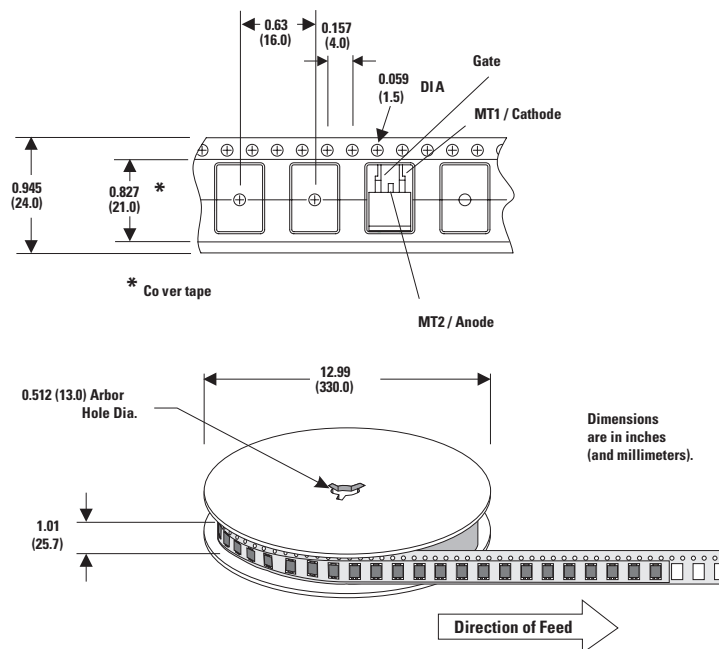
Packing Options

Part Number	Marking	Weight	Packing Mode	M.O.Q.
Sxx30RTP	Sxx30R	2.2 g	Tube Pack	1000 (50 per tube)
Sxx30NRP	Sxx30N	1.6 g	Embossed Carrier	500

Note: xx = Voltage

Reel Pack (RP) for TO-263 Embossed Carrier Specifications

Meets all EIA-481-2 Standards



Disclaimer Notice

Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littelfuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at <http://www.littelfuse.com/disclaimer-electronics>.



Part of:

