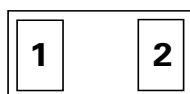


SC4340L-01ETG

4.0 V, 0.55 pF, 20 A, Low Clamping Voltage, Bidirectional TVS, Ultra Low Capacitance ESD protection



Pinout



Functional Block Diagram



Description

The SC4340L-01ETG provides ultra-low capacitance, bidirectional and a high level of protection for electronic equipment that may experience destructive electrostatic discharges (ESD). The typical capacitance of 0.55 pF helps ensure excellent signal integrity on the most challenging consumer electronics interfaces. It can safely absorb repetitive ESD strikes at ± 30 kV (contact discharge, IEC 61000-4-2) without performance degradation and safely dissipate 20 A of 8/20 μ s surge current (IEC 61000-4-5 2nd edition).

Features

- ESD, IEC 61000-4-2, ± 30 kV contact/air
- EFT, IEC 61000-4-4, 50 A (5/50 ns)
- Maximum surge tolerance, IEC 61000-4-5, 2nd edition, 20 A (8/20 μ s)
- Low leakage current of 50 nA (Max) at 4 V
- Ultra low capacitance of 0.55 pF (Typ @ $V_R = 0$ V)
- Small SOD882 packaging helps save board space
- Halogen-free, lead-free and RoHS compliant
- Moisture Sensitivity Level (MSL-1)
- AEC-Q101 & AEC-Q006 qualified and PPAP capable

Applications

- 1G/2.5G/10G Ethernet
- Audio/A2B
- DisplayPort interface
- HDMI 2.0 data lines
- USB 2.0/3.0
- USB 3.1 Gen 1

Life Support Note:

Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.

SC4340L-01ETG

4.0 V, 0.55 pF, 20 A, Low Clamping Voltage, Bidirectional TVS, Ultra Low Capacitance ESD protection

Absolute Maximum Ratings

Symbol	Parameter	Value	Units
I_{PP}	Peak Current ($t_p = 8/20 \mu s$)	20	A
T_{OP}	Operating Temperature	-40 to 125	°C
T_{STOR}	Storage Temperature	-55 to 150	°C

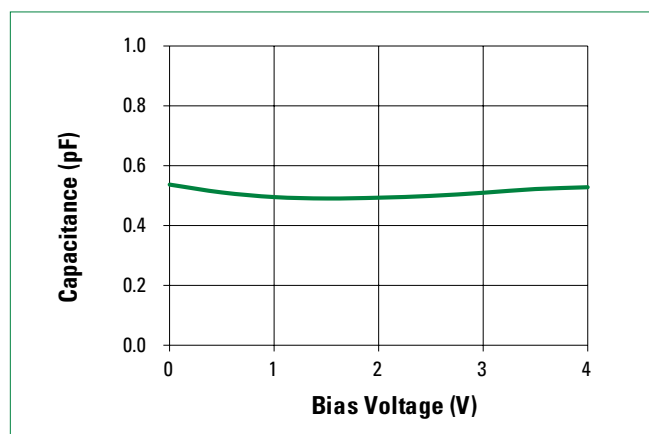
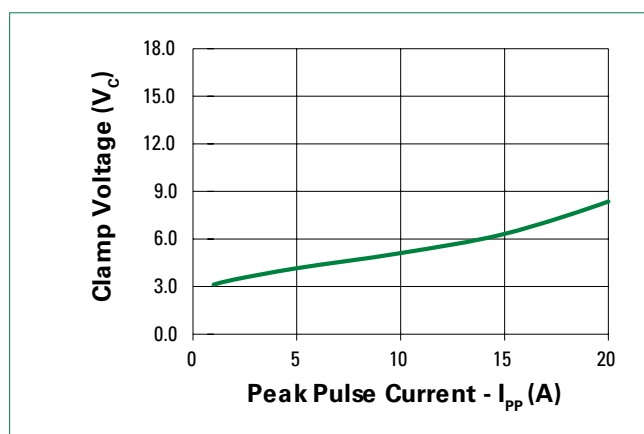
Caution: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the component. This is a stress only rating and operation of the component at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Electrical Characteristics ($T_{OP} = 25 \text{ }^\circ\text{C}$)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Reverse Standoff Voltage	V_{RWM}				4	V
Breakdown Voltage	V_{BR}	$I_R = 1 \text{ mA}$	5.5	6.5	8.5	V
Reverse Leakage Current	I_{LEAK}	$V_R = 4 \text{ V}$		25	50	nA
Clamp Voltage ¹	V_C	$I_{PP} = 16 \text{ A}$, $t_p = 0.2/100 \text{ ns}$ (TLP), I/O to GND		5.5		V
		$I_{PP} = 1 \text{ A}$, $t_p = 8/20 \mu s$, I/O to GND		3.1		V
		$I_{PP} = 10 \text{ A}$, $t_p = 8/20 \mu s$, I/O to GND		5.5		V
		$I_{PP} = 20 \text{ A}$, $t_p = 8/20 \mu s$, I/O to GND		8.5		V
Dynamic Resistance ²	R_{DYN}	TLP, $t_p = 100 \text{ ns}$, I/O to GND		0.17		Ω
ESD Withstand Voltage ^{1,3}	V_{ESD}	IEC 61000-4-2 (Contact Discharge)	± 30			kV
		IEC 61000-4-2 (Air Discharge)	± 30			kV
Diode Capacitance ¹	C_{IO-GND}	Reverse Bias = 0 V, $f = 1 \text{ MHz}$, I/O to GND		0.55	0.60	pF

Note:

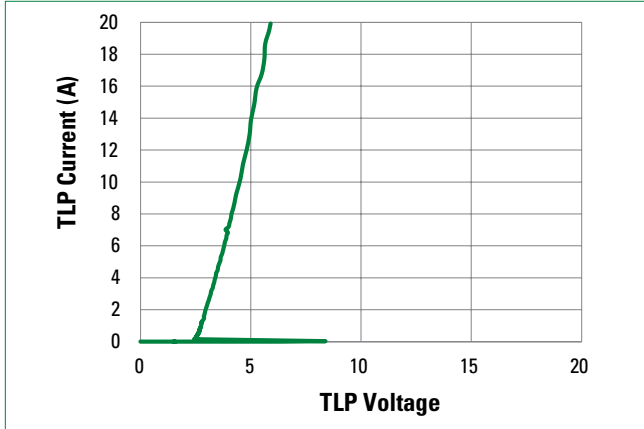
- Parameter is guaranteed by design and/or component characterization.
- Transmission Line Pulse (TLP) with 100 ns width, 0.2 ns rise time, and average window $t_1 = 70 \text{ ns}$ to $t_2 = 90 \text{ ns}$
- Device stressed with ten non-repetitive ESD pulses.

Capacitance vs. Reverse Bias**Clamping Voltage vs I_{PP}** 

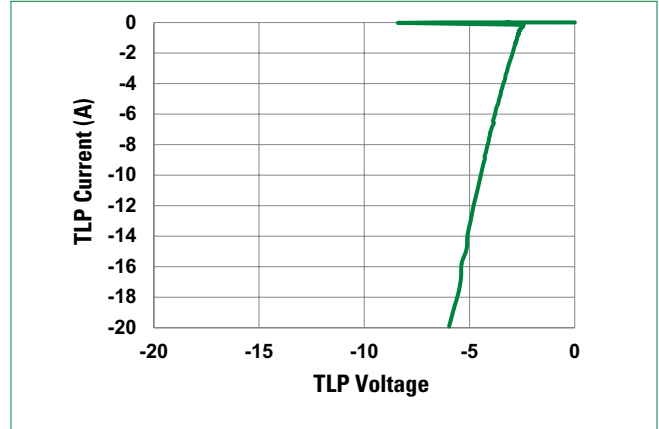
SC4340L-01ETG

4.0 V, 0.55 pF, 20 A, Low Clamping Voltage, Bidirectional TVS, Ultra Low Capacitance ESD protection

Positive Transmission Line Pulsing (TLP) Plot



Negative Transmission Line Pulsing (TLP) Plot



IEC 61000-4-2 +8 kV Contact ESD Clamping Voltage



IEC 61000-4-2 -8 kV Contact ESD Clamping Voltage

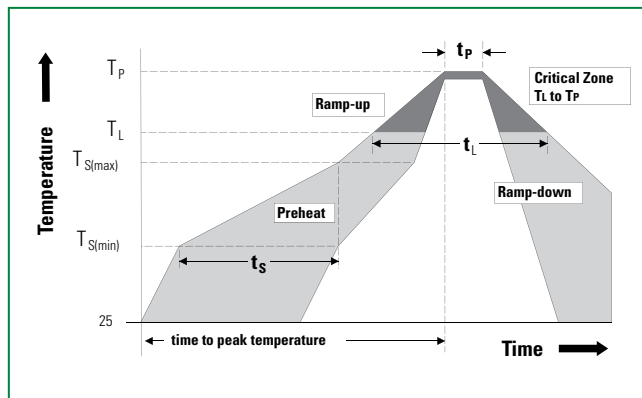


SC4340L-01ETG

4.0 V, 0.55 pF, 20 A, Low Clamping Voltage, Bidirectional TVS, Ultra Low Capacitance ESD protection

Soldering Parameters

Reflow Condition		Pb-free assembly
Pre Heat	- Temperature Min ($T_{s(min)}$)	150 °C
	- Temperature Max ($T_{s(max)}$)	200 °C
	- Time (min to max) (t_s)	60 – 120 seconds
Average Ramp Up Rate (Liquidus) Temp (T_L) to Peak		3 °C/second max
$T_{s(max)}$ to T_L - Ramp-up Rate		3 °C/second max
Reflow	- Temperature (T_L) (Liquidus)	217 °C
	- Temperature (t_L)	60 – 150 seconds
Peak Temperature (T_p)		260 ^{+0/-5} °C
Time Within 5 °C of Actual Peak Temperature (t_p)		30 seconds
Ramp-down Rate		6 °C/second max
Time 25 °C to Peak Temperature (T_p)		8 minutes max
Do Not Exceed		260 °C



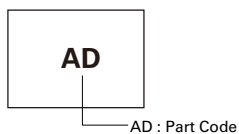
Ordering Information

Part Number	Package	Min. Order Qty.
SC4340L-01ETG	SOD882	10000

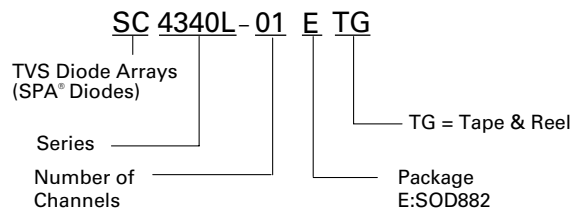
Product Characteristics

Lead Plating	Pre-Plated frame
Lead Material	Copper alloy
Body Material	Molded compound
Flammability	UL recognized compound meeting flammability rating V-0

Part Marking System

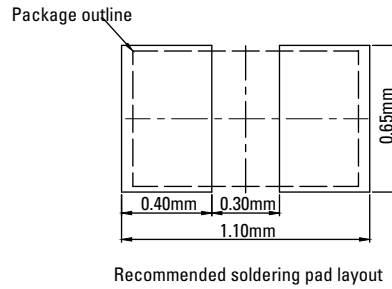
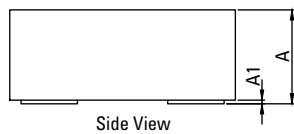
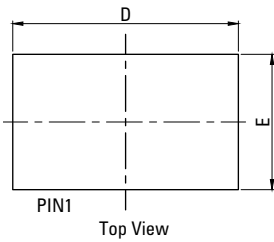


Part Numbering System

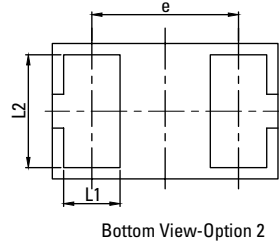
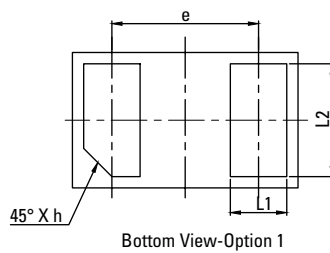
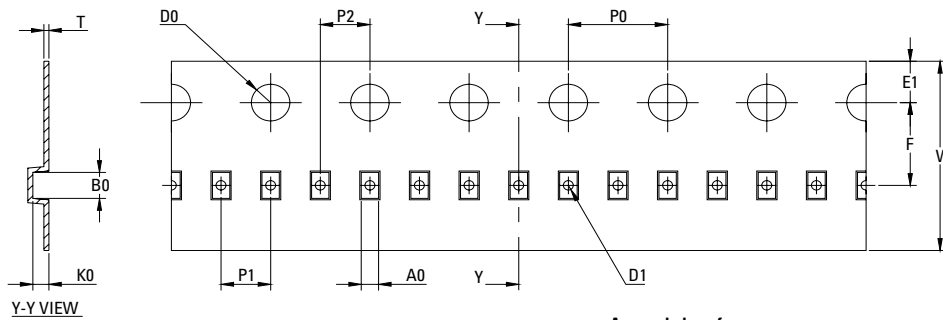


SC4340L-01ETG

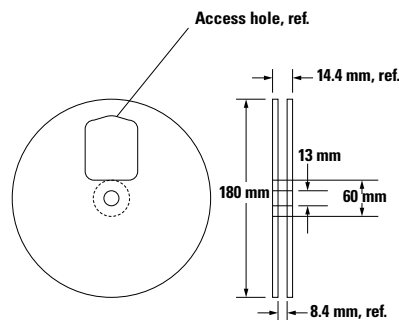
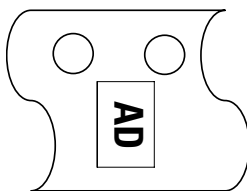
4.0 V, 0.55 pF, 20 A, Low Clamping Voltage, Bidirectional TVS, Ultra Low Capacitance ESD protection

Package Dimensions – SOD882

Symbol	Millimeters		
	Min	Nom	Max
A	0.45	0.50	0.55
A1	0.00	0.02	0.05
L1	0.20	0.25	0.30
L2	0.45	0.50	0.55
D	0.90	1.00	1.10
E	0.50	0.60	0.70
e	0.65 BSC		
h	0.07	0.12	0.17

**Embossed Carrier Tape & Reel Specification – SOD882**

Component Orientation in Tape



Symbol	Millimeters
A0	0.70+/-0.045
B0	1.10+/-0.045
D0	1.55+/-0.05
D1	0.40+/-0.05
E1	1.75+/-0.10
F	3.50+/-0.05
K0	0.65+/-0.045
P0	4.00+/-0.10
P1	2.00+/-0.10
P2	2.00+/-0.05
T	0.20+/-0.05
W	8.00+0.30/ -0.10

Disclaimer Notice - Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littelfuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at <http://www.littelfuse.com/disclaimer-electronics>.