

SP4340L-01WTG

4.0 V, 0.45 pF, 20 A, Low Clamping Voltage, Bidirectional TVS, Ultra Low Capacitance ESD protection

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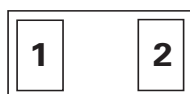
Description

The SP4340L-01WTG provides ultra-low capacitance, bidirectional and a high level of protection for electronic equipment that may experience destructive electrostatic discharges (ESD). The typical capacitance of 0.45 pF helps ensure excellent signal integrity on the most challenging consumer electronics interfaces. It can safely absorb repetitive ESD strikes at ± 30 kV (contact discharge, IEC 61000-4-2) without performance degradation and safely dissipate 20 A of 8/20 μ s surge current (IEC 61000-4-5 2nd edition).

Features

- ESD, IEC 61000-4-2, ± 30 kV contact/air
- EFT, IEC 61000-4-4, 50 A (5/50 ns)
- Maximum surge tolerance, IEC 61000-4-5, 2nd edition, 20 A (8/20 μ s)
- Low leakage current of 50 nA (Max) at 4 V
- Ultra low capacitance of 0.45 pF (Typ @ $V_R = 0$ V)
- Halogen-free, lead-free and RoHS compliant
- Moisture Sensitivity Level (MSL-1)

Pinout



Functional Block Diagram



Applications

- 1G/2.5G/10G Ethernet
- DisplayPort interface
- HDMI2.0 data lines
- LVDS
- USB2.0/3.0
- USB 3.1 Gen 1

Life Support Note:

Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.

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Absolute Maximum Ratings

Symbol	Parameter	Value	Units
I_{PP}	Peak Current ($t_p = 8/20 \mu s$)	20	A
T_{OP}	Operating Temperature	-40 to 125	°C
T_{STOR}	Storage Temperature	-55 to 150	°C

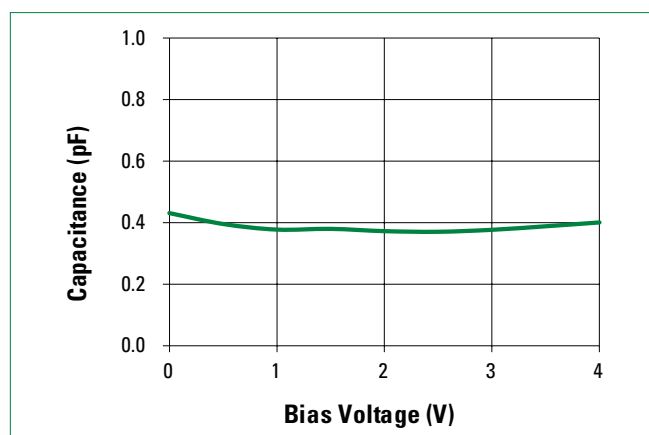
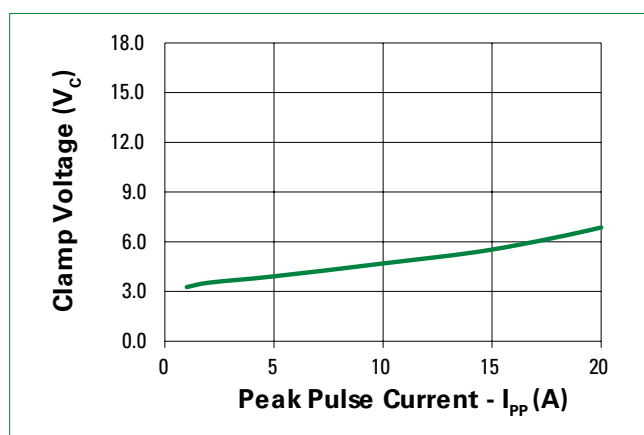
Caution: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the component. This is a stress only rating and operation of the component at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Electrical Characteristics ($T_{OP} = 25 \text{ }^\circ\text{C}$)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Reverse Standoff Voltage	V_{RWM}				4	V
Breakdown Voltage	V_{BR}	$I_R = 1 \text{ mA}$	5.5	6.5	8.5	V
Reverse Leakage Current	I_{LEAK}	$V_R = 4 \text{ V}$		1	50	nA
Clamp Voltage ¹	V_C	$I_{PP} = 16 \text{ A}$, $t_p = 0.2/100 \text{ ns}$ (TLP), I/O to GND		4.5		V
		$I_{PP} = 1 \text{ A}$, $t_p = 8/20 \mu s$, I/O to GND		3.1		V
		$I_{PP} = 10 \text{ A}$, $t_p = 8/20 \mu s$, I/O to GND		4.5		V
		$I_{PP} = 20 \text{ A}$, $t_p = 8/20 \mu s$, I/O to GND		6.5		V
Dynamic Resistance ²	R_{DYN}	TLP, $t_p = 100 \text{ ns}$, I/O to GND		0.1		Ω
ESD Withstand Voltage ^{1,3}	V_{ESD}	IEC 61000-4-2 (Contact Discharge)	± 30			kV
		IEC 61000-4-2 (Air Discharge)	± 30			kV
Diode Capacitance ¹	C_{IO-GND}	Reverse Bias = 0 V, $f = 1 \text{ MHz}$, I/O to GND		0.45	0.55	pF

Note:

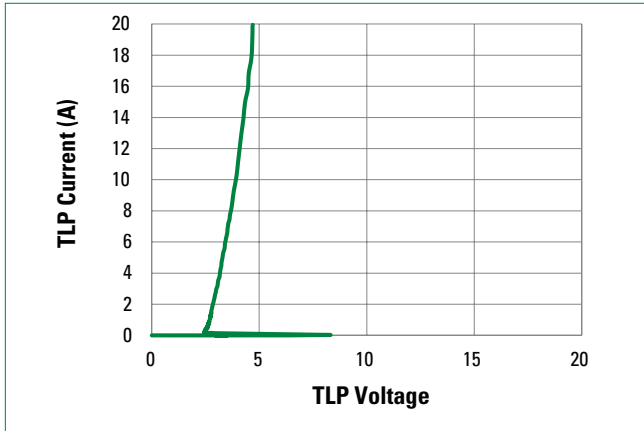
- Parameter is guaranteed by design and/or component characterization.
- Transmission Line Pulse (TLP) with 100ns width, 0.2ns rise time, and average window $t_1=70\text{ns}$ to $t_2=90\text{ns}$
- Device stressed with ten non-repetitive ESD pulses.

Capacitance vs. Reverse Bias**Clamping Voltage vs I_{PP}** 

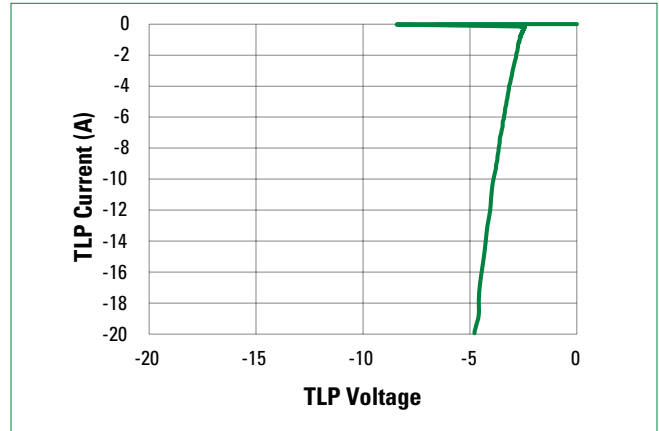
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Positive Transmission Line Pulsing (TLP) Plot



Negative Transmission Line Pulsing (TLP) Plot



IEC 61000-4-2 +8 kV Contact ESD Clamping Voltage



IEC 61000-4-2 -8 kV Contact ESD Clamping Voltage

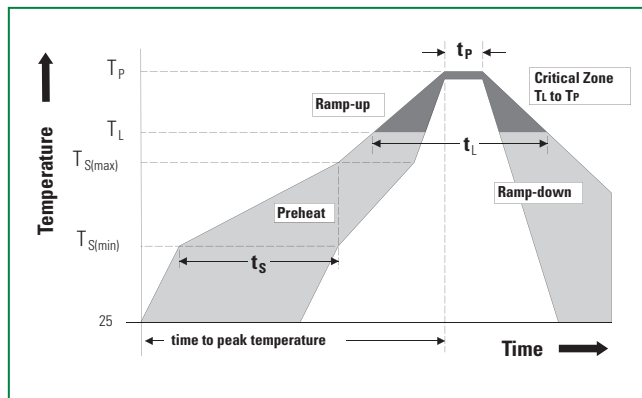


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Soldering Parameters

Reflow Condition		Pb-free assembly
Pre Heat	- Temperature Min ($T_{s(min)}$)	150 °C
	- Temperature Max ($T_{s(max)}$)	200 °C
	- Time (min to max) (t_s)	60 – 120 seconds
Average Ramp Up Rate (Liquidus) Temp (T_L) to Peak		3 °C/second max
$T_{s(max)}$ to T_L - Ramp-up Rate		3 °C/second max
Reflow	- Temperature (T_L) (Liquidus)	217°C
	- Temperature (t_L)	60 – 150 seconds
Peak Temperature (T_p)		260 ^{+0/-5} °C
Time Within 5 °C of Actual Peak Temperature (t_p)		30 seconds
Ramp-down Rate		6 °C/second max
Time 25 °C to Peak Temperature (T_p)		8 minutes max
Do Not Exceed		260 °C



Ordering Information

Part Number	Package	Min. Order Qty.
SP4340L-01WTG	Flipchip	10000

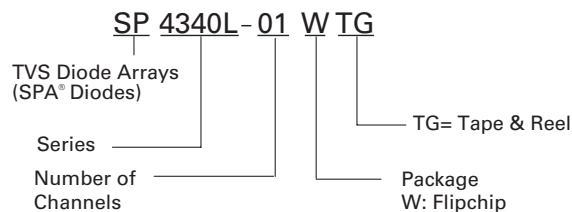
Product Characteristics

Lead Plating	Matte Tin Plating
Lead Material	Copper Bump
Flammability	UL recognized compound meeting flammability rating V-0

Part Marking System

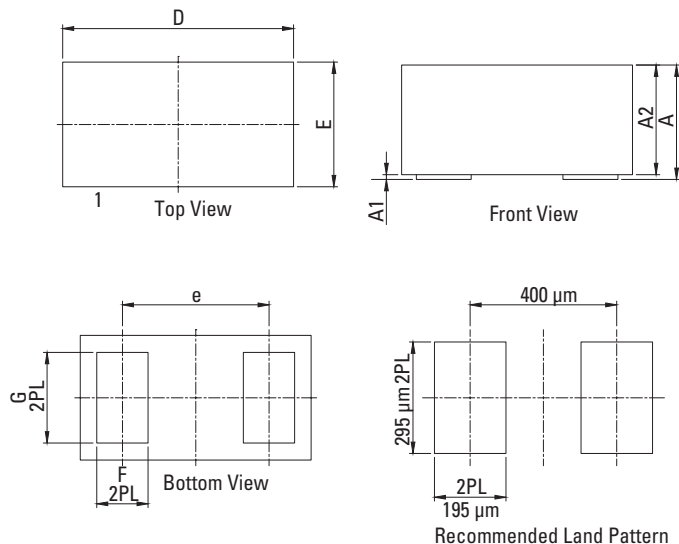


Part Numbering System

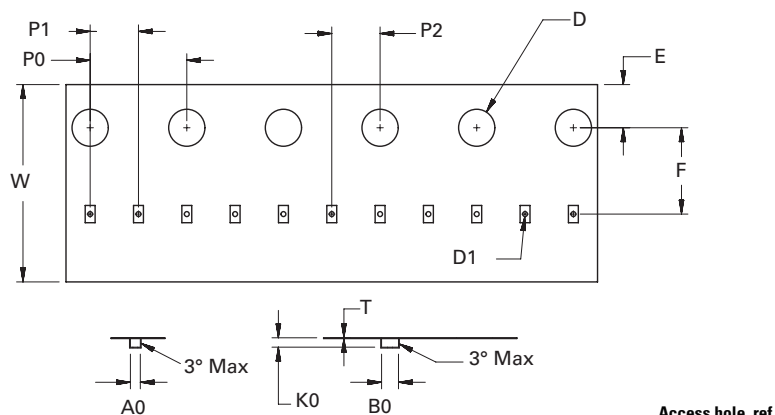


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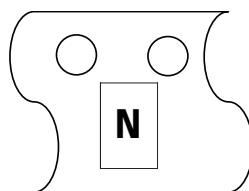
Package Dimensions – Flipchip

Symbol	Millimeters		
	Min	Mon	Max
A	0.273	0.301	0.329
A1	0.008	0.011	0.014
A2	0.265	0.290	0.315
e	0.400 BSC		
D	0.580	0.600	0.620
E	0.280	0.300	0.320
F	0.132	0.140	0.148
G	0.232	0.240	0.248

Embossed Carrier Tape & Reel Specification – Flipchip

Symbol	Millimeters
A0	0.40 +/-0.03
B0	0.70+/-0.03
D	ø 1.50 + 0.10
D1	ø 0.20 +/- 0.05
E	1.75+/-0.10
F	3.50+/-0.05
K0	0.34+/-0.03
P0	4.00+/-0.10
P1	2.00+/-0.05
P2	2.00+/-0.05
W	8.00+0.30/-0.10
T	0.20+/-0.02

Component Orientation in Tape



8 mm Tape and Reel

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