

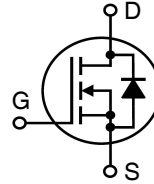
X2-Class HiperFET™ Power MOSFET

IXFP22N65X2M

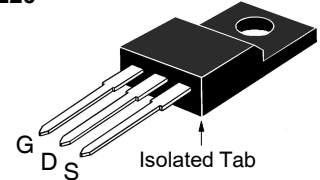
$$\begin{aligned} V_{DSS} &= 650V \\ I_{D25} &= 22A \\ R_{DS(on)} &\leq 145m\Omega \end{aligned}$$

(Electrically Isolated Tab)

N-Channel Enhancement Mode



OVERMOLDED
TO-220



G = Gate D = Drain
S = Source

Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	650	V
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C , $R_{GS} = 1M\Omega$	650	V
V_{GSS}	Continuous	± 30	V
V_{GSM}	Transient	± 40	V
I_{D25}	$T_C = 25^\circ\text{C}$, Limited by T_{JM}	22	A
I_{DM}	$T_C = 25^\circ\text{C}$, Pulse Width Limited by T_{JM}	44	A
I_A	$T_C = 25^\circ\text{C}$	5	A
E_{AS}	$T_C = 25^\circ\text{C}$	1	J
dv/dt	$I_S \leq I_{DM}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ\text{C}$	50	V/ns
P_D	$T_C = 25^\circ\text{C}$	37	W
T_J		-55 ... +150	$^\circ\text{C}$
T_{JM}		150	$^\circ\text{C}$
T_{stg}		-55 ... +150	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering	300	$^\circ\text{C}$
T_{SOLD}	1.6 mm (0.062in.) from Case for 10s	260	$^\circ\text{C}$
V_{ISOL}	50/60 Hz, 1 Minute	2500	V~
M_d	Mounting Torque	1.13 / 10	Nm/lb.in
Weight		2.5	g

Features

- International Standard Package
- Plastic Overmolded Tab
- High Voltage Package
- Low $R_{DS(ON)}$ and Q_G
- Avalanche Rated
- 2500V~ Electrical Isolation
- Low Package Inductance

Advantages

- High Power Density
- Easy to Mount
- Space Savings

Applications

- Switch-Mode and Resonant-Mode Power Supplies
- DC-DC Converters
- PFC Circuits
- AC and DC Motor Drives
- Robotics and Servo Controls

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV_{DSS}	$V_{GS} = 0V$, $I_D = 250\mu A$	650		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 1.5mA$	3.5		5.0 V
I_{GSS}	$V_{GS} = \pm 30V$, $V_{DS} = 0V$			± 100 nA
I_{DSS}	$V_{DS} = V_{DSS}$, $V_{GS} = 0V$ $T_J = 125^\circ\text{C}$			10 μA 1.5 mA
$R_{DS(on)}$	$V_{GS} = 10V$, $I_D = 11A$, Note 1			145 m Ω

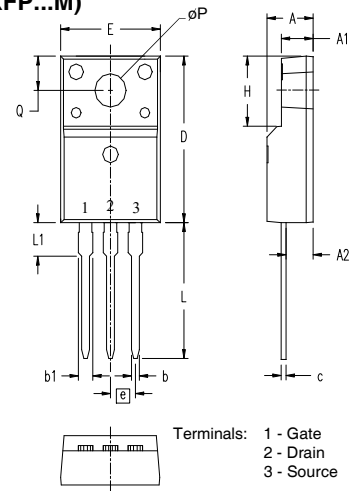
Symbol Test Conditions (T _J = 25°C, Unless Otherwise Specified)		Characteristic Values		
		Min.	Typ.	Max
g_{fs}	V _{DS} = 10V, I _D = 11A, Note 1	8	22	S
R_{Gi}	Gate Input Resistance		1.0	Ω
C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		2190	pF
C_{oss}			1450	pF
C_{rss}			1.3	pF
Effective Output Capacitance				
C_{o(er)}	Energy related	V_{GS} = 0V V_{DS} = 0.8 • V_{DSS}	92	pF
C_{o(tr)}	Time related		330	pF
t_{d(on)}	Resistive Switching Times V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 11A R_G = 10Ω (External)		30	ns
t_r			37	ns
t_{d(off)}			42	ns
t_f			18	ns
Q_{g(on)}	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 11A		37	nC
Q_{gs}			12	nC
Q_{gd}			14	nC
R_{thJC}			0.50	3.37 °C/W
R_{thCS}				°C/W

Source-Drain Diode

Symbol Test Conditions (T _J = 25°C, Unless Otherwise Specified)		Characteristic Values			
		Min.	Typ.	Max	
I _S	V _{GS} = 0V			22	A
I _{SM}	Repetitive, Pulse Width Limited by T _{JM}			88	A
V _{SD}	I _F = I _S , V _{GS} = 0V, Note 1			1.4	V
t _{rr}	I _F = 11A, -di/dt = 100A/μs V _R = 100V		145		ns
Q _{RM}			890		nC
I _{RM}			12		A

Note 1. Pulse test, $t \leq 300\mu s$, duty cycle, $d \leq 2\%$.

**OVERMOLDED TO-220
(IXFP...M)**



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.177	.193	4.50	4.90
A1	.092	.108	2.34	2.74
A2	.101	.117	2.56	2.96
b	.028	.035	0.70	0.90
b1	.050	.058	1.27	1.47
c	.018	.024	0.45	0.60
D	.617	.633	15.67	16.07
E	.392	.408	9.96	10.36
e	.100 BSC		2.54 BSC	
H	.255	.271	6.48	6.88
L	.499	.523	12.68	13.28
L1	.119	.135	3.03	3.43
ØP	.121	.129	3.08	3.28
Q	.126	.134	3.20	3.40

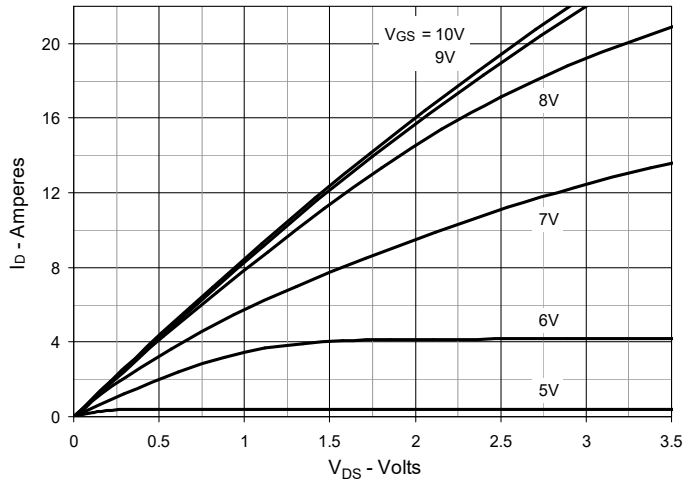
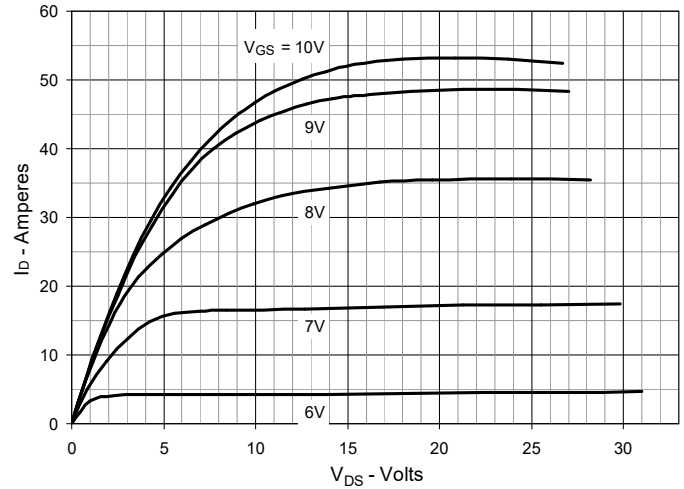
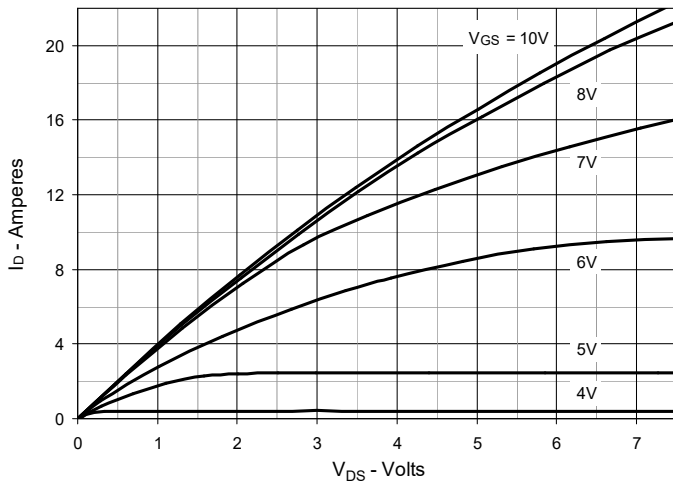
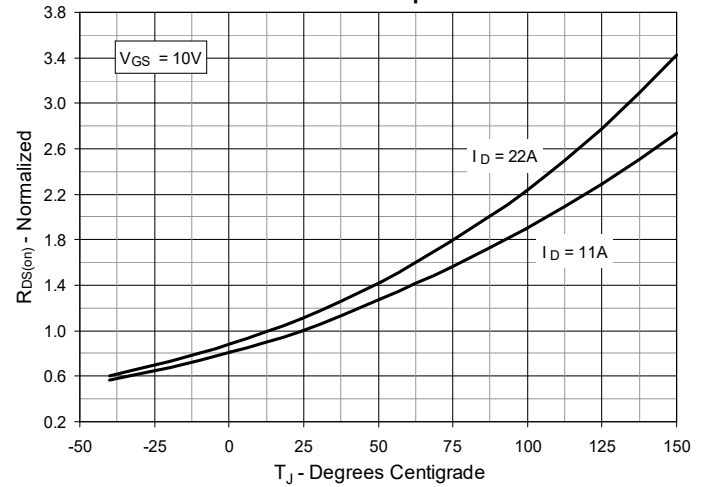
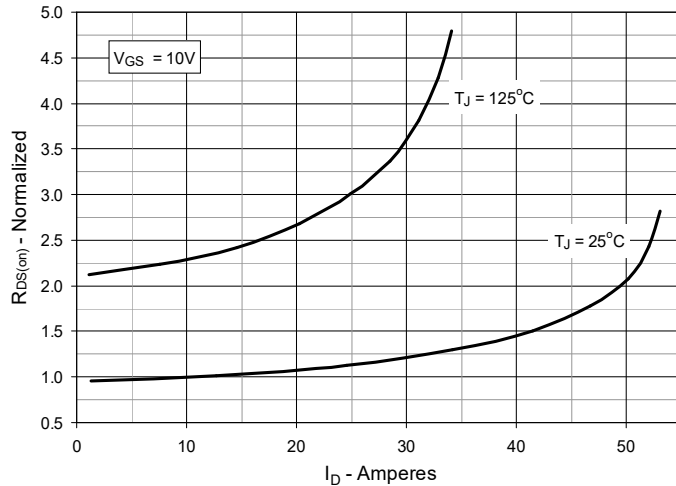
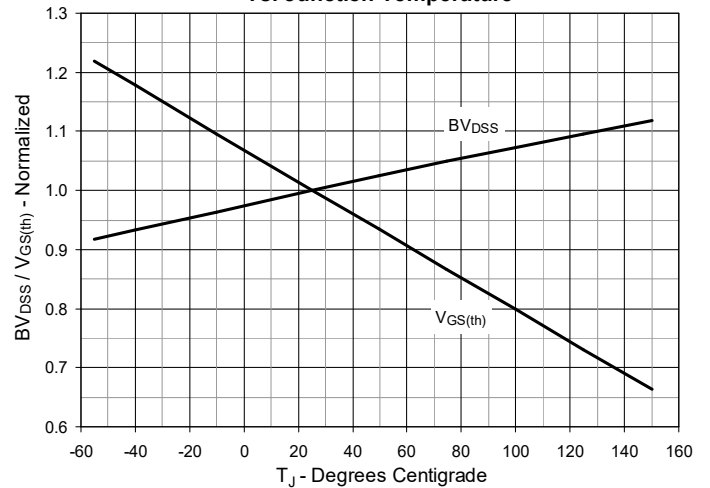
Fig. 1. Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 2. Extended Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 3. Output Characteristics @ $T_J = 125^\circ\text{C}$

Fig. 4. $R_{DS(on)}$ Normalized to $I_D = 11\text{A}$ Value vs. Junction Temperature

Fig. 5. $R_{DS(on)}$ Normalized to $I_D = 11\text{A}$ Value vs. Drain Current

Fig. 6. Normalized Breakdown & Threshold Voltages vs. Junction Temperature


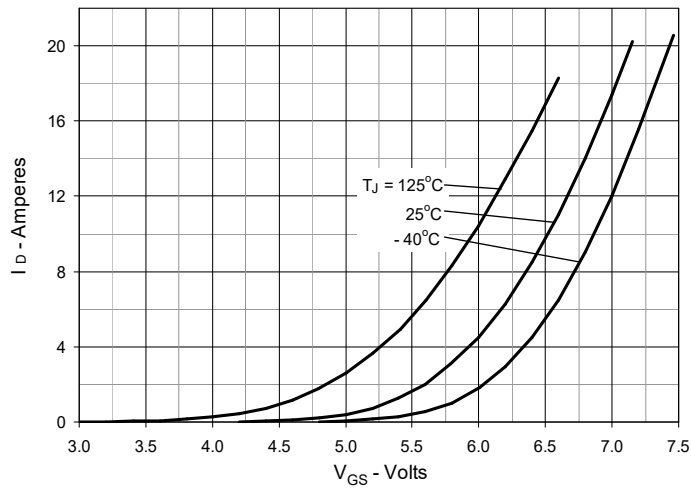
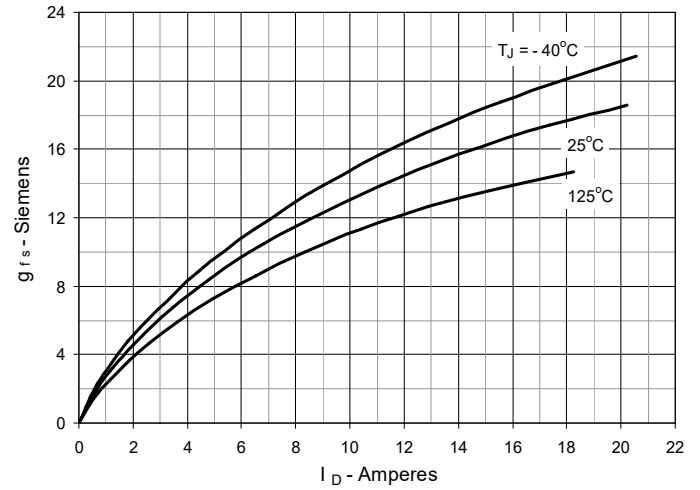
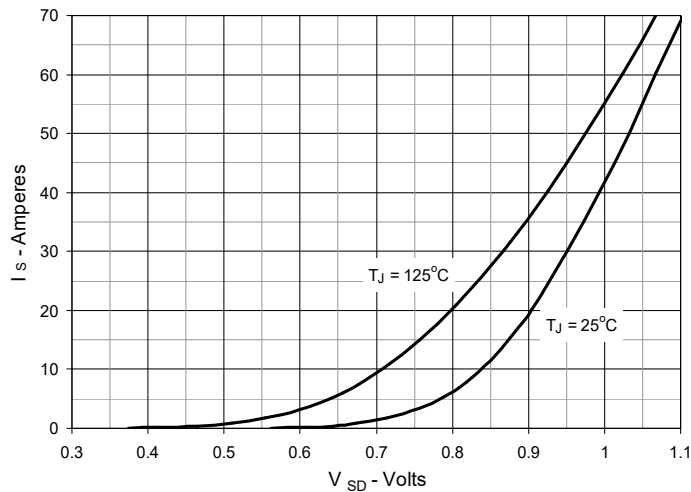
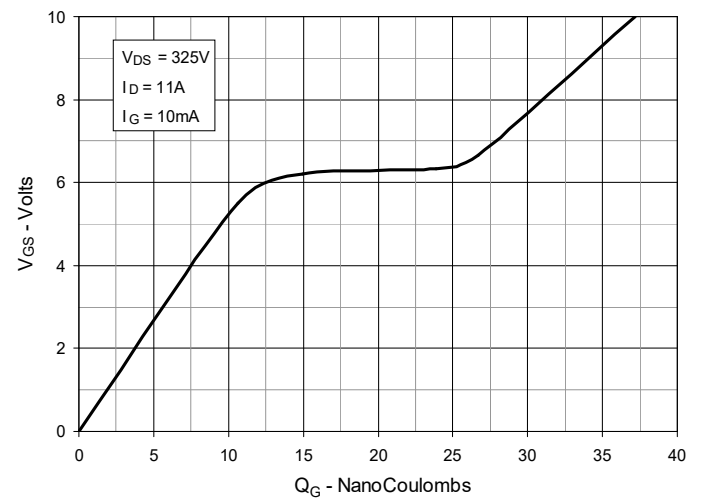
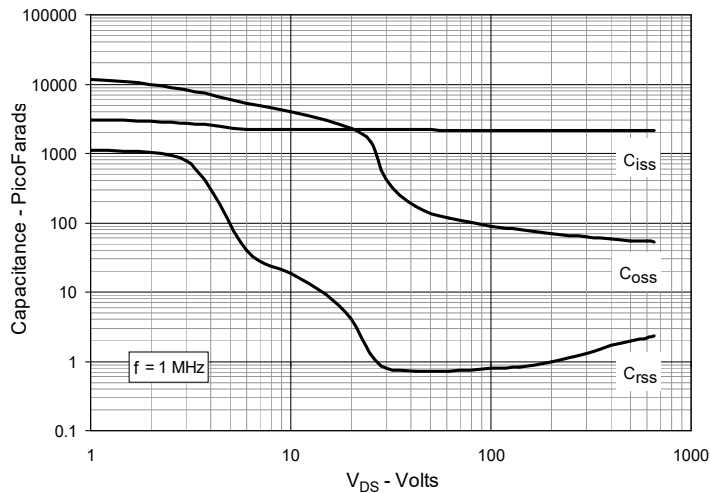
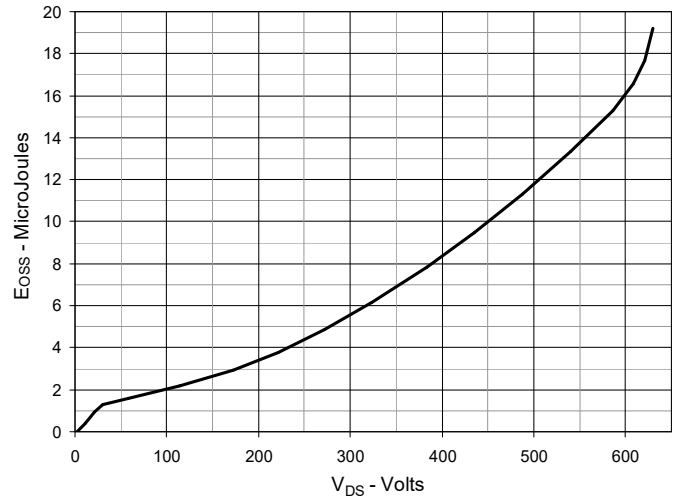
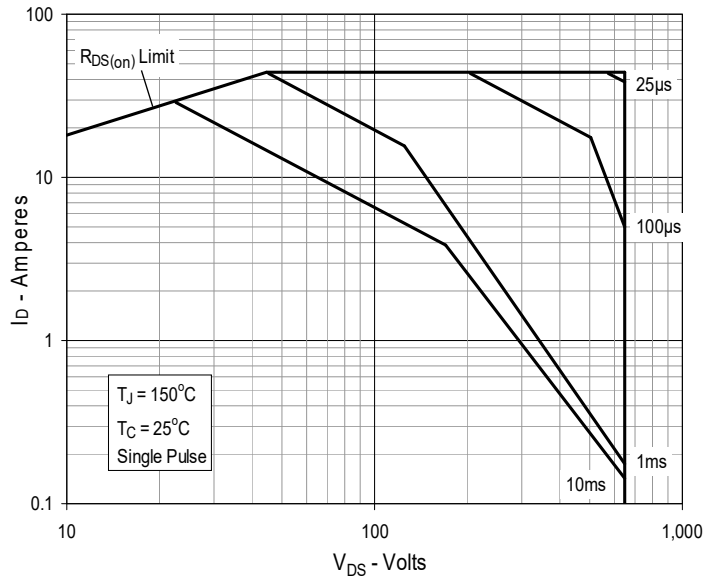
Fig. 7. Input Admittance

Fig. 8. Transconductance

Fig. 9. Forward Voltage Drop of Intrinsic Diode

Fig. 10. Gate Charge

Fig. 11. Capacitance

Fig. 12. Output Capacitance Stored Energy


Fig. 13. Forward-Bias Safe Operating Area

Fig. 14. Maximum Transient Thermal Impedance
