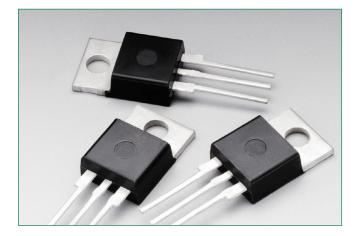
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## MCR12DG, MCR12MG, MCR12NG Silicon Controlled Rectifiers — 400V - 800V



## **Additional Information**







Resources

Accessories

Samples

## **Functional Diagram**



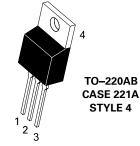
## Description

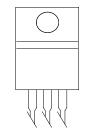
Designed primarily for half-wave ac control applications, such as motor controls, heating controls and power supplies; or wherever half-wave silicon gate-controlled, solid-state devices are needed.

Pin Out

## Features

- Blocking Voltage to 800 Volts
- On-State Current Rating of 12 Amperes RMS at 80°C
- High Surge Current Capability
  100 Amperes
- Rugged, Economical TO-220AB Package
- Glass Passivated Junctions for Reliability and Uniformity
- Minimum and Maximum Values of IGT, VGT an IH Specified for Ease of Design
- High Immunity to dv/dt 100 V/µsec Minimum at 125°C
- These are Pb–Free devices







#### Maximum Ratings (T<sub>1</sub> = 25°C unless otherwise noted)

Rating Part		Symbol	Value	Unit
Peak Repetitive Off-State Voltage (Note 1)MCR12DG(T_j = -40 to 125°C, Sine Wave, 50 to 60 Hz, Gate Open)MCR12MGMCR12NG		V <sub>drm,</sub> V <sub>rrm</sub>	400 600 800	V
On-State RMS Current (180° Conduction Angles; $T_c = 80^{\circ}C_c$	I <sub>T (RMS)</sub>	12	А	
Peak Non-repetitive Surge Current (1/2 Cycle, Sine Wave 60	I <sub>TSM</sub>	100	А	
Circuit Fusing Consideration ( $t = 8.3 \text{ ms}$ )	l²t	41	A <sup>2</sup> sec	
Forward Peak Gate Power (Pulse Width $\leq 1.0 \ \mu$ s, T <sub>c</sub> = 80°C)	P <sub>GM</sub>	5.0	W	
Forward Average Gate Power (t = 8.3 ms, $T_c = 80^{\circ}$ C)	P <sub>G (AV)</sub>	0.5	W	
Average On-State Current (180° Conduction Angles; $T_c = 80$	I <sub>T(AV)</sub>	7.8	А	
Forward Peak Gate Current (Pulse Width $\leq$ 1.0 s, T <sub>c</sub> = 90°C)	I <sub>GM</sub>	2.0	А	
Operating Junction Temperature Range	T,	-40 to +125	°C	
Storage Temperature Range	T <sub>sta</sub>	-40 to +150	°C	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. V<sub>DBM</sub> and V<sub>RBM</sub> for all types can be applied on a continuous basis. Batings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

#### **Thermal Characteristics**

Rating		Symbol	Value	Unit
Thermal Resistance,	Junction-to-Case (AC) Junction-to-Ambient	R <sub>ejc</sub> R <sub>eja</sub>	2.2 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds		TL	260	°C

## **Electrical Characteristics - OFF** ( $T_J = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
Peak Repetitive Forward or Reverse Blocking Current	T <sub>1</sub> = 25°C	I <sub>DRM</sub> ,	-	-	0.01	mΛ
( $V_{\rm D}$ = Rated $V_{\rm DRM}$ and $V_{\rm RRM}$ ; Gate Open)	T_ = 125°C	I	-	-	2.0	mA

#### **Electrical Characteristics - ON**

Characteristic	Symbol	Min	Тур	Max	Unit
Peak Forward On–State Voltage (Note 2) ( $I_{TM} = 24 \text{ A}$ )	V <sub>TM</sub>	_	-	2.2	V
Gate Trigger Current (Continuous dc) (V $_{\rm D}$ = 12 V, R $_{\rm L}$ = 100 $\Omega)$	I <sub>gt</sub>	2.0	8.0	20	mA
Holding Current (V $_{\rm D}$ = 12 V, Initiating Current = 200 mA, Gate Open)	I <sub>H</sub>	4.0	20	40	mA
Latch Current ( $V_D = 12 \text{ V}, I_G = 20 \text{ mA}$ )	I <sub>L</sub>	6.0	25	60	mA
Gate Trigger Voltage (Continuous dc) ( $V_D = 12$ Vdc, $R_L = 100 \Omega$ )	V <sub>gt</sub>	0.5	0.65	1.0	V

#### **Dynamic Characteristics**

Characteristic	Symbol	Min	Тур	Max	Unit
Critical Rate–of–Rise of Off-State Voltage ( $V_D = Rated V_{DRM}$ , Exponential Waveform, Gate Open, $T_J = 125^{\circ}C$ )	dv/dt	100	250	_	V/µs
Repetitive Critical Rate of Rise of On–State Current IPK = 50 A, Pw = 40 µsec, diG/dt = 1 A/µsec, Igt = 50 mA	di/dt	_	_	50	A/µs

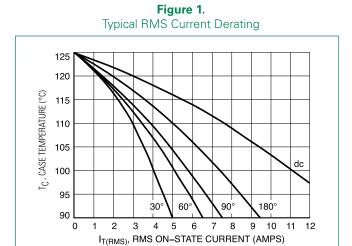
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 2. Indicates Pulse Test: Pulse Width  $\leq$  2.0 ms, Duty Cycle  $\leq$  2%



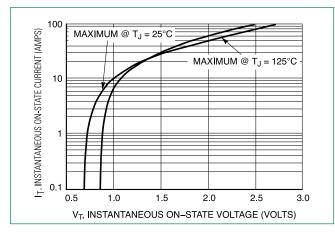
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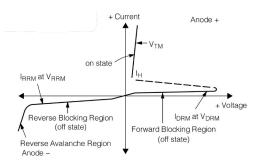
### Voltage Current Characteristic of SCR

Symbol	Parameter
V <sub>drm</sub>	Peak Repetitive Forward Off State Voltage
I <sub>DRM</sub>	Peak Forward Blocking Current
V <sub>RRM</sub>	Peak Repetitive Reverse Off State Voltage
I <sub>RRM</sub>	Peak Reverse Blocking Current
V <sub>TM</sub>	Maximum On State Voltage
I <sub>H</sub>	Holding Current

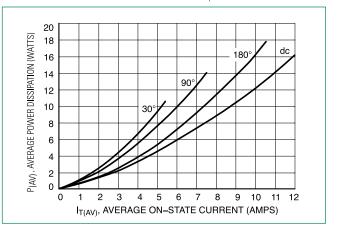


**Figure 3.** Typical On–State Characteristics

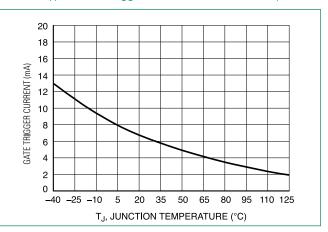




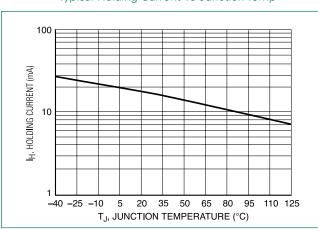
**Figure 2.** On-State Power Dissipation



**Figure 4.** Typical Gate Trigger Current vs Junction Temp

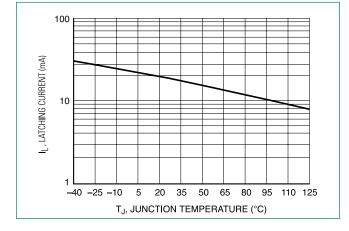


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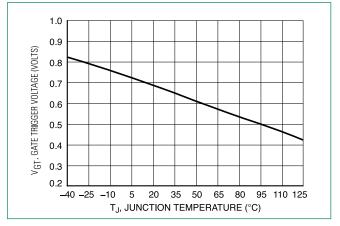


**Figure 5.** Typical Holding Current vs Junction Temp





**Figure 6.** Typical Gate Trigger Voltage vs Junction Temp

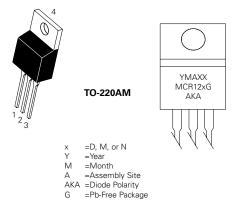


# MCR12DG, MCR12MG, MCR12NG Silicon Controlled Rectifiers — 400V - 800V

**Dimensions** 

## -T- SEATING PLANE в S-Q U ĸ 7 R .1

**Part Marking System** 



	Inches		Millin	neters	
Dim	Min	Max	Min	Мах	
А	0.590	0.620	14.99	15.75	
В	0.380	0.420	9.65	10.67	
С	0.178	0.188	4.52	4.78	
D	0.025	0.035	0.64	0.89	
F	0.142	0.147	3.61	3.73	
G	0.095	0.105	2.41	2.67	
Н	0.110	0.130	2.79	3.30	
J	0.018	0.024	0.46	0.61	
К	0.540	0.575	13.72	14.61	
L	0.060	0.075	1.52	1.91	
Ν	0.195	0.205	4.95	5.21	
Q	0.105	0.115	2.67	2.92	
R	0.085	0.095	2.16	2.41	
S	0.045	0.060	1.14	1.52	
Т	0.235	0.255	5.97	6.47	
U	0.000	0.050	0.00	1.27	
V	0.045		1.15		
Z		0.080		2.04	

Pin Assignment			
1	Cathode		
2	Anode		
3	Gate		
4	Anode		

### **Ordering Information**

Device	Package	Shipping
MCR12DG	70.0004.0	
MCR12MG	TO-220AB (Pb-Free)	1000 Units / Box
MCR12NG	(101100)	

1. Dimensioning and tolerancing per ansi y14.5m, 1982.

2. Controlling dimension: inch.

Dimension z defines a zone where all body and lead irregularities are allowed.

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