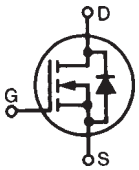
IXYS

PolarHV™ HiPerFET Power MOSFET

IXFH 18N60P
IXFV 18N60P
IXFV 18N60PS

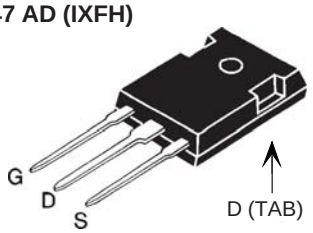
V_{DSS} = 600 V
I_{D25} = 18 A
$R_{DS(on)}$ ≤ 400 mΩ
t_{rr} ≤ 200 ns

N-Channel Enhancement Mode
Fast Intrinsic Diode
Avalanche Rated

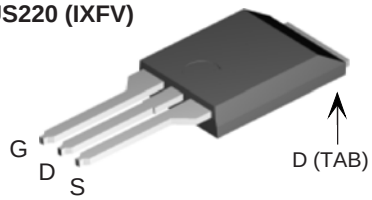


Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	T_J = 25°C to 150°C	600	V
V_{DGR}	T_J = 25°C to 150°C; R_{GS} = 1 MΩ	600	V
V_{GS}	Continuous	±30	V
V_{GSM}	Tranisent	±40	V
I_{D25}	T_C = 25°C	18	A
I_{DM}	T_C = 25°C, pulse width limited by T_{JM}	45	A
I_{AR}	T_C = 25°C	18	A
E_{AR}	T_C = 25°C	30	mJ
E_{AS}	T_C = 25°C	1.0	J
dv/dt	$I_S \le I_{DM}$, di/dt ≤ 100 A/µs, $V_{DD} \le V_{DSS}$, $T_J \le 150°C$, R_G = 5 Ω	10	V/ns
P_D	T_C = 25°C	360	W
T_J		-55 ... +150	°C
T_{JM}		150	°C
T_{stg}		-55 ... +150	°C
T_L	1.6 mm (0.062 in.) from case for 10 s	300	°C
T_{SOLD}	Plastic body for 10 s	260	°C
M_d	Mounting torque (TO-247)	1.13/10	Nm/lb.in.
Weight	TO-247	6	g
	PLUS220 & PLUS220SMD	4	g

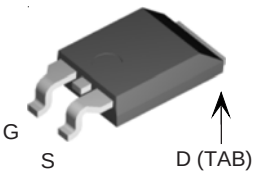
TO-247 AD (IXFH)



PLUS220 (IXFV)



PLUS220SMD (IXFV...S)



G = Gate D = Drain
S = Source TAB = Drain

Symbol (T_J = 25°C, unless otherwise specified)	Test Conditions	Characteristic Values Min.	Typ.	Max.	
BV_{DSS}	V_{GS} = 0 V, I_D = 250 µA	600			V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, I_D = 2.5 mA	3.0		5.5	V
I_{GSS}	V_{GS} = ±30 V, V_{DS} = 0 V			±100	nA
I_{DSS}	$V_{DS} = V_{DSS}$, V_{GS} = 0 V			25	µA
	T_J = 125°C			250	µA
$R_{DS(on)}$	V_{GS} = 10 V, $I_D = 0.5\ I_{D25}$, Pulse test, t ≤ 300 µs, duty cycle d ≤ 2 %			400	mΩ

Features

- International standard packages
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
  - easy to drive and to protect

Advantages

- Easy to mount
- Space savings
- High power density

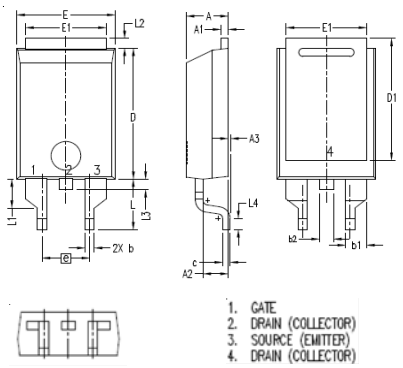
Symbol	Test Conditions	Characteristic Values (T_J = 25°C unless otherwise specified) Min.	Typ.	Max.	
g_{fs}	V_{DS} = 20 V; I_D = 0.5 I_{D25}, Note 1	9	16		S
C_{iss}	V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz		2500		pF
C_{oss}			280		pF
C_{rss}			23		pF
$t_{d(on)}$	V_{GS} = 10 V, V_{DS} = 0.5 V_{DSS}, I_D = I_{D25} R_G = 5 Ω (External)		21		ns
t_r			22		ns
$t_{d(off)}$			62		ns
t_f			22		ns
$Q_{g(on)}$	V_{GS} = 10 V, V_{DS} = 0.5 V_{DSS}, I_D = 0.5 I_{D25}		50		nC
Q_{gs}			15		nC
Q_{gd}			18		nC
R_{thJC}				0.35	°C/W
R_{thCS}	(TO-247, PLUS220)		0.21		°C/W

Source-Drain Diode

Symbol	Test Conditions	Characteristic Values (T_J = 25°C, unless otherwise specified) Min.	Typ.	Max.	
I_S	V_{GS} = 0 V			18	A
I_{SM}	Repetitive			54	A
V_{SD}	I_F = I_S, V_{GS} = 0 V, Note 1			1.5	V
t_{rr}	I_S = 18 A, -di/dt = 100 A/µs			200	ns
Q_{RM}	V_R = 100 V, V_{GS} = 0 V		0.8		µC
F_{RM}			5		A

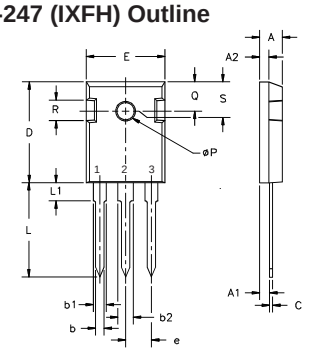
Note 1: Pulse test, t ≤ 300 µs, duty cycled ≤ 2 %

PLUS220SMD (IXFV_S) Outline



SYM	INCHES MIN	INCHES MAX	MILLIMETER MIN	MILLIMETER MAX
A	.169	.185	4.30	4.70
A1	.028	.035	0.70	0.90
A2	.098	.118	2.50	3.00
A3	.000	.010	0.00	0.25
b	.035	.047	0.90	1.20
b1	.080	.095	2.03	2.41
b2	.054	.064	1.37	1.63
c	.028	.035	0.70	0.90
D	.551	.591	14.00	15.00
D1	.512	.539	13.00	13.70
E	.394	.433	10.00	11.00
E1	.331	.346	8.40	8.80
e	.200BSC		5.08 BSC	
L	.209	.228	5.30	5.80
L1	.118	.138	3.00	3.50
L2	.035	.051	0.90	1.30
L3	.047	.059	1.20	1.50
L4	.039	.059	1.00	1.50

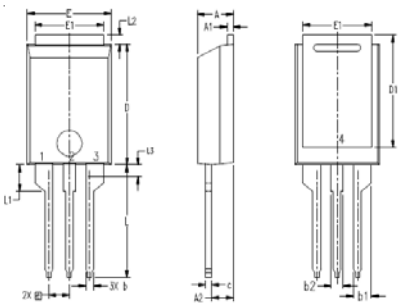
TO-247 (IXFH) Outline



Terminals: 1 - Gate 2 - Drain
3 - Source Tab - Drain

Dim.	Millimeter Min.	Millimeter Max.	Inches Min.	Inches Max.
A	4.7	5.3	.185	.209
A_1	2.2	2.54	.087	.102
A_2	2.2	2.6	.059	.098
b	1.0	1.4	.040	.055
b_1	1.65	2.13	.065	.084
b_2	2.87	3.12	.113	.123
C	.4	.8	.016	.031
D	20.80	21.46	.819	.845
E	15.75	16.26	.610	.640
e	5.20	5.72	0.205	0.225
L	19.81	20.32	.780	.800
L1		4.50		.177
∅P	3.55	3.65	.140	.144
Q	5.89	6.40	0.232	0.252
R	4.32	5.49	.170	.216
S	6.15 BSC		242 BSC	

PLUS220 (IXFV) Outline



SYM	INCHES MIN	INCHES MAX	MILLIMETER MIN	MILLIMETER MAX
A	.169	.185	4.30	4.70
A1	.028	.035	0.70	0.90
A2	.098	.118	2.50	3.00
b	.035	.047	0.90	1.20
b1	.080	.095	2.03	2.41
b2	.054	.064	1.37	1.63
c	.028	.035	0.70	0.90
D	.551	.591	14.00	15.00
D1	.512	.539	13.00	13.70
E	.394	.433	10.00	11.00
E1	.331	.346	8.40	8.80
e	.100 BSC		2.54 BSC	
L	.512	.551	13.00	14.00
L1	.118	.138	3.00	3.50
L2	.035	.051	0.90	1.30
L3	.047	.059	1.20	1.50

Fig. 1. Output Characteristics @ 25ºC

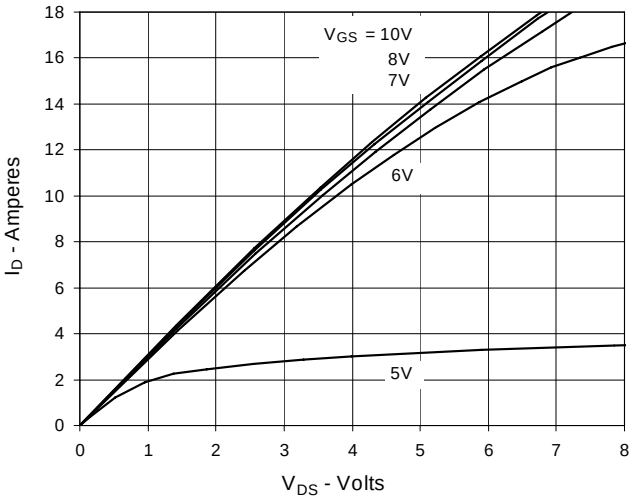


Fig. 2. Extended Output Characteristics @ 25ºC

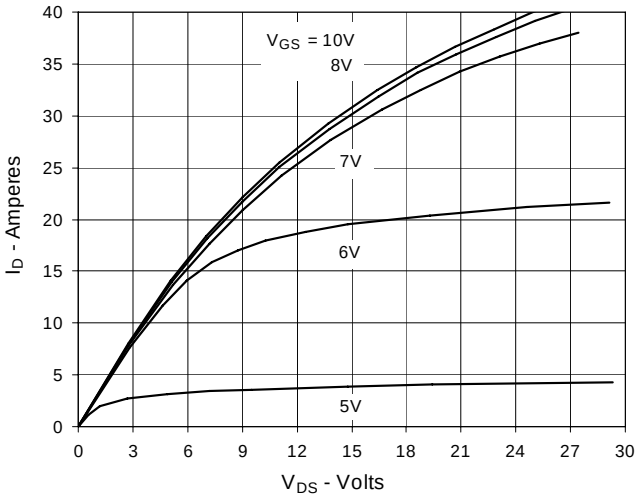


Fig. 3. Output Characteristics @ 125ºC

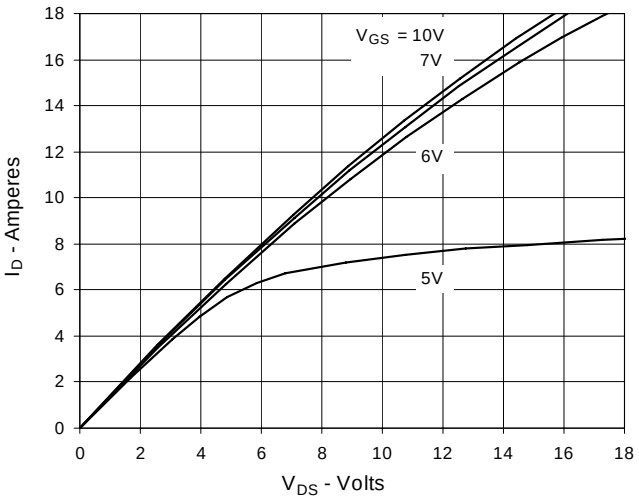


Fig. 4. $R_{DS(on)}$ Normalized to I_D = 9A Value vs. Junction Temperature

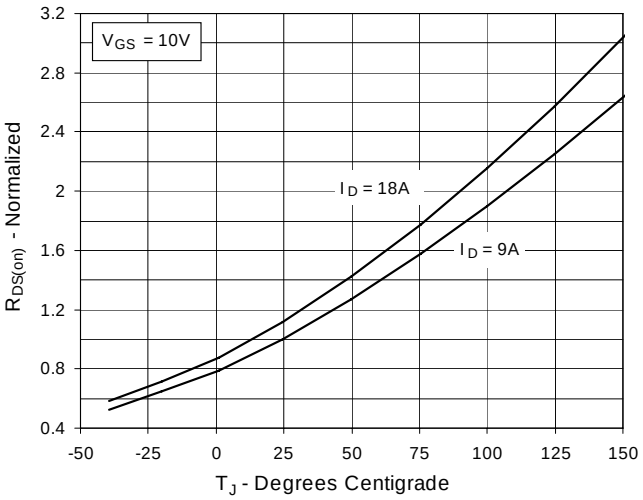


Fig. 5. $R_{DS(on)}$ Normalized to I_D = 9A Value vs. Drain Current

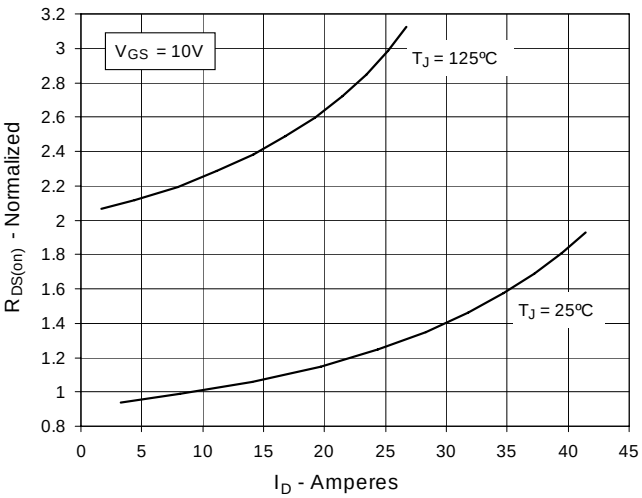


Fig. 6. Maximum Drain Current vs. Case Temperature

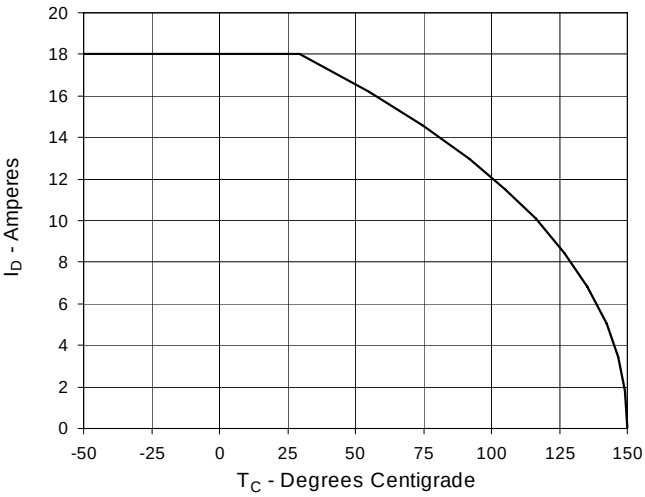


Fig. 7. Input Admittance

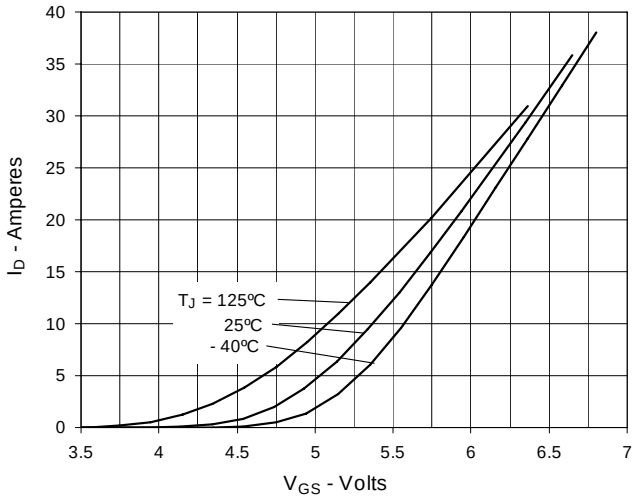


Fig. 8. Transconductance

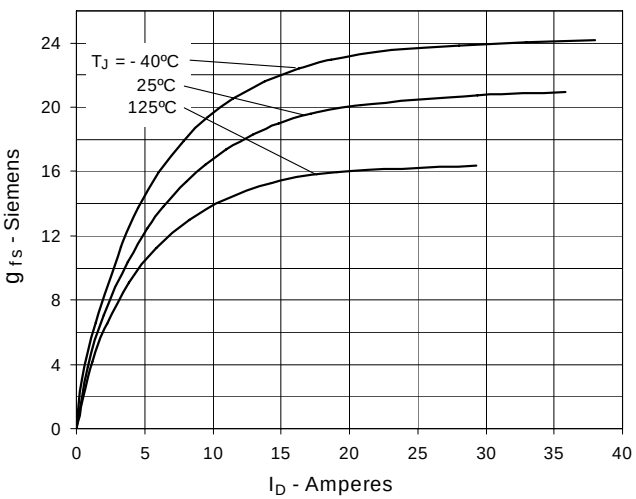


Fig. 9. Forward Voltage Drop of Intrinsic Diode

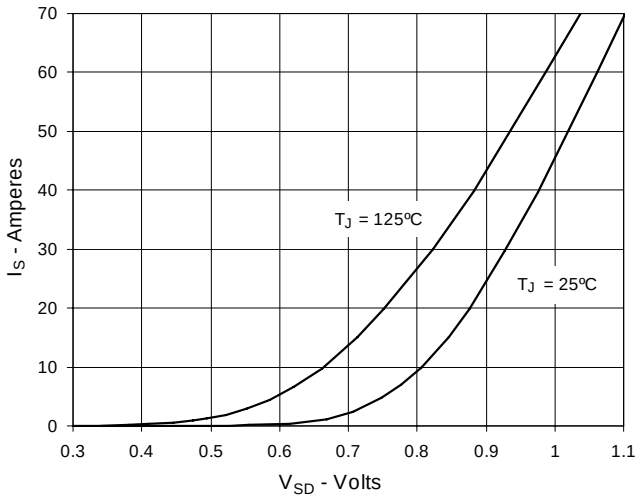


Fig. 10. Gate Charge

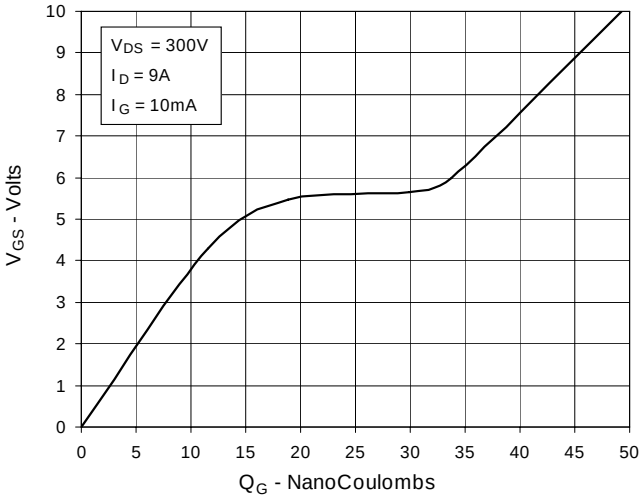


Fig. 11. Capacitance

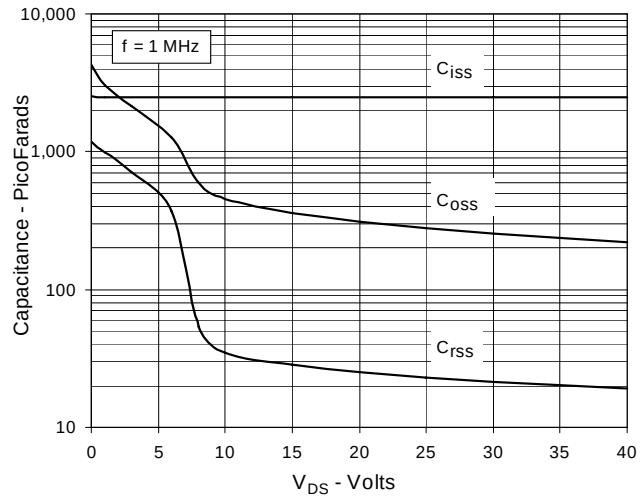


Fig. 12. Forward-Bias Safe Operating Area

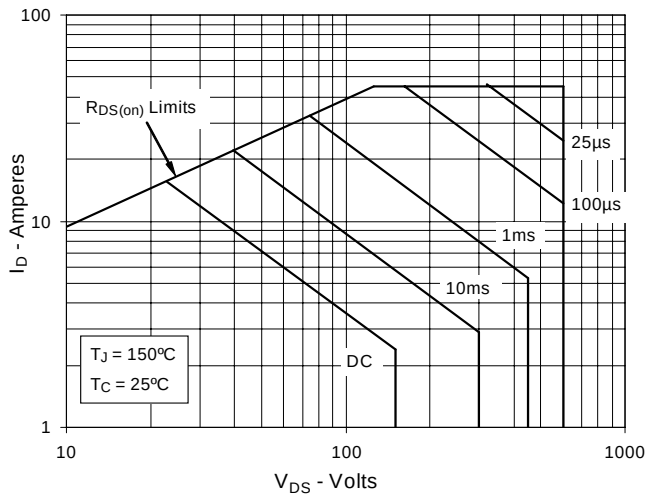


Fig. 13. Maximum Transient Thermal Resistance