The SP3004 has ultra low capacitance rail-to-rail diodes with an additional zener diode fabricated in a proprietary silicon avalanche technology to protect each I/O pin providing a high level of protection for electronic equipment that may experience destructive electrostatic discharges (ESD). These robust diodes can safely absorb repetitive ESD strikes at the maximum level (Level 4) specified in the IEC 61000-4-2 international standard without performance degradation. Their very low loading capacitance also makes them ideal for protecting high speed signal pins such as HDMI, DVI, USB2.0, and IEEE 1394.

**Features**

- RoHS compliant and Lead-free
- Low capacitance of 0.85pF (TYP) per I/O
- ESD protection of ±12kV contact discharge, ±15kV air discharge, (IEC 61000-4-2)
- EFT protection, IEC 61000-4-4, 40A (5/50ns)
- Low leakage of 10nA MAX with $V_{R}=3.3V$
- Small SOT563 package saves board space
- Lightning Protection, IEC 61000-4-5, 2nd Edition, 4A (8/20µs)
- AEC-Q101 qualified

**Applications**

- Computer Peripherals
- Mobile Phones
- PDA's
- Digital Cameras
- Network Hardware/Ports
- Test Equipment
- Medical Equipment

**Application Example**

A single 4 channel SP300x-04 device can be used to protect four of the data lines in a HDMI/DVI interface. Two (2) SP300x-04 devices provide protection for the main data lines. Low voltage ASIC HDMI/DVI drivers can also be protected with the SP300x-04, the $+V_{CC}$ pins on the SP300x-04 can be substituted with a suitable bypass capacitor or in some backdrive applications the $+V_{CC}$ of the SP300x-04 can be floated or NC.

Life Support Note:

*Not Intended for Use in Life Support or Life Saving Applications*

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.
## Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Value</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{PP}$</td>
<td>Peak Current ($t_p=8/20 \mu s$)</td>
<td>4</td>
<td>A</td>
</tr>
<tr>
<td>$T_{OP}$</td>
<td>Operating Temperature</td>
<td>–40 to 125</td>
<td>°C</td>
</tr>
<tr>
<td>$T_{STOR}$</td>
<td>Storage Temperature</td>
<td>–55 to 150</td>
<td>°C</td>
</tr>
</tbody>
</table>

**CAUTION:** Stresses above those listed in “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

## Electrical Characteristics ($T_{OP}=25 ^\circ C$)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Test Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Reverse Standoff Voltage</td>
<td>$V_{INM}$</td>
<td>$I_P \leq 1 \mu A$</td>
<td>6</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Reverse Leakage Current$^1$</td>
<td>$I_{LEAK}$</td>
<td>$V_P=3.3 V$</td>
<td>10</td>
<td>nA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Clamp Voltage$^1$</td>
<td>$V_C$</td>
<td>$I_{PP}=1 A$, $t_p=8/20 \mu s$, Fwd</td>
<td>10.0</td>
<td>12.0</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{PP}=2 A$, $t_p=8/20 \mu s$, Fwd</td>
<td>11.8</td>
<td>15.0</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>ESD Withstand Voltage$^1$</td>
<td>$V_{ESD}$</td>
<td>IEC 61000-4-2 (Contact)</td>
<td>±12</td>
<td>kV</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>IEC 61000-4-2 (Air)</td>
<td>±15</td>
<td>kV</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Dynamic Resistance</td>
<td>$R_{DYN}$</td>
<td>$(V_{O2} - V_{O1}) / (I_{PP2} - I_{PP1})$</td>
<td>1.8</td>
<td>Ω</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Diode Capacitance$^1$</td>
<td>$C_{O-GND}$</td>
<td>Reverse Bias=0V</td>
<td>0.95</td>
<td>1.1</td>
<td>1.25</td>
<td>pF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Reverse Bias=1.65V</td>
<td>0.7</td>
<td>0.85</td>
<td>1</td>
<td>pF</td>
</tr>
<tr>
<td>Diode Capacitance$^1$</td>
<td>$C_{O-I/O}$</td>
<td>Reverse Bias=0V</td>
<td>0.5</td>
<td>pF</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Note:** Parameter is guaranteed by design and/or device characterization.

## Insertion Loss (S21) I/O to GND

![Insertion Loss Graph](image)

## Capacitance vs. Bias Voltage

![Capacitance vs. Bias Voltage Graph](image)

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Revised: 09/06/19
TVS Diode Array (SPA® Diodes)
Low Capacitance ESD Protection - SP3004 Series

Capacitance vs. Frequency

Leakage Current vs. Temperature

Soldering Parameters

Reflow Condition
- Temperature Min (T_{S(min)})
- Temperature Max (T_{S(max)})
- Time to peak (t_{p})

Temperature Min (T_{S(min)})
- Preheat
- Reflow

Temperature Max (T_{S(max)})
- Peak Temperature (T_{L})
- Time within 5°C of peak (t_{r})

Average ramp up rate (Liquidus) Temp (T_{L}) to peak
3°C/second max

Ramp-up Rate
3°C/second max

Ramp-down Rate
6°C/second max

Reflow
- Temperature (T_{p}) (Liquidus)
- Temperature (t_{L})

Peak Temperature (T_{L})
260°±5 °C

Time within 5°C of actual peak (t_{r})
20 – 40 seconds

Time 25°C to peak Temperature (T_{L})
8 minutes Max.

Do not exceed
260°C

Product Characteristics

Lead Plating
- Pre-Plated Frame

Lead Material
- Copper Alloy

Lead Coplanarity
- 0.0004 inches (0.102mm)

Substitute Material
- Silicon

Body Material
- Molded Epoxy

Flammability
- UL 94 V-0

Notes:
1. All dimensions are in millimeters
2. Dimensions include solder plating.
3. Dimensions are exclusive of mold flash & metal burr.
4. Blo is facing up for mold and facing down for trim/form, i.e. reverse trim/form.
TVS Diode Array (SPA® Diodes)
Low Capacitance ESD Protection - SP3004 Series

Package Dimensions — SOT563

Part Numbering System

Part Marking System

Ordering Information

Embossed Carrier Tape & Reel Specification — SOT563

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