IXYS UK WESTCODE

Date:- 01 August 2012

Data Sheet Issue:- 3

Distributed Gate Thyristor

Type R0830LC12x to R0830LC14x

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{DRM}	Repetitive peak off-state voltage, (note 1)	1200-1400	V
V_{DSM}	Non-repetitive peak off-state voltage, (note 1)	1200-1400	V
V_{RRM}	Repetitive peak reverse voltage, (note 1)	1200-1400	V
V_{RSM}	Non-repetitive peak reverse voltage, (note 1)	1300-1500	V

	OTHER RATINGS		MAXIMUM LIMITS	UNITS
$I_{T(AV)M}$	Maximum average on-state current, T_{sink}=55°C, (note 2)		830	A
$I_{T(AV)M}$	Maximum average on-state current. T_{sink}=85°C, (note 2)		528	A
$I_{T(AV)M}$	Maximum average on-state current. T_{sink}=85°C, (note 3)		289	A
$I_{T(RMS)}$	Nominal RMS on-state current, T_{sink}=25°C, (note 2)		1713	A
$I_{T(d.c.)}$	D.C. on-state current, T_{sink}=25°C, (note 4)		1318	A
I_{TSM}	Peak non-repetitive surge t_p=10ms, V_{rm}=60%V_{RRM}, (note 5)		8500	A
I_{TSM2}	Peak non-repetitive surge t_p=10ms, V_{rm}≤10V, (note 5)		9350	A
I^2t	I^2t capacity for fusing t_p=10ms, V_{rm}=60%V_{RRM}, (note 5)		361×10^3	A^2s
I^2t	I^2t capacity for fusing t_p=10ms, V_{rm}≤10V, (note 5)		437×10^3	A^2s
$(di/dt)_{cr}$	Critical rate of rise of on-state current (note 6)	Single Shot	1500	A/µs
		Repetitive (50Hz, 60s)	1000	
		Continuous (50Hz)	500	
V_{RGM}	Peak reverse gate voltage		5	V
$P_{G(AV)}$	Mean forward gate power		2	W
P_{GM}	Peak forward gate power		30	W
$T_{j\,op}$	Operating temperature range		-40 to +125	°C
T_{stg}	Storage temperature range		-40 to +150	°C

Notes:-

1) De-rating factor of 0.13% per °C is applicable for T_j below 25°C.
2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
4) Double side cooled.
5) Half-sinewave, 125°C T_j initial.
6) V_D=67% V_{DRM}, I_{FG}=2A, t_r≤0.5µs, T_{case}=125°C.

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V_{TM}	Maximum peak on-state voltage	-	-	2.4	I_{TM}=1400A	V
V_{T0}	Threshold voltage	-	-	1.9		V
r_T	Slope resistance	-	-	0.357		mΩ
$(dv/dt)_{cr}$	Critical rate of rise of off-state voltage	200	-	-	V_D=80% V_{DRM}, Linear ramp, Gate o/c	V/µs
I_{DRM}	Peak off-state current	-	-	70	Rated V_{DRM}	mA
I_{RRM}	Peak reverse current	-	-	70	Rated V_{RRM}	mA
V_{GT}	Gate trigger voltage	-	-	3.0	T_j=25°C V_D=10V, I_T=3A	V
I_{GT}	Gate trigger current	-	-	300		mA
V_{GD}	Gate non-trigger voltage	-	-	0.25	Rated V_{DRM}	V
I_H	Holding current	-	-	1000	T_j=25°C	mA
t_{gd}	Gate controlled turn-on delay time		0.4	1.0	V_D=67% V_{DRM}, I_{TM}=1500A, di/dt=60A/µs, I_{FG}=2A, t_r=0.5µs, T_j=25°C	µs
t_{gt}	Turn-on time	-	0.8	2.0		
Q_{rr}	Recovered charge	-	285	-	I_{TM}=1000A, t_p=1000µs, di/dt=60A/µs, V_r=50V	µC
Q_{ra}	Recovered charge, 50% Chord	-	110	130		µC
I_{rm}	Reverse recovery current	-	95	-		A
t_{rr}	Reverse recovery time	-	2.7	-		µs
t_q	Turn-off time (note 2)	-	25	-	I_{TM}=1000A, t_p=1000µs, di/dt=60A/µs, V_r=50V, V_{dr}=33%V_{DRM}, dV_{dr}/dt=20V/µs	µs
		15	-	35	II$_{TM}$=1000A, t_p=1000µs, di/dt=60A/µs, V_r=50V, V_{dr}=33%V_{DRM}, dV_{dr}/dt=200V/µs	
R_{thJK}	Thermal resistance, junction to heatsink (note 3)	-	-	0.032	Double side cooled	K/W
		-	-	0.064	Single side cooled	K/W
F	Mounting force	10	-	20		kN
Wt	Weight	-	340	-	Outline option LC	g

Notes:-
1) Unless otherwise indicated T_j=125°C.
2) The required t_q (specified with dV_{dr}/dt=200V/µs) is represented by 'x' in the device part number. See ordering information for details of t_q codes.
3) For other clamp forces, please consult factory

Notes on Ratings and Characteristics

1.0 Voltage Grade Table

Voltage Grade	V_{DRM} V_{DSM} V	V_{RRM} V	V_{RSM} V	V_D DC V	V_R DC V
12	1200	1200	1300	1300	810
14	1400	1400	1500	1500	930

2.0 Extension of Voltage Grades

This report is applicable to other and higher voltage grades when supply has been agreed by Sales/Production.

3.0 Extension of Turn-off Time

This Report is applicable to other t_q/re-applied dv/dt combinations when supply has been agreed by Sales/Production.

4.0 Repetitive dv/dt

Higher dv/dt selections are available up to 1000V/µs on request.

5.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_j below 25°C.

6.0 Snubber Components

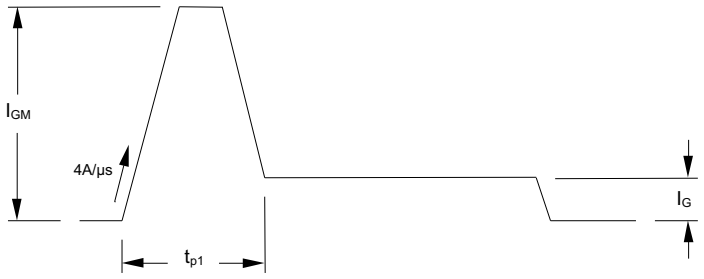
When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

7.0 Rate of rise of on-state current

The maximum un-primed rate of rise of on-state current must not exceed 1500A/µs at any time during turn-on on a non-repetitive basis. For repetitive performance, the on-state rate of rise of current must not exceed 1000A/µs at any time during turn-on. Note that these values of rate of rise of current apply to the total device current including that from any local snubber network.

8.0 Gate Drive

The nominal requirement for a typical gate drive is illustrated below. An open circuit voltage of at least 30V is assumed. This gate drive must be applied when using the full di/dt capability of the device.



The magnitude of I_{GM} should be between five and ten times I_{GT}, which is shown on page 2. Its duration (t_{p1}) should be 20µs or sufficient to allow the anode current to reach ten times I_L, whichever is greater. Otherwise, an increase in pulse current could be needed to supply the necessary charge to trigger. The 'back-porch' current I_G should remain flowing for the same duration as the anode current and have a magnitude in the order of 1.5 times I_{GT}.

9.0 Frequency Ratings

The curves illustrated in figures 10 to 18 are for guidance only and are superseded by the maximum ratings shown on page 1.

10.0 Square wave ratings

These ratings are given for load component rate of rise of forward current of 100 and 500 A/µs.

11.0 Duty cycle lines

The 100% duty cycle is represented on all the ratings by a straight line. Other duties can be included as parallel to the first.

12.0 Maximum Operating Frequency

The maximum operating frequency is set by the on-state duty, the time required for the thyristor to turn off (t_q) and for the off-state voltage to reach full value (t_v), i.e.

$$f_{\max} = \frac{1}{t_{pulse} + t_q + t_v}$$

13.0 On-State Energy per Pulse Characteristics

These curves enable rapid estimation of device dissipation to be obtained for conditions not covered by the frequency ratings.

Let E_p be the Energy per pulse for a given current and pulse width, in joules
Let R_{thJK} be the steady-state d.c. thermal resistance (junction to sink)
and T_K be the heat sink temperature.

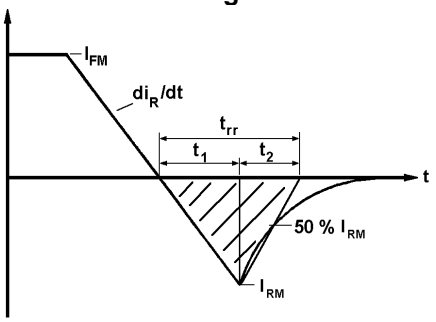
Then the average dissipation will be:

$$W_{AV} = E_P \cdot f \text{ and } T_{K(\max.)} = 125 - \left(W_{AV} \cdot R_{thJK}\right)$$

14.0 Reverse recovery ratings

(i) Q_{ra} is based on 50% I_{rm} chord as shown in Fig. 1

Fig. 1



(ii) Q_{rr} is based on a 150µs integration time i.e.

$$Q_{rr} = \int_0^{150\mu s} i_{rr}.dt$$

(iii)

$$K\ Factor = \frac{t_1}{t_2}$$

15.0 Reverse Recovery Loss

15.1 Determination by Measurement

From waveforms of recovery current obtained from a high frequency shunt (see Note 1, Page 5) and reverse voltage present during recovery, an instantaneous reverse recovery loss waveform must be constructed. Let the area under this waveform be E joules per pulse. A new heat sink temperature can then be evaluated from the following:

$$T_{K(new)} = T_{K(original)} - E \cdot \left(k + f \cdot R_{thJK}\right)$$

Where k=0.227 (°C/W)/s
E = Area under reverse loss waveform per pulse in joules (W.s.)
f = rated frequency Hz at the original heat sink temperature.
R_{thJK} = d.c. thermal resistance (°C/W).

The total dissipation is now given by:

$$W_{(TOT)} = W_{(original)} + E \cdot f$$

15.2 Determination without Measurement

In circumstances where it is not possible to measure voltage and current conditions, or for design purposes, the additional losses E in joules may be estimated as follows.

Let E be the value of energy per reverse cycle in joules (curves in Figure 9).
Let f be the operating frequency in Hz

$$T_{K(new)} = T_{K(original)} - \left(E \cdot R_{th} \cdot f\right)$$

Where $T_{K\,(new)}$ is the required maximum heat sink temperature and
$T_{K\,(original)}$ is the heat sink temperature given with the frequency ratings.

A suitable R-C snubber network is connected across the thyristor to restrict the transient reverse voltage to a peak value (V_{rm}) of 67% of the maximum grade. If a different grade is being used or V_{rm} is other than 67% of Grade, the reverse loss may be approximated by a pro rata adjustment of the maximum value obtained from the curves.

NOTE 1- Reverse Recovery Loss by Measurement

This thyristor has a low reverse recovered charge and peak reverse recovery current. When measuring the charge, care must be taken to ensure that:

(a) a.c. coupled devices such as current transformers are not affected by prior passage of high amplitude forward current.
(b) A suitable, polarised, clipping circuit must be connected to the input of the measuring oscilloscope to avoid overloading the internal amplifiers by the relatively high amplitude forward current signal
(c) Measurement of reverse recovery waveform should be carried out with an appropriate critically damped snubber, connected across diode anode to cathode. The formula used for the calculation of this snubber is shown below:

$$R^2 = 4 \cdot \frac{V_r}{C_S \cdot di/dt}$$

Where: V_r = Commutating source voltage
C_S = Snubber capacitance
R = Snubber resistance

16.0 Computer Modelling Parameters

16.1 Calculating V_T using ABCD Coefficients

The on-state characteristic I_T vs V_T, on page 7 is represented in two ways;
(i) the well established V_{T0} and r_T tangent used for rating purposes and
(ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V_T in terms of I_T given below:

$$V_T = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

The constants, derived by curve fitting software, are given in this report for hot and cold characteristics where possible. The resulting values for V_T agree with the true device characteristic over a current range, which is limited to that plotted.

125°C Coefficients	
A	2.41395901
B	-0.2538009
C	2.2821×10^{-4}
D	4.022639×10^{-3}

16.2 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}}\right)$$

Where $p = 1$ to n, n is the number of terms in the series.

t = Duration of heating pulse in seconds.
r_t = Thermal resistance at time t.
r_p = Amplitude of p_{th} term.
τ_p = Time Constant of r_{th} term.

D.C. Double Side Cooled				
Term	1	2	3	4
r_p	0.01771901	4.240625×10^{-3}	6.963806×10^{-3}	3.043661×10^{-3}
τ_p	0.7085781	0.1435833	0.03615196	2.130842×10^{-3}

D.C. Single Side Cooled					
Term	1	2	3	4	5
r_p	0.03947164	0.01022837	8.789912×10^{-3}	4.235162×10^{-3}	1.907609×10^{-3}
τ_p	4.090062	1.078983	0.08530917	0.01128791	1.240861×10^{-3}

Curves

Figure 1 - On-state characteristics of Limit device

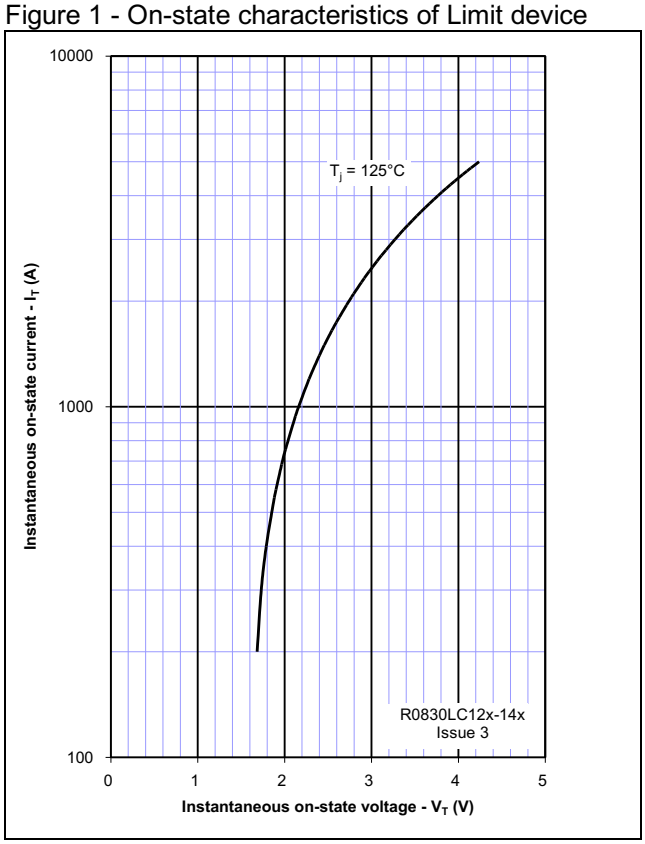


Figure 2 - Transient thermal impedance

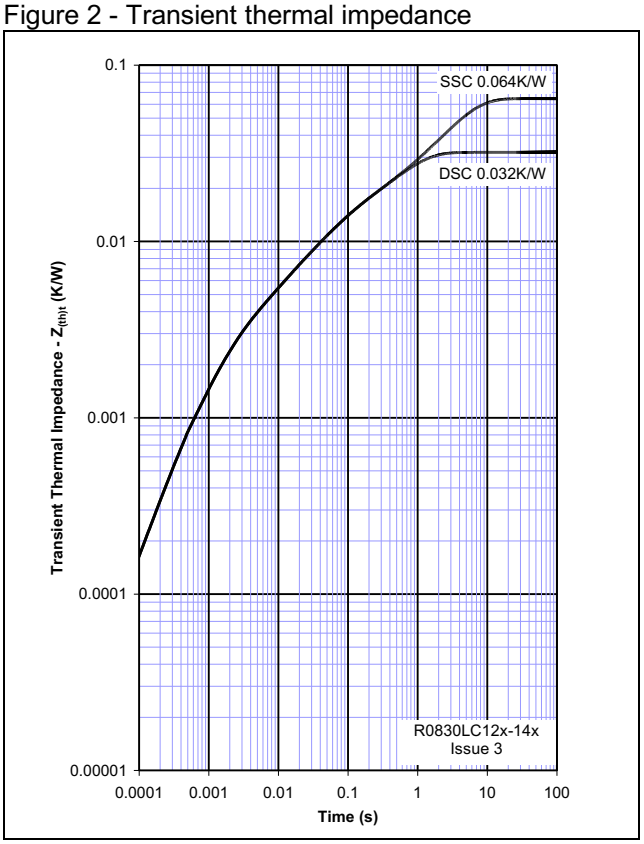


Figure 3 - Gate characteristics - Trigger limits

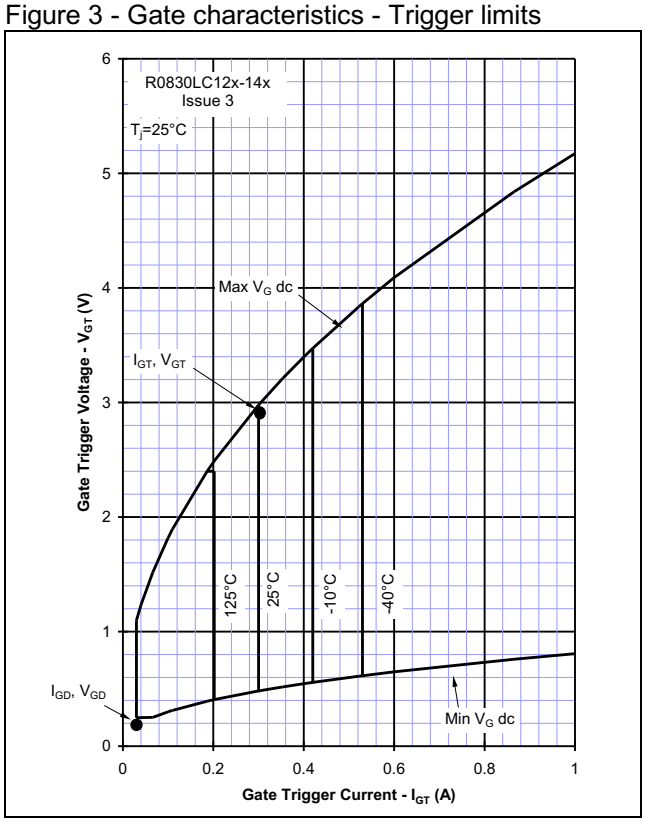


Figure 4 - Gate characteristics - Power curves

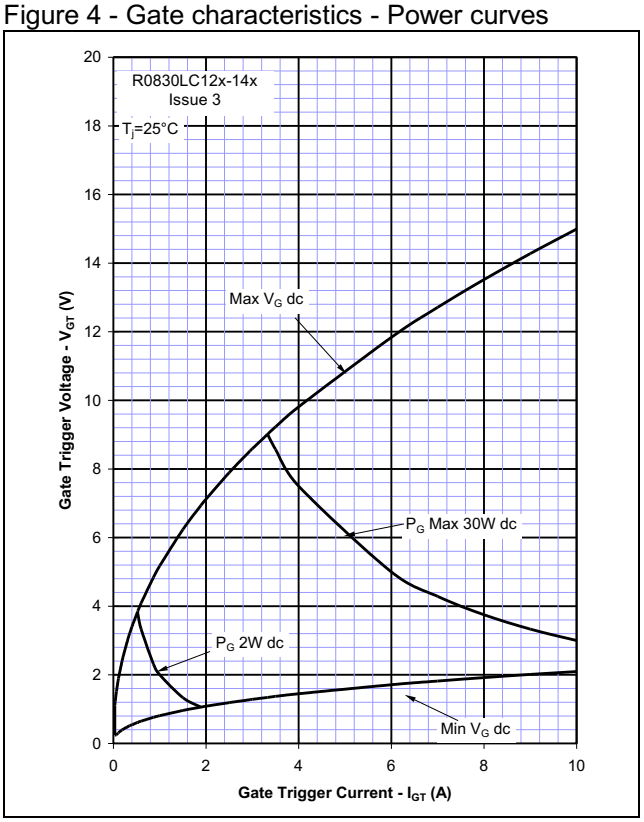


Figure 5 - Total recovered charge, Q_{rr}

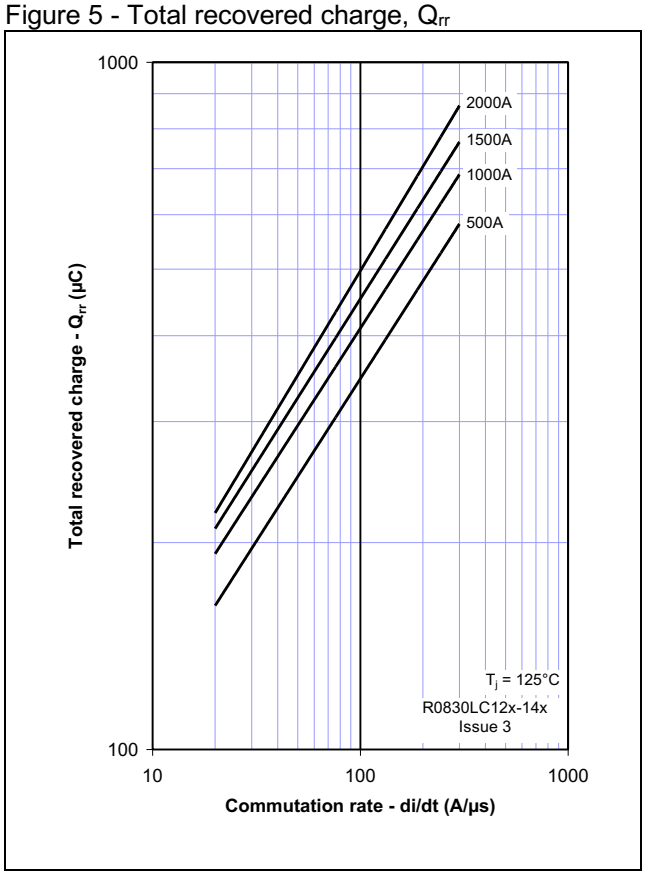


Figure 6 - Recovered charge, Q_{ra} (50% chord)

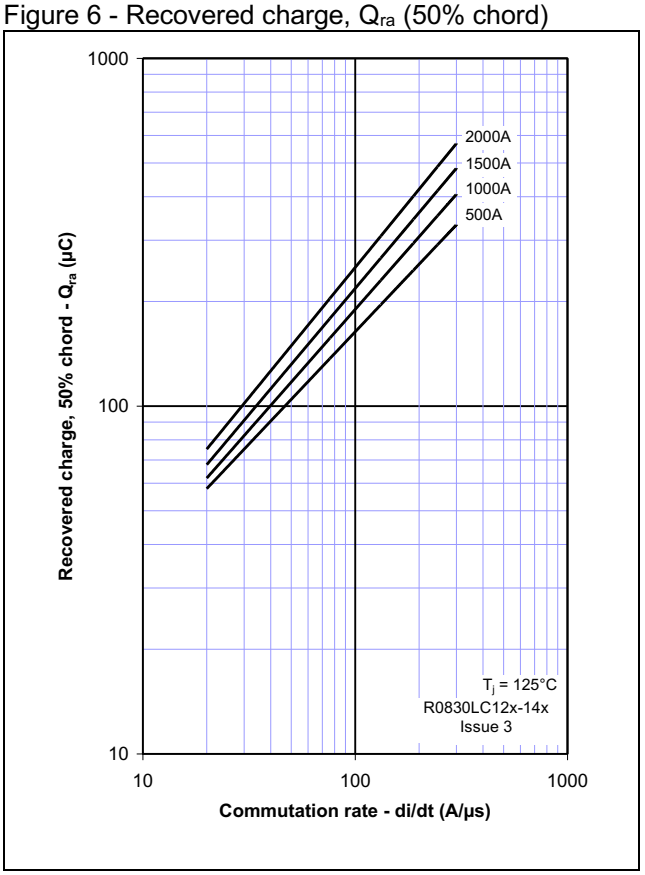


Figure 7 - Peak reverse recovery current, I_{rm}

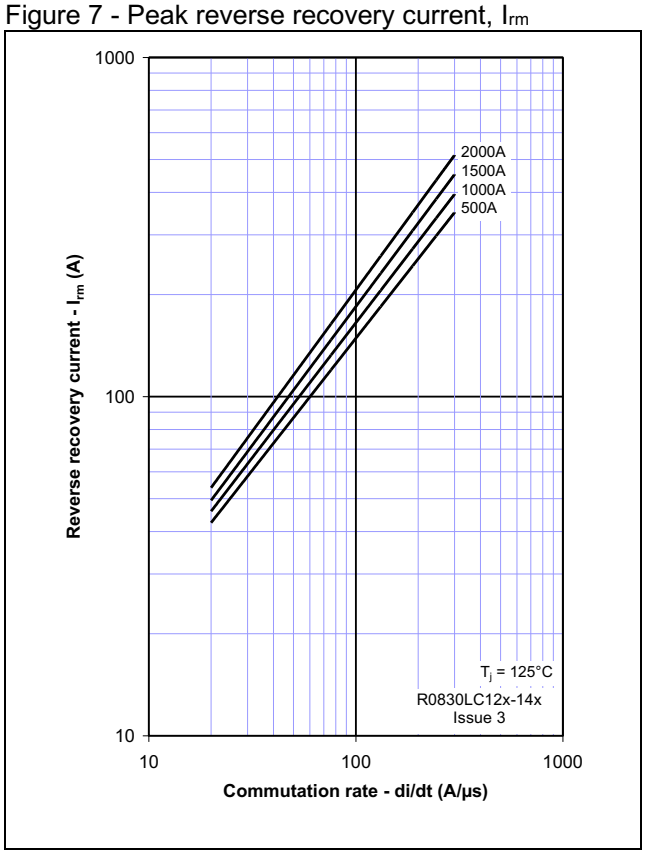


Figure 8 - Maximum recovery time, t_{rr} (50% chord)

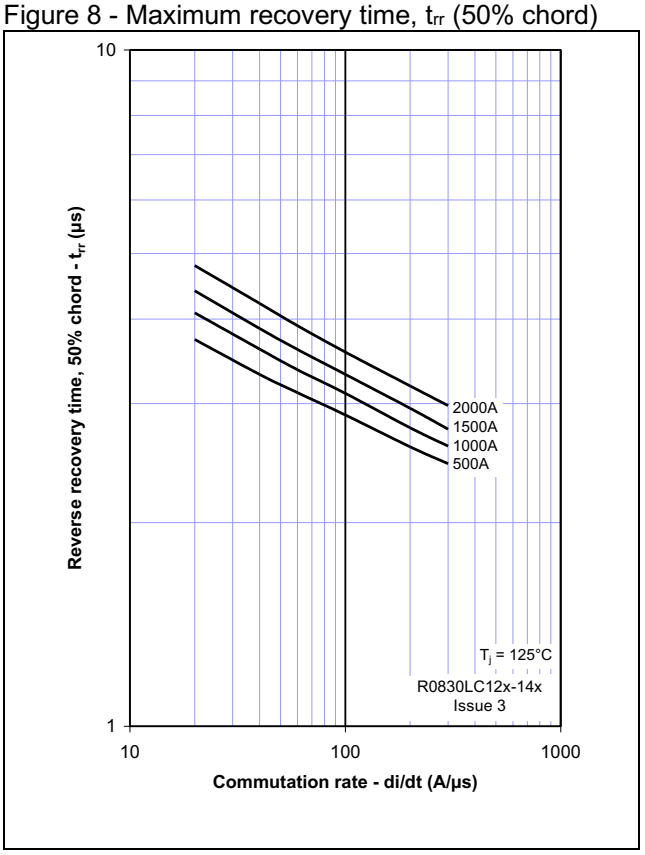


Figure 9 – Reverse recovery energy per pulse

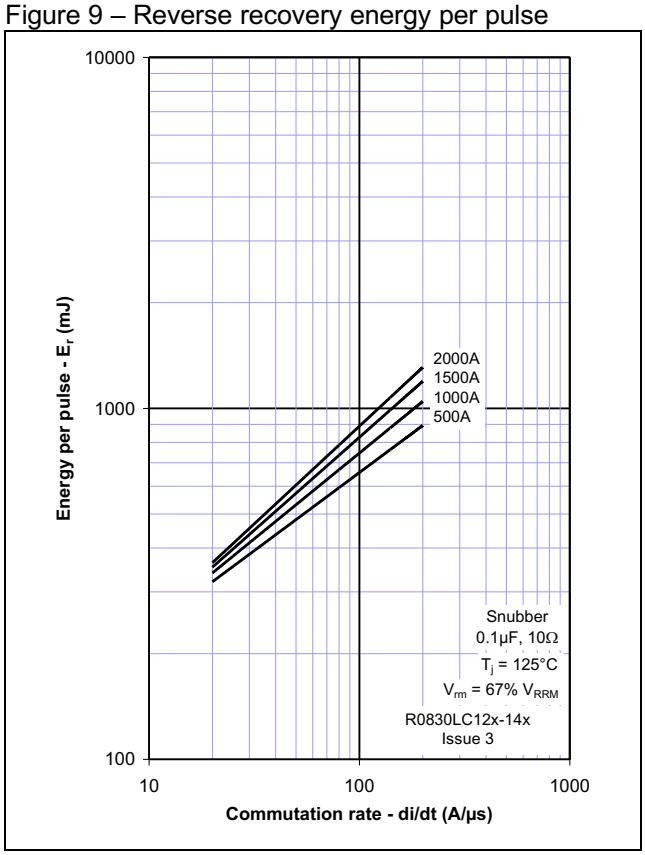


Figure 10 - Sine wave energy per pulse

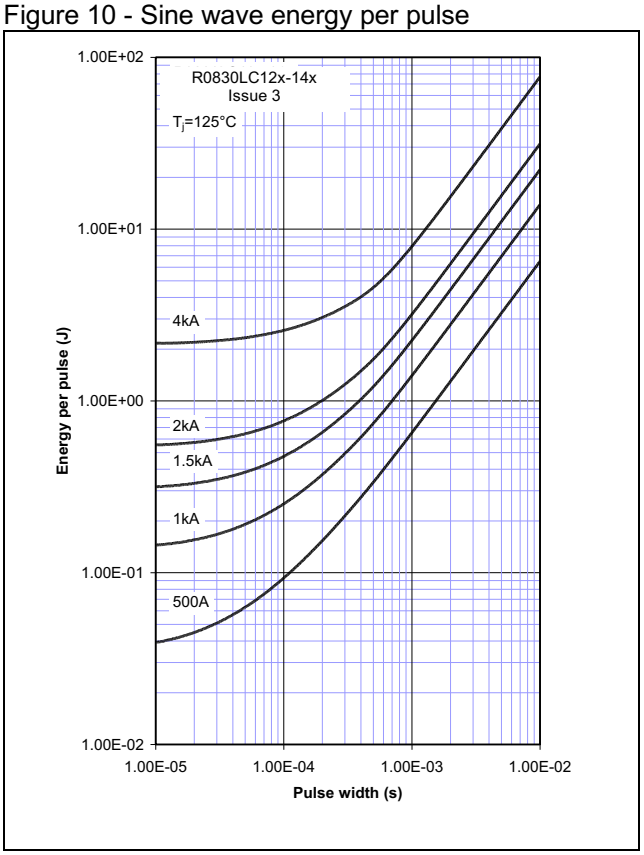


Figure 11 - Sine wave frequency ratings

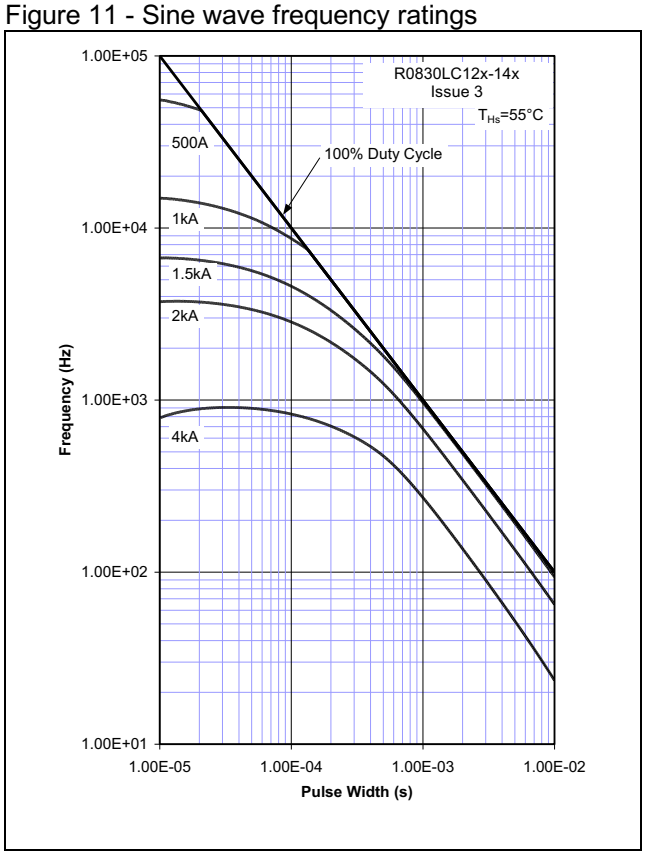


Figure 12 - Sine wave frequency ratings

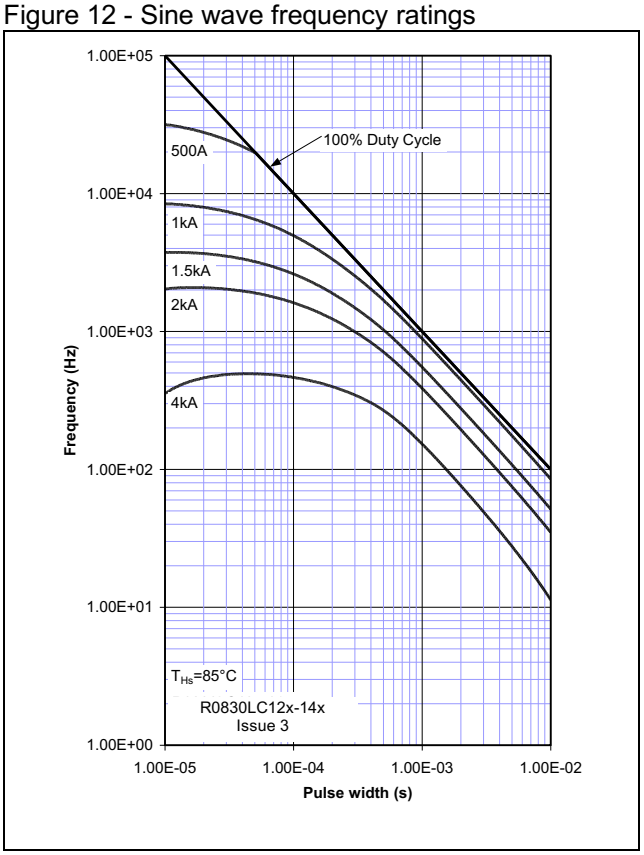


Figure 13 - Square wave frequency ratings

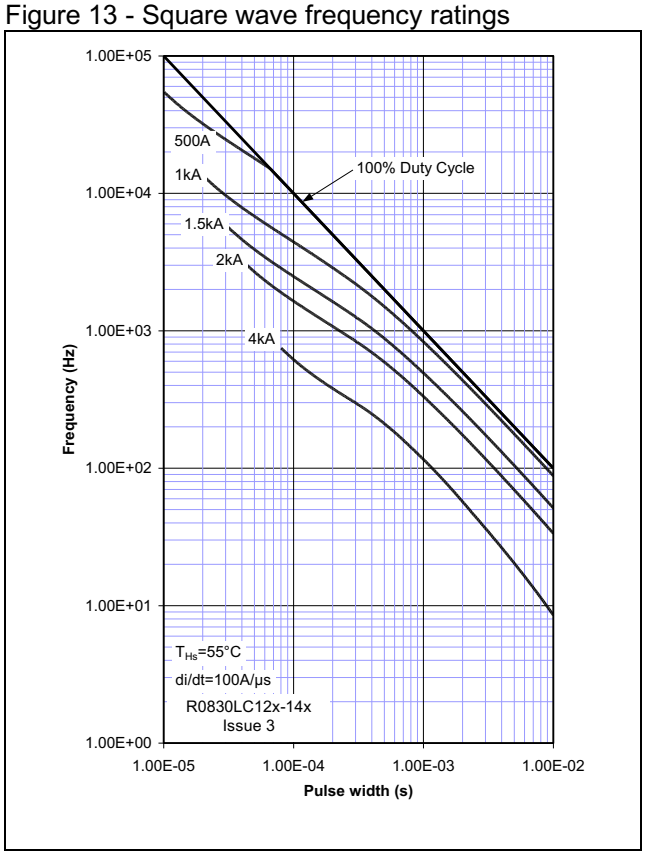


Figure 14 - Square wave frequency ratings

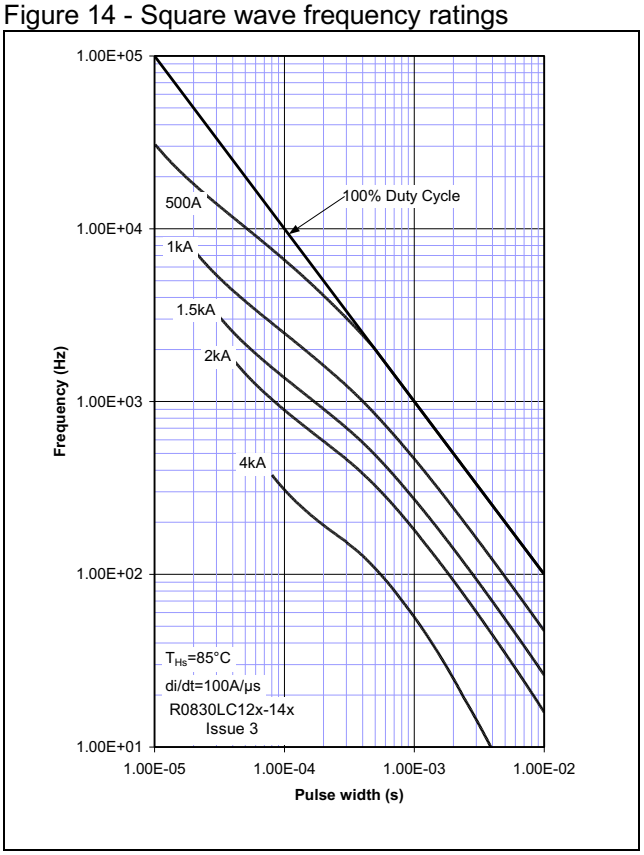


Figure 15 - Square wave frequency ratings

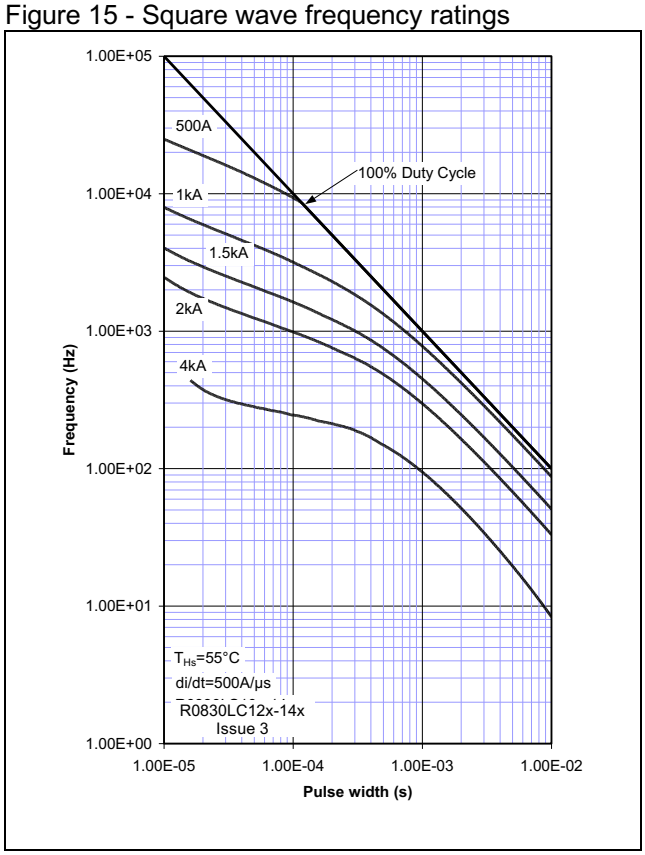


Figure 16 - Square wave frequency ratings

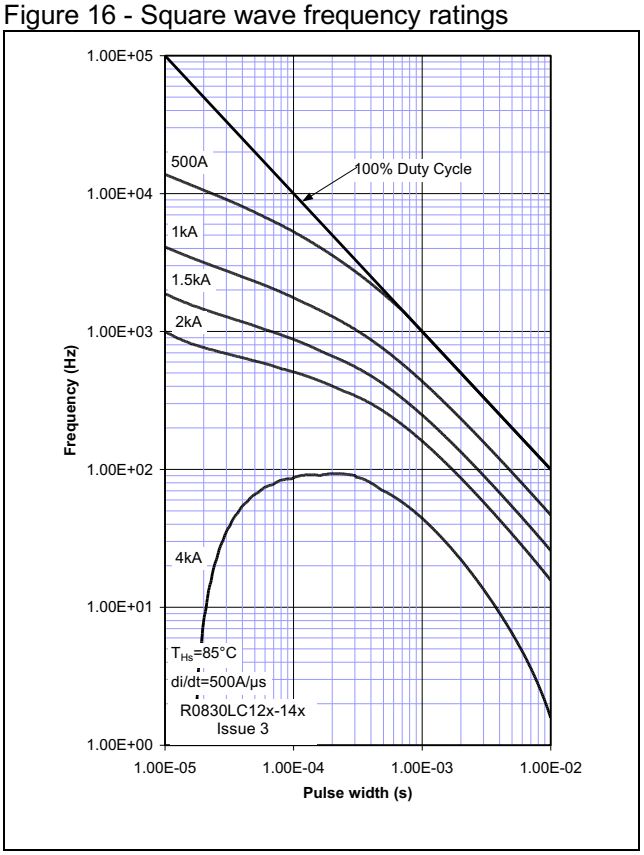


Figure 17 - Square wave energy per pulse

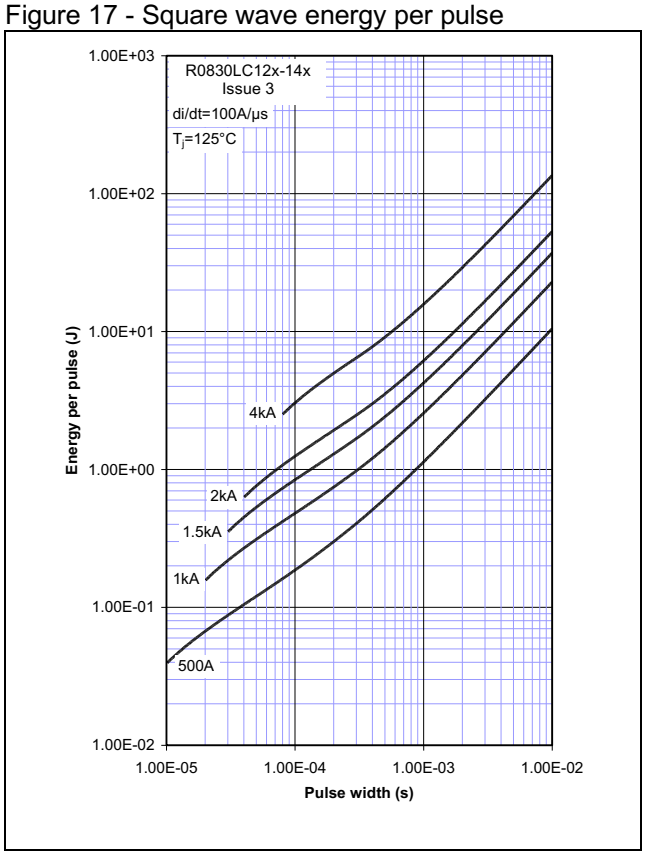


Figure 18 - Square wave energy per pulse

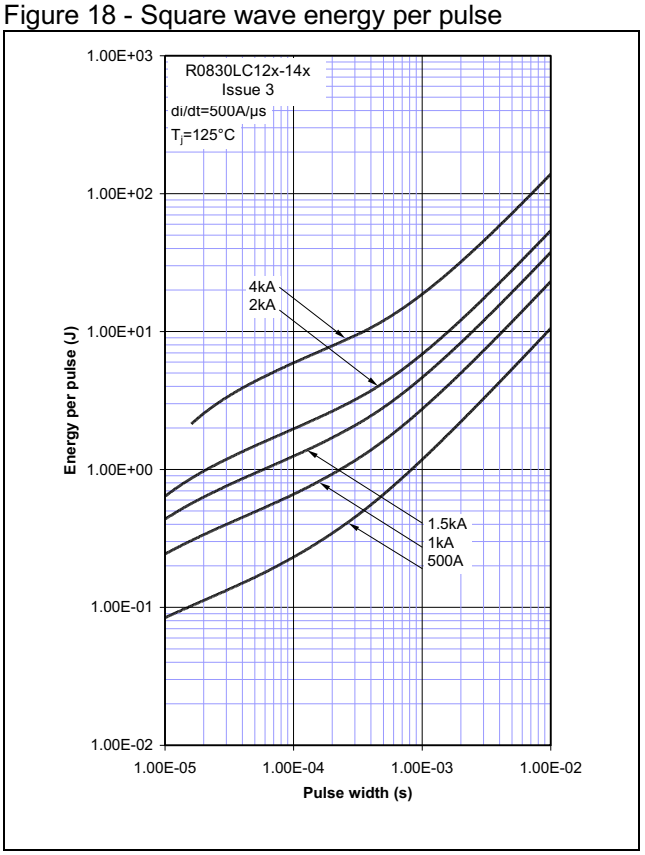
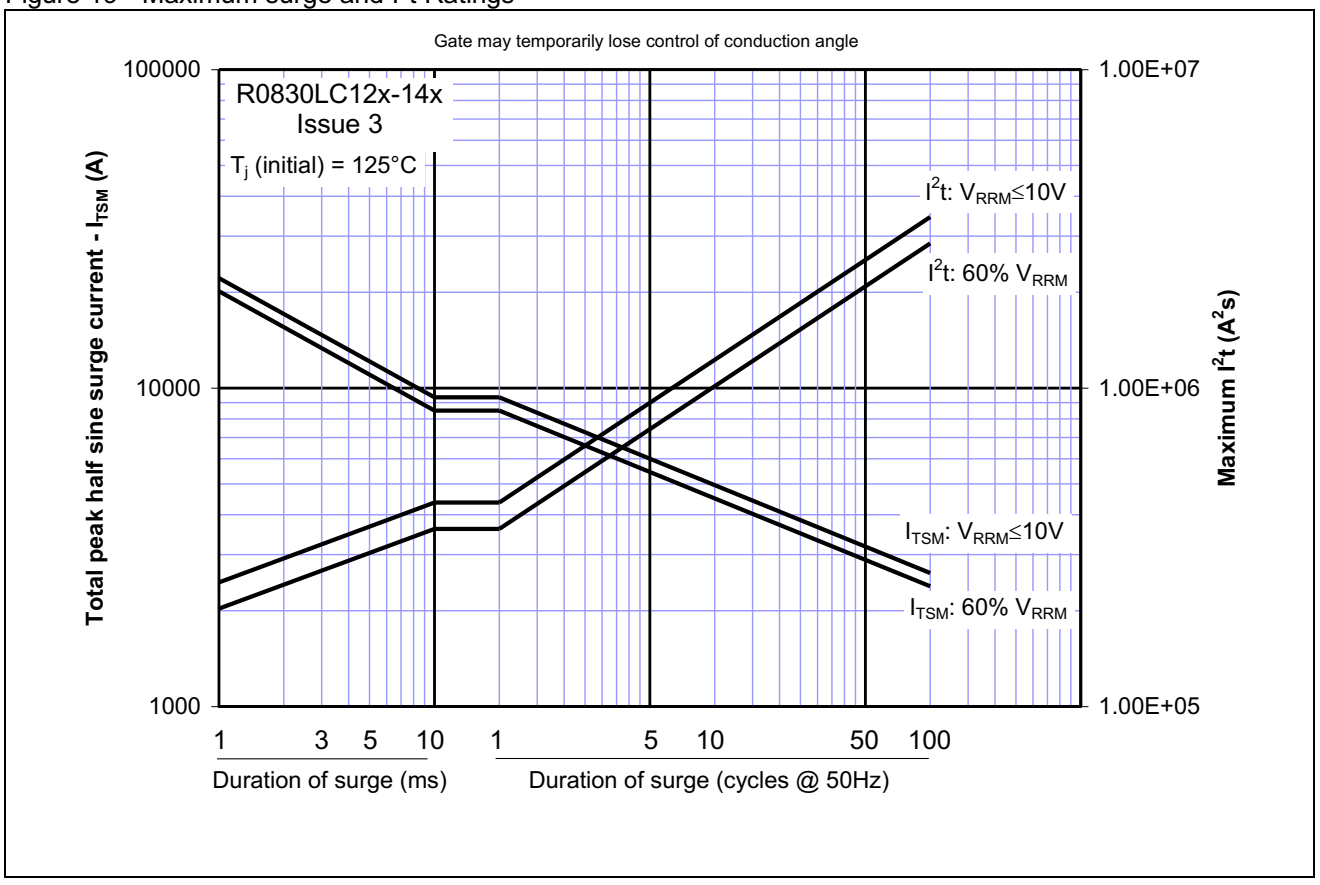
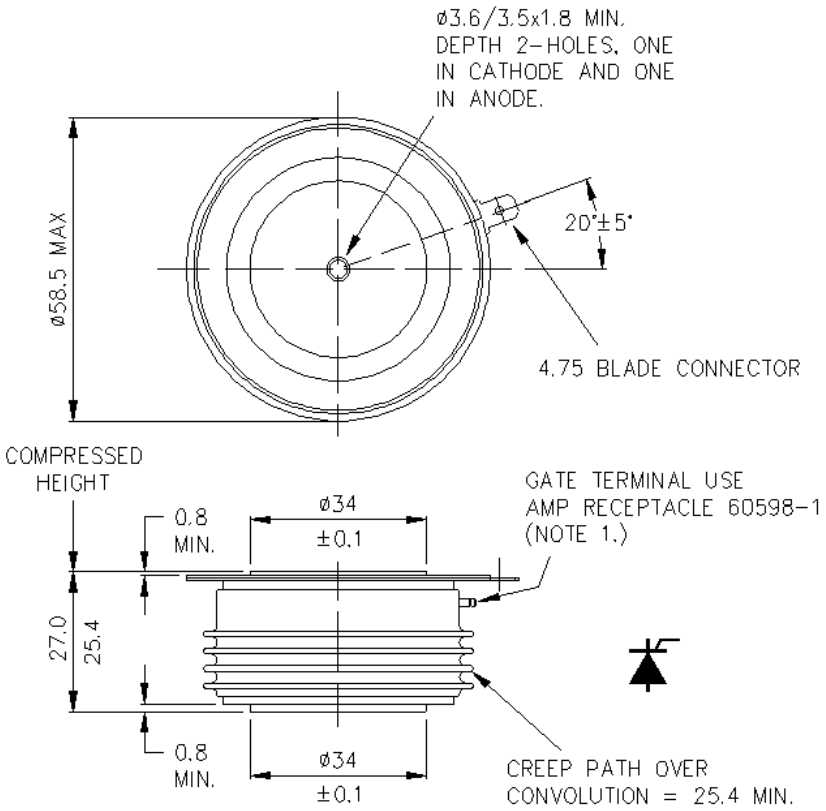


Figure 19 - Maximum surge and I^2t Ratings



Outline Drawing & Ordering Information



101A216

ORDERING INFORMATION (Please quote 10 digit code as below)

R0830	LC	♦ ♦	♦
Fixed Type Code	Outline Code	Fixed Voltage Code $V_{DRM}/100$ 12-14	t_q Code C=15µs, D=20µs, E=25µs, F = 30µs

Typical order code: R0830LC12E – 1200V V_{DRM}, 1200V V_{RRM} 25µs t_q, 27mm clamp height capsule.

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