

Data Sheet Issue:- 2

Rectifier Diode Types W3477MC360 and W3477MC400

Development part number Wx252MC360-400

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
Vrrm	Repetitive peak reverse voltage, (note 1)	3600-4000	V
Vrsm	Non-repetitive peak reverse voltage, (note 1)	3700-4100	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
IF(AV)M	Maximum average forward current, T _{sink} =55°C, (note 2)	3470	А
I _{F(AV)M}	Maximum average forward current. T _{sink} =100°C, (note 2)	2409	А
IF(AV)M	Maximum average forward current. T _{sink} =100°C, (note 3)	1421	А
I _{F(RMS)M}	Nominal RMS forward current, T _{sink} =25°C, (note 2)	6380	А
I _{F(d.c.)}	D.C. forward current, T _{sink} =25°C, (note 4)	5592	А
IFSM	Peak non-repetitive surge t _p =10ms, V _{rm} =60%V _{RRM} , (note 5)	28200	Α
I _{FSM2}	Peak non-repetitive surge t _p =10ms, V _{rm} ≤10V, (note 5)	31000	А
l²t	$I^{2}t$ capacity for fusing t _p =10ms, V _{rm} =60%V _{RRM} , (note 5)	3.98×10 ⁶	A ² s
l²t	$I^{2}t$ capacity for fusing t _p =10ms, V _{rm} ≤10V, (note 5)	4.81×10 ⁶	A ² s
T _{j op}	Operating temperature range	-40 to +160	°C
T _{stg}	Storage temperature range	-55 to +160	°C

Notes:-

1) De-rating factor of 0.13% per °C is applicable for T_j below 25°C.

2) Double side cooled, single phase; 50Hz, 180° half-sinewave.

3) Cathode side cooled, single phase; 50Hz, 180° half-sinewave.

4) Double side cooled.

5) Half-sinewave, $160^{\circ}C T_{j}$ initial.



Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
Vfm	Maximum peak forward voltage	-	-	1.34	IFM=3000A	V
V _{FM}	Maximum peak forward voltage	-	-	2.10	I _{FM} =8000A	V
Vt0	Threshold voltage	-	-	0.908		V
r⊤	Slope resistance	-	-	0.146		mΩ
I _{RRM}	Peak reverse current	-	-	100	Rated V _{RRM}	mA
Qrr	Recovered charge	-	6000	7200		μC
Qra	Recovered charge, 50% Chord	-	3800	-	I _{TM} =1000A, t _p =1000µs, di/dt=10A/µs,	μC
Irm	Reverse recovery current	-	200	-	Vr=100V	А
t _{rr}	Reverse recovery time, 50% chord	-	38	-		μs
		-	-	0.0140	Double side cooled	K/W
RthJK	Thermal resistance, junction to heatsink	-	-	0.0265	Anode side cooled	K/W
		-	-	0.0297	Cathode side cooled	K/W
F	Mounting force	25	-	31	Note 2	kN
Wt	Weight		530			g

Notes:-

Unless otherwise indicated T_j=160°C.
For other clamp forces, please consult factory.



Notes on Ratings and Characteristics

1.0 Voltage Grade Table

Voltage Grade	V _{RRM} V	V _{RSM} V	V _R DC V
3600	3600	3700	2050
4000	4000	4100	2280

2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_j below 25°C.

4.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

5.0 Computer Modelling Parameters

5.1 Device Dissipation Calculations

Where V_{T0} =0.908V, r_T=0.146m Ω ,

 R_{th} = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

Supplementary Thermal Impedance						
Conduction Angle	6 phase (60°)	3 phase (120°)	1⁄2 wave (180°)	d.c.		
Square wave Double Side Cooled	0.01665	0.01581	0.01516	0.0140		
Square wave Cathode Side Cooled	0.03217	0.03147	0.03090	0.0297		
Sine wave Double Side Cooled	0.01612	0.01531	0.01436			
Sine wave Cathode Side Cooled	0.03174	0.03105	0.03022			

Form Factors						
Conduction Angle	6 phase (60°)	3 phase (120°)	½ wave (180°)	d.c.		
Square wave	2.449	1.732	1.414	1		
Sine wave	2.778	1.879	1.57			



5.2 Calculating V_F using ABCD Coefficients

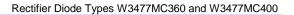
The on-state characteristic IF vs. VF, on page 6 is represented in two ways;

- (i) the well established V_{T0} and r_T tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V_F in terms of I_F given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V_F agree with the true device characteristic over a current range, which is limited to that plotted.

25°C Coefficients		160°C Coefficients	
А	0.7582219	А	0.3928004
В	2.173347×10 ⁻³	В	0.03185368
С	6.524855×10⁻⁵	С	9.588061×10⁻⁵
D	6.610407×10 ⁻³	D	7.304107×10 ⁻³





5.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}}\right)$$

Where p = 1 to *n*, *n* is the number of terms in the series and:

- t = Duration of heating pulse in seconds.
- r_t = Thermal resistance at time t.
- r_p = Amplitude of p_{th} term.
- $\tau_{p} = \text{ Time Constant of } r_{th} \text{ term.}$

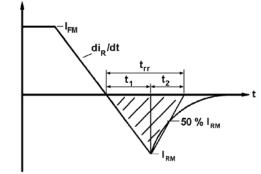
The coefficients for this device are shown in the tables below:

D.C. Double Side Cooled						
Term	erm 1 2 3 4					
r _p	8.594785×10 ⁻³	3.308247×10 ⁻³	1.039072×10 ⁻³	7.916582×10 ⁻⁴		
τρ	0.7185764	0.09970181	0.02165834	5.266433×10 ⁻³		

D.C. Cathode Side Cooled						
Term	Term 1 2 3					
r _p	0.02196926	5.845724×10 ⁻³	1.904897×10 ⁻³			
τρ	4.127141	0.1629998	8.832583×10 ⁻³			

6.0 Reverse recovery ratings

(i) Q_{ra} is based on 50% I_{rm} chord as shown in Fig. 1





(ii) Q_{rr} is based on a 150µs integration time i.e.

$$Q_{rr} = \int_{0}^{150\,\mu s} i_{rr}.dt$$

(iii)

K Factor =
$$\frac{t_1}{t_2}$$



<u>Curves</u>

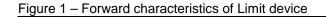
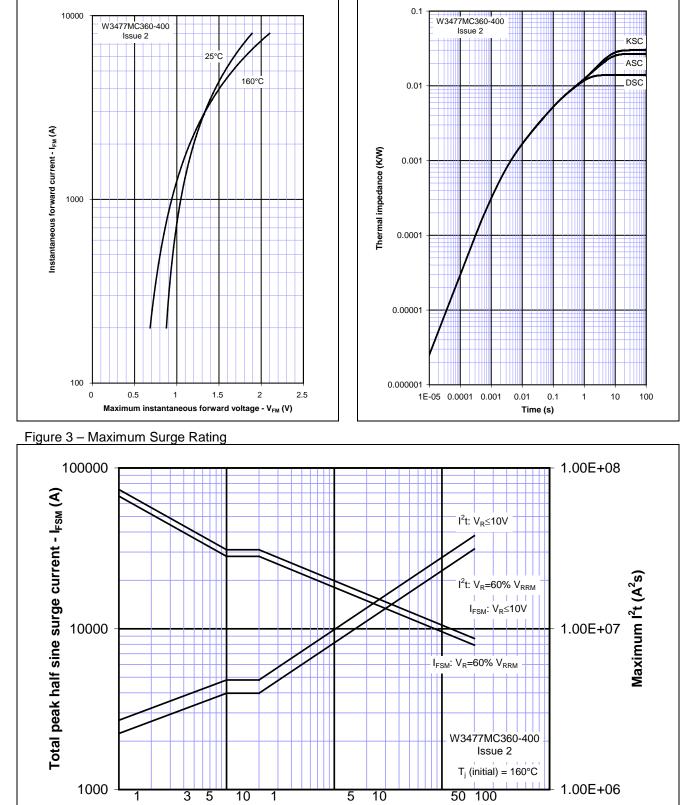


Figure 2 – Transient thermal impedance



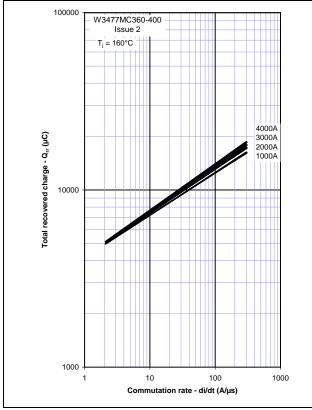
Duration of surge (ms)

Duration of surge (cycles @ 50Hz)

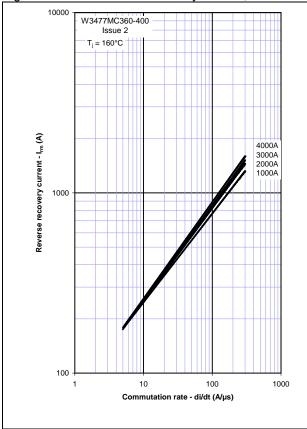


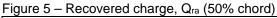
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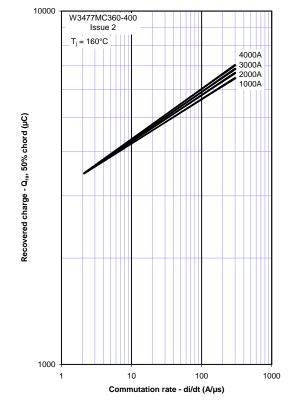
Figure 4 – Total recovered charge, Qrr













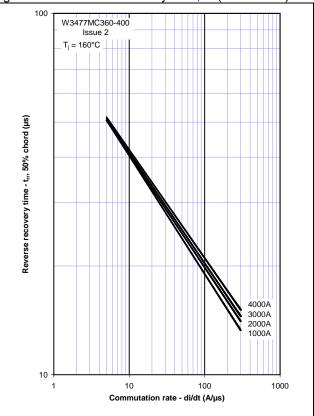




Figure 8 – Forward current vs. Power dissipation – Double Side Cooled

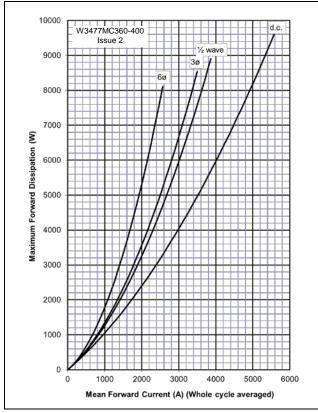


Figure 10 – Forward current vs. Power dissipation – Cathode Side Cooled

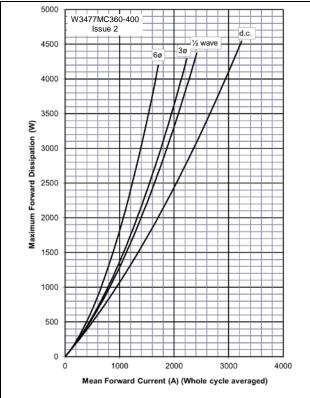


Figure 9 – Forward current vs. Heatsink temperature – Double Side Cooled

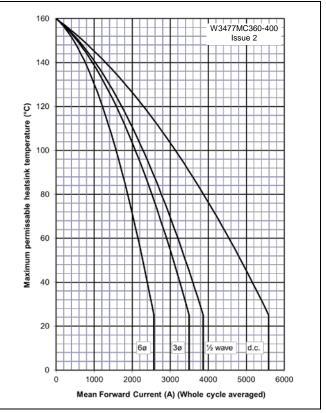
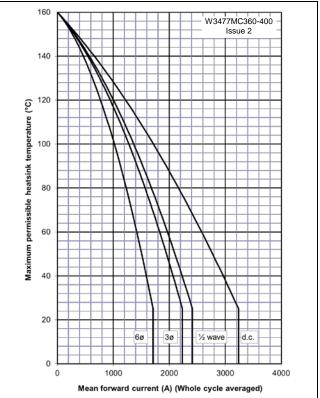
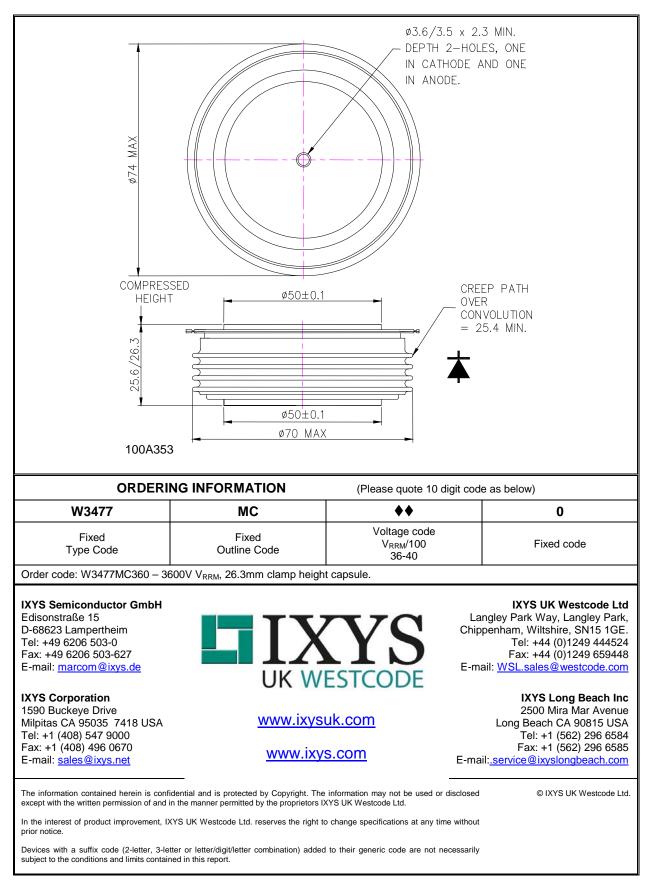


Figure 11 – Forward current vs. Heatsink temperature – Cathode Side Cooled





Outline Drawing & Ordering Information





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