

Parameter	Rating	Units
AC Operating Voltage	20 - 240	V_{rms}
Load Current	1.5	A_{rms}
On-State Voltage Drop	1.4	V_P (at $I_L = 1.5A_P$)
Blocking Voltage	600	V_P

Features

- Load Current up to $1.5A_{rms}$
- 600V_P Blocking Voltage
- High Surge Current: 15A
- Rapid Turn-On (Non-Zero-Cross Turn-On)
- 5mA Sensitivity
- Creepage Distance: 0.220" on Output Pins
- 12.5mm External Creepage Distance
- DC Control, AC Output
- Optically Isolated
- Low EMI and RFI Generation
- High Noise Immunity
- Flammability Rating UL 94 V-0

Applications

- HVAC Control (Heating, Ventilation, Air Conditioning)
- Lighting
- Programmable Control
- Process Control
- Power Control Panels
- Remote Switching
- Gas Pump Electronics
- Contactors
- Large Relays
- Solenoids
- Motors
- Heaters
- Meters

Description

CPC1964BX6 is an AC Solid State Switch utilizing dual power SCR outputs. This device features Rapid Turn-On (non-zero-cross) control of the output SCRs, which makes it ideal for precisely switching AC loads independent of the load voltage phase.

The optically coupled input and output circuits provide 5000V_{rms} of isolation and noise immunity between the control and load circuits. As a result, the CPC1964BX6 is well suited for industrial environments where electromagnetic interference would disrupt the operation of plant facility communication and control systems.

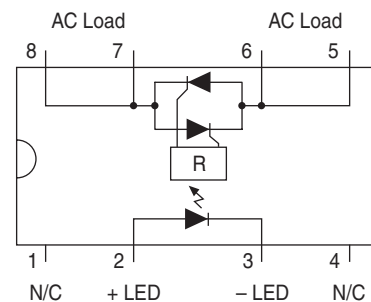
Approvals

- UL Recognized Component: File E69938
- CSA Certified Component: Certificate 1172007

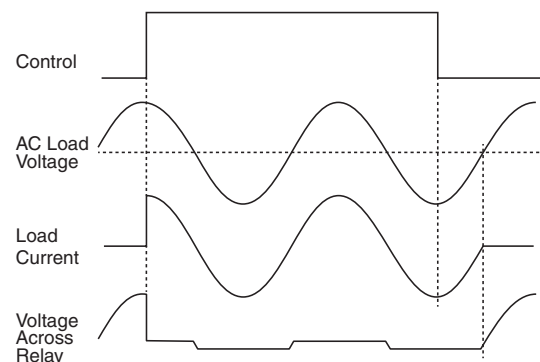
Ordering Information

Part #	Description
CPC1964BX6	8-Pin Power SOIC (25/Tube)

Pin Configuration



Rapid Turn-On (Non Zero-Cross) Waveforms



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage (V_{DRM})	600	V_P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
di/dt Critical Rate of Rise of On-State Current	20	A/ μ s
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	2400	mW
ESD, Human Body Model	4	kV
i^2t Fusing Current (1/2 Sine Wave, 60Hz)	2	A ² s
Isolation Voltage, Input to Output	5000	V_{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 1.33 mW / °C

² Derate linearly 20 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

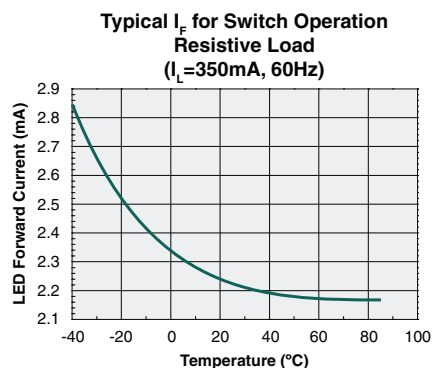
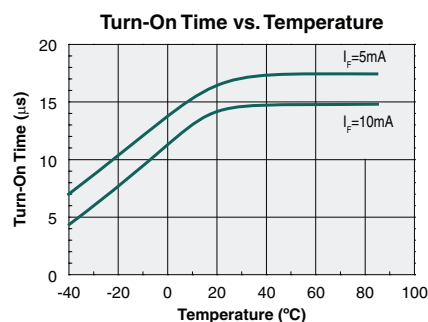
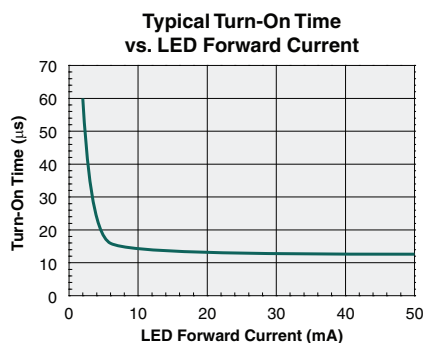
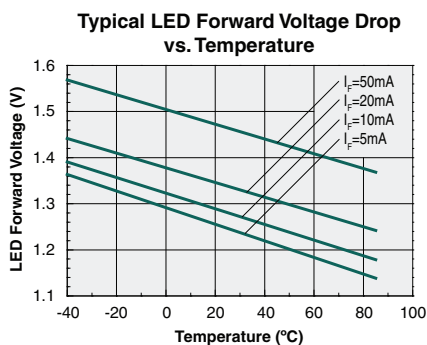
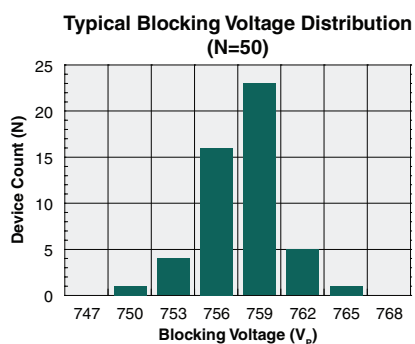
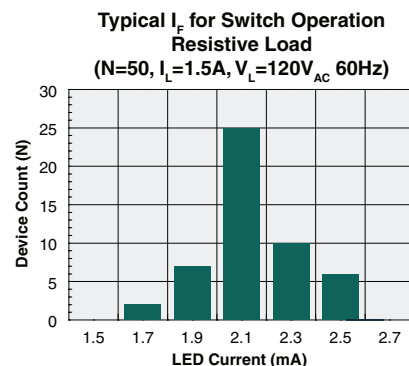
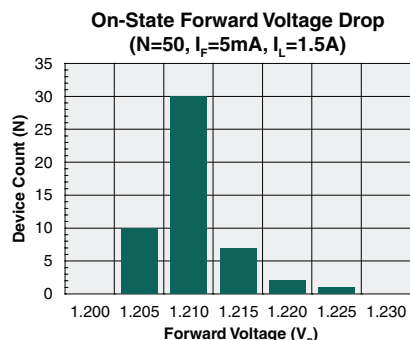
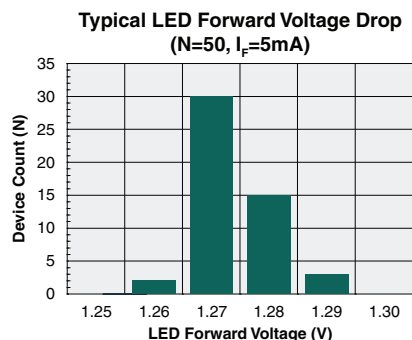
Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Electrical Characteristics @ 25°C

Parameters	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current, Continuous	$V_L = 20\text{--}240V_{rms}$	I_L	0.07	-	1.5	A_{rms}
Maximum Surge Current	$t \leq 16\text{ms}$	I_P	-	-	15	A
Off State Leakage Current	V_{DRM}	I_{LEAK}	-	-	100	μA_P
On-State Voltage Drop	$I_L = 1.5A_P$	-	-	1.21	1.4	V_P
Off-State dV/dt	-	dV/dt	1000	-	-	V/ μ s
Switching Speeds	$I_F = 5\text{mA}$, Resistive $V_L = 20\text{V}$, 60Hz	t_{on}	-	20	500	μ s
Turn-on		t_{off}	-	-	0.5	cycles
Turn-off						
Holding Current	-	I_H	-	44	75	mA
Latching Current	-	I_L	-	48	75	mA
Operating Frequency	-		20	-	500	Hz
Input Characteristics						
Input Control Current to Activate ¹	60Hz	I_F	-	-	5	mA
Input Drop-out Voltage	-	-	0.8	-	-	V
Input Voltage Drop	$I_F = 5\text{mA}$	V_F	0.9	1.2	1.5	V
Reverse Input Current	$V_R = 5\text{V}$	I_R	-	-	10	μ A
Common Characteristics						
Input to Output Capacitance	$V_{IO} = 0\text{V}$, $f = 1\text{MHz}$	C_{IO}	-	-	3	pF

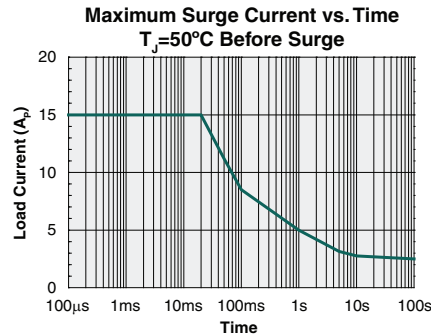
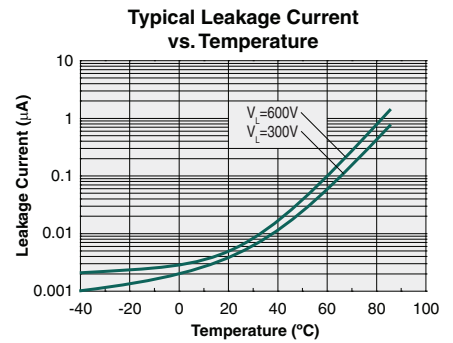
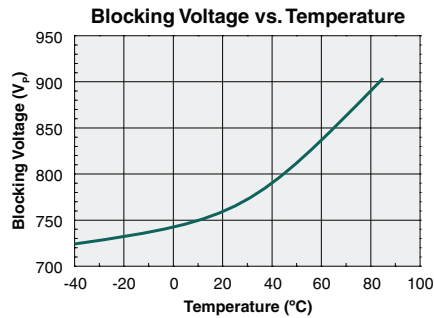
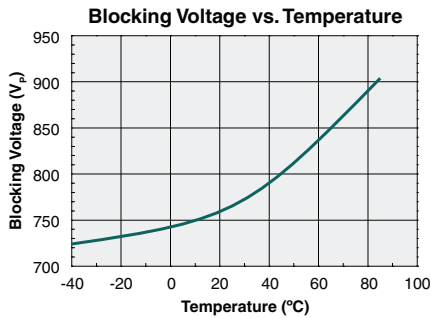
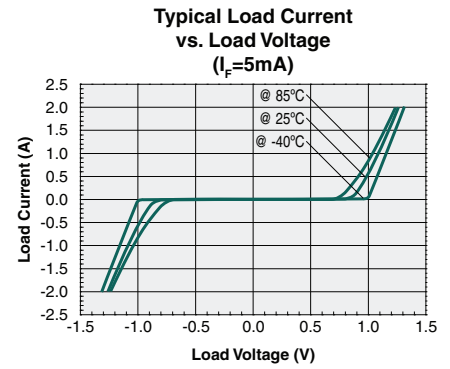
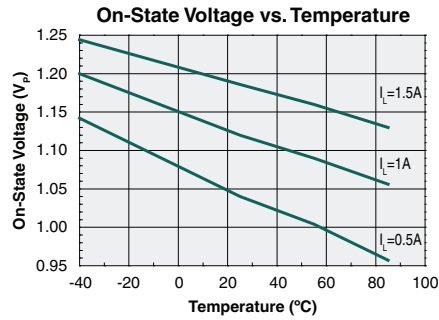
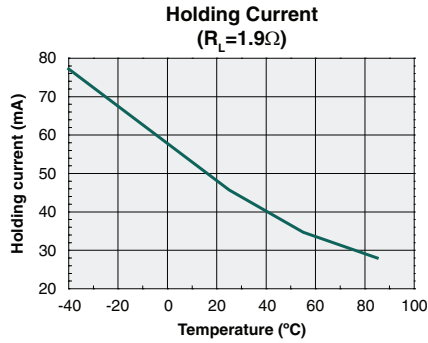
¹ For high-noise environments, or for high-frequency operation, use $I_F \geq 10\text{mA}$.

PERFORMANCE DATA*



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

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Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
CPC1964BX6	MSL 1

ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the Classification Temperature (T_C) of this product and the maximum dwell time the body temperature of this device may be ($T_C - 5$)°C or greater. The classification temperature sets the Maximum Body Temperature allowed for this device during lead-free reflow processes. For through-hole devices, and any other processes, the guidelines of **J-STD-020** must be observed.

Device	Classification Temperature (T_C)	Dwell Time (t_p)	Max Reflow Cycles
CPC1964BX6	245°C	30 seconds	3

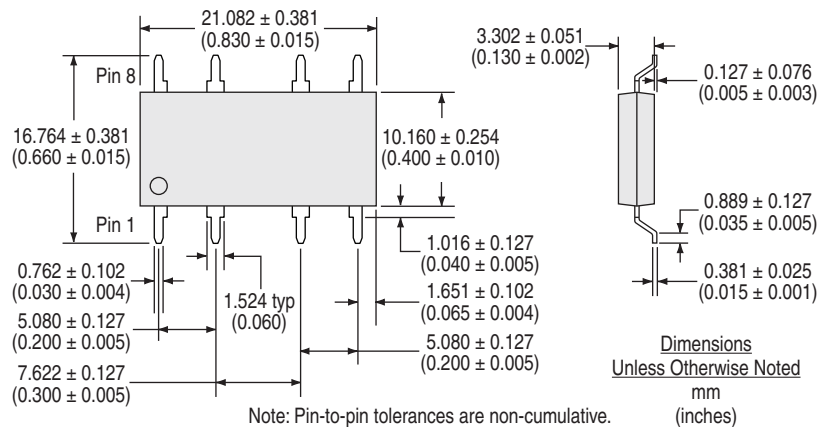
Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to flux or solvents that are Chlorine- or Fluorine-based.

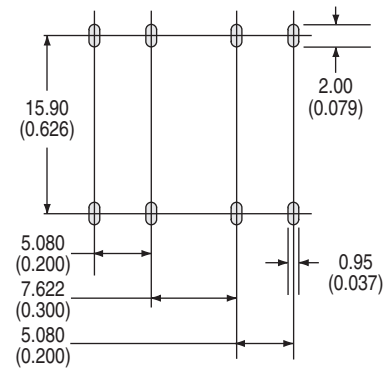


MECHANICAL DIMENSIONS

CPC1964BX6



Recommended PCB Pattern



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