INTEGRATED CIRCUITS DIVISION

IAD110P
Multifunction Telecom Circuits:
SSR 1-Form-A (1) / Optocoupler (2)

Parameter	Rating	Units
Blocking Voltage	350	V_P
Load Current	100	mA_{rms} / mA_{DC}
On-Resistance (max)	35	Ω

Features

- 3750V_{rms} Input/Output Isolation
- Bidirectional Current Sensing
- Bidirectional Current Switching
- Replaces up to Three or Four Components
- Three Functions in One Package
- Small 16-Pin SOIC Package
- FCC Compatible
- No EMI/RFI Generation
- Tape & Reel Versions Available

Applications

- Telecommunications
  - Telecom Switching
  - Tip/Ring Circuits
  - Modem Switching (Laptop, Notebook, Pocket Size)
  - Hook Switch
  - Dial Pulsing
  - Ground Start
  - Ringing Injection
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Industrial Controls

Description

The IAD110P Multifunction Telecom switch combines a 350V normally open (1-Form-A) relay and two optocouplers in a single package. The relay uses optically coupled MOSFET technology to provide 3750V_{rms} of input to output isolation. The efficient MOSFET switch and photovoltaic die use IXYS Integrated Circuits Division's patented OptoMOS® architecture. The optically coupled output is controlled by highly efficient infrared LEDs. The IAD110P enables telecom circuit designers to combine three discrete functions in a single component that uses less space than traditional discrete component solutions.
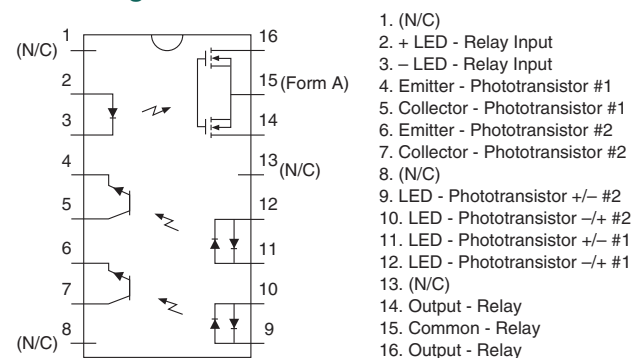
Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1305490
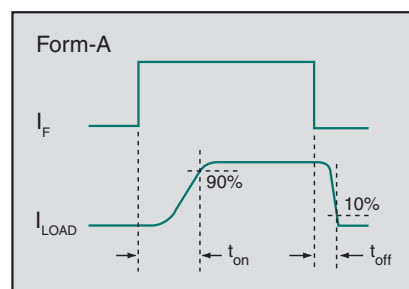- TUV EN 62368-1: Certificate B 082667 0008

Ordering Information

Part #	Description
IAD110P	16-Pin SOIC (50/Tube)
IAD110PTR	16-Pin SOIC (1000/Reel)

Pin Configuration

1. (N/C)
2. + LED - Relay Input
3. – LED - Relay Input
4. Emitter - Phototransistor #1
5. Collector - Phototransistor #1
6. Emitter - Phototransistor #2
7. Collector - Phototransistor #2
8. (N/C)
9. LED - Phototransistor +/– #2
10. LED - Phototransistor –/+ #2
11. LED - Phototransistor +/– #1
12. LED - Phototransistor –/+ #1
13. (N/C)
14. Output - Relay
15. Common - Relay
16. Output - Relay

Switching Characteristics of Normally Open Devices

IXYS

INTEGRATED CIRCUITS DIVISION

IAD110P

Absolute Maximum Ratings @ 25°C

Parameter	Symbol	Ratings	Units
Input Control Current, Relay	I_F	50	mA
Total Package Dissipation [1]	P_T	1	W
Isolation Voltage, Input to Output	V_{ISO}	3750	V_{rms}
Operational Temperature	T_A	-40 to +85	°C
Storage Temperature	T_{STG}	-40 to +125	°C

[1] Derate linearly 1.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

## Electrical Characteristics @25°C: Relay Section

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Blocking Voltage (Peak)	I_L=1µA	V_L	-	-	350	V_P
Load Current						
Continuous	-	I_L	-	-	100	mA_{rms} / mA_{DC}
Peak	t=10ms	I_{LPK}	-	-	350	mA_P
On-Resistance	I_L=100mA	R_{ON}	-	-	35	Ω
Off-State Leakage Current	V_L=350V, T_J=25°C	I_{LEAK}	-	-	1	µA
Switching Speeds						
Turn-On	I_F=5mA, V_L=10V	t_{on}	-	-	3	ms
Turn-Off		t_{off}	-	-	3	ms
Output Capacitance	V_L=50V, f=1MHz	C_{OUT}	-	25	-	pF
Input Characteristics						
Input Control Current to Activate	I_L=100mA	I_F	-	-	5	mA
Input Control Current to Deactivate	I_L=1mA	I_F	0.4	-	-	mA
Input Voltage Drop	I_F=5mA	V_F	0.9	1.2	1.4	V
Reverse Input Voltage	-	V_R	-	-	5	V
Reverse Input Current	V_R=5V	I_R	-	-	10	µA

Electrical Characteristics @25°C: Detector Section

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Phototransistor Blocking Voltage	I_C=10µA	BV_{CEO}	20	50	-	V
Phototransistor Dark Current	V_{CE}=5V, I_F=0mA	I_{CEO}	-	50	500	nA
Saturation Voltage	I_C=2mA, I_F=16mA	V_{SAT}	-	0.3	0.5	V
Current Transfer Ratio	I_F=6mA, V_{CE}=0.5V	CTR	33	-	-	%
Input Characteristics						
Input Control Current	I_C=2mA, V_{CE}=0.5V	I_F	-	2	6	mA
Input Voltage Drop	I_F=5mA	V_F	0.9	1.2	1.4	V
Input Current (Detector Must be Off)	I_C=1µA, V_{CE}=5V	-	-	5	25	µA
Capacitance, Input to Output	V_L=50V, f=1MHz	$C_{I/O}$	-	3	-	pF
Isolation, Input to Output	-	$V_{I/O}$	3750	-	-	V_{rms}

IXYS
INTEGRATED CIRCUITS DIVISION

RELAY PERFORMANCE DATA*

Typical LED Forward Voltage Drop
(N=50, I_F=5mA, T_A=25ºC)
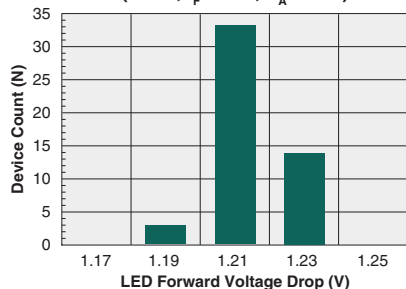
Typical Turn-On Time
(N=50, I_F=5mA, I_L=100mA$_{DC}$, T_A=25ºC)
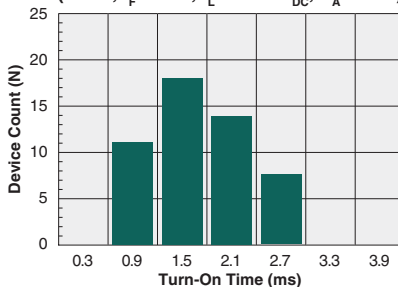
Typical Turn-Off Time
(N=50, I_F=5mA, I_L=100mA$_{DC}$, T_A=25ºC)
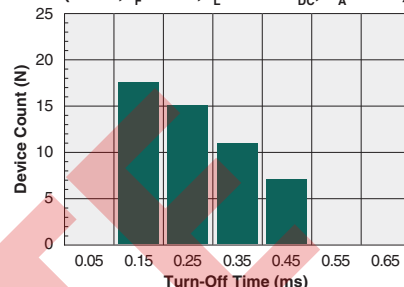
Typical I_F for Switch Operation
(N=50, I_L=100mA$_{DC}$, T_A=25ºC)
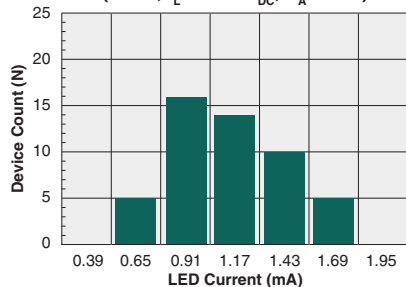
Typical I_F for Switch Dropout
(N=50, I_L=100mA$_{DC}$, T_A=25ºC)
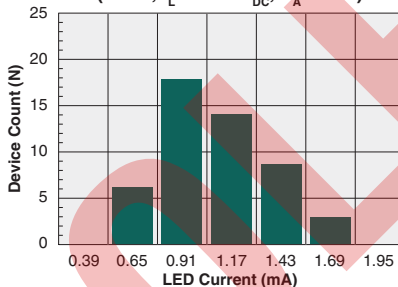
Typical On-Resistance Distribution
(N=50, I_F=5mA, I_L=100mA$_{DC}$, T_A=25ºC)
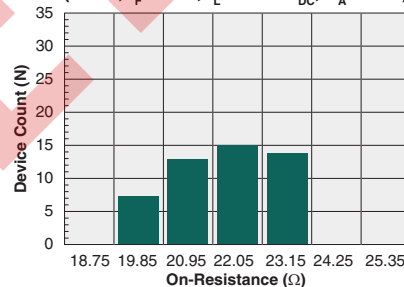
Typical Blocking Voltage Distribution
(N=50, T_A=25ºC)
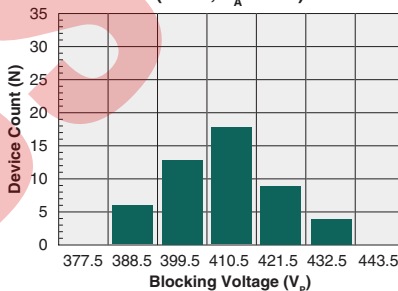
Typical LED Forward Voltage Drop
vs. Temperature

I_F=50mA
I_F=30mA
I_F=20mA
I_F=10mA
I_F=5mA
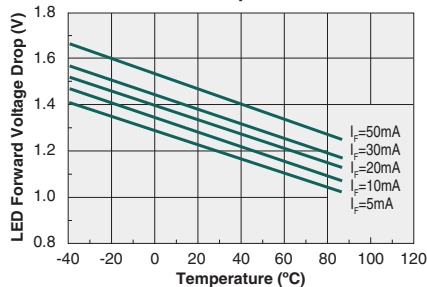
Typical Turn-On Time
vs. LED Forward Current
(I_L=100mA$_{DC}$)
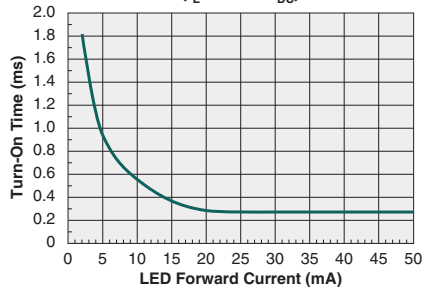
Typical Turn-Off Time
vs. LED Forward Current
(I_L=100mA$_{DC}$)
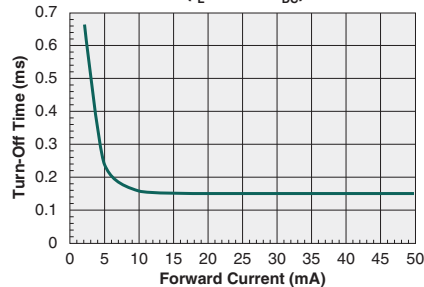
* Unless otherwise noted, data presented in these graphs is typical of device operation at 25ºC.

IXYS
INTEGRATED CIRCUITS DIVISION

IAD110P

RELAY PERFORMANCE DATA (cont.)*

Typical Turn-On Time vs. Temperature (I_L=100mA$_{DC}$)

Typical Turn-Off vs. Temperature (I_F=5mA, I_L=100mA$_{DC}$)

Maximum Load Current vs. Temperature

Typical I_F for Switch Operation vs. Temperature (I_L=100mA$_{DC}$)

Typical I_F for Switch Dropout vs. Temperature (I_L=100mA$_{DC}$)

Typical On-Resistance vs. Temperature (I_F=5mA, I_L=100mA$_{DC}$)

Typical Load Current vs. Load Voltage (I_F=5mA, T_A=25°C)

Typical Blocking Voltage vs. Temperature

Typical Leakage vs. Temperature Measured across Pins 14&16
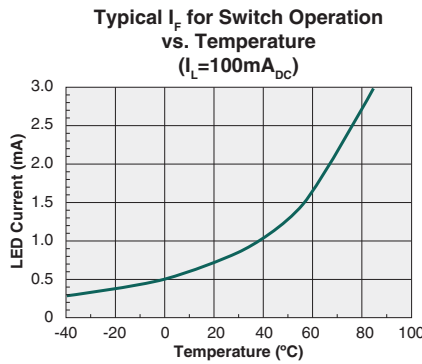

Energy Rating Curve
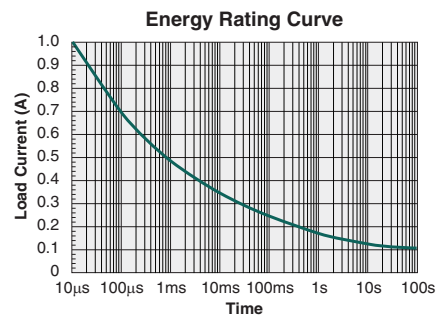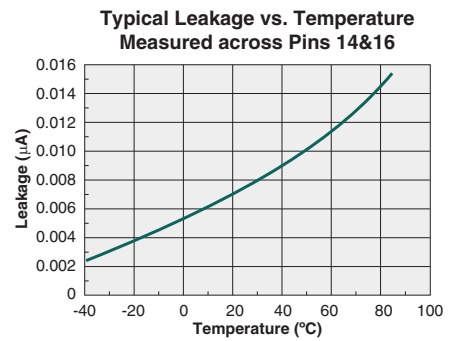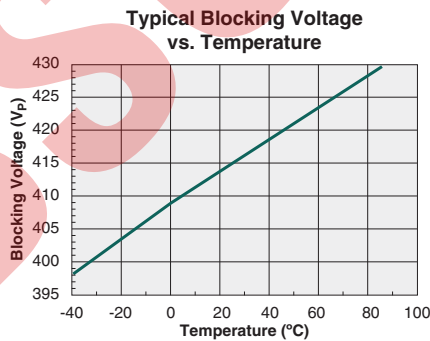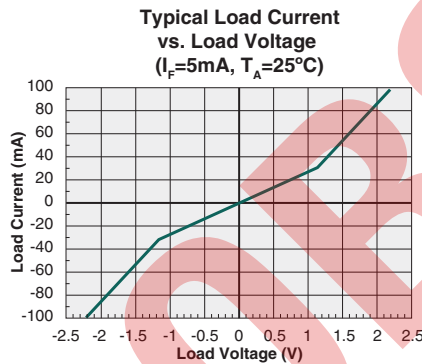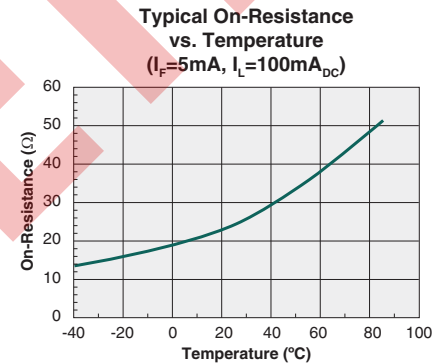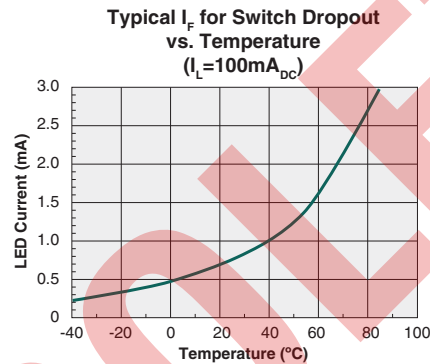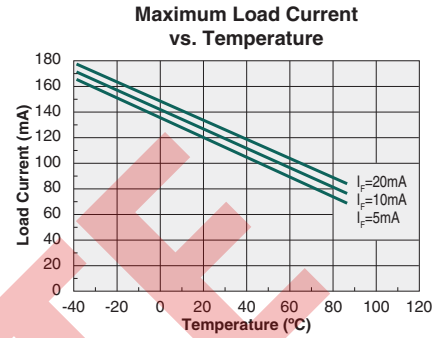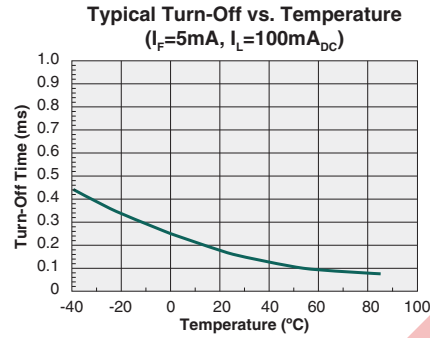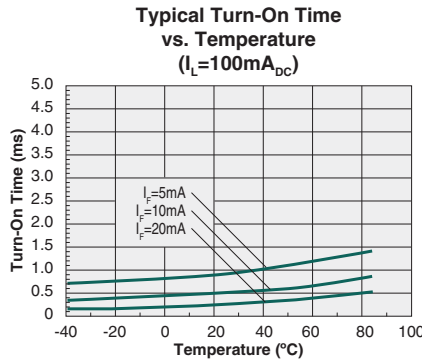
* Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

DETECTOR PERFORMANCE DATA*
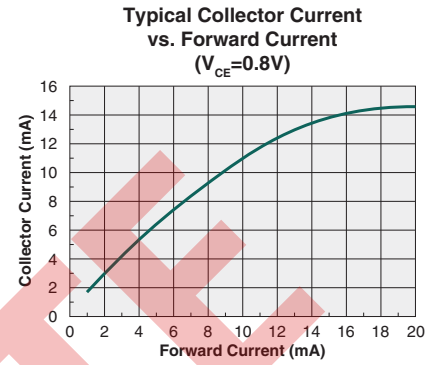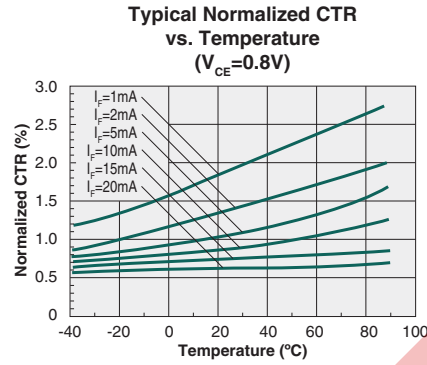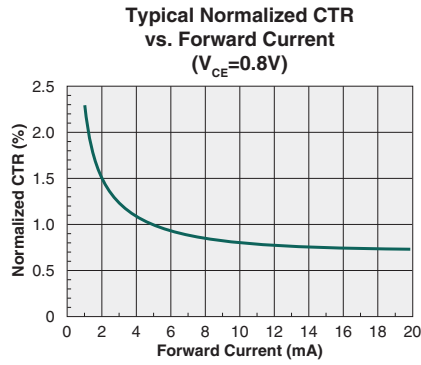
**Typical Normalized CTR
vs. Forward Current
(V$_{CE}$=0.8V)**

**Typical Normalized CTR
vs. Temperature
(V$_{CE}$=0.8V)**

**Typical Collector Current
vs. Forward Current
(V$_{CE}$=0.8V)**

* Unless otherwise noted, data presented in these graphs is typical of device operation at 25ºC.

Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL)** classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
IAD110P	MSL 3

ESD Sensitivity

This product is ESD Sensitive, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the **IPC/JEDEC J-STD-020** Classification Temperature (T_C) and the maximum dwell time the body temperature of these surface mount devices may be (T_C - 5)°C or greater. The Classification Temperature sets the Maximum Body Temperature allowed for these devices during reflow soldering processes.

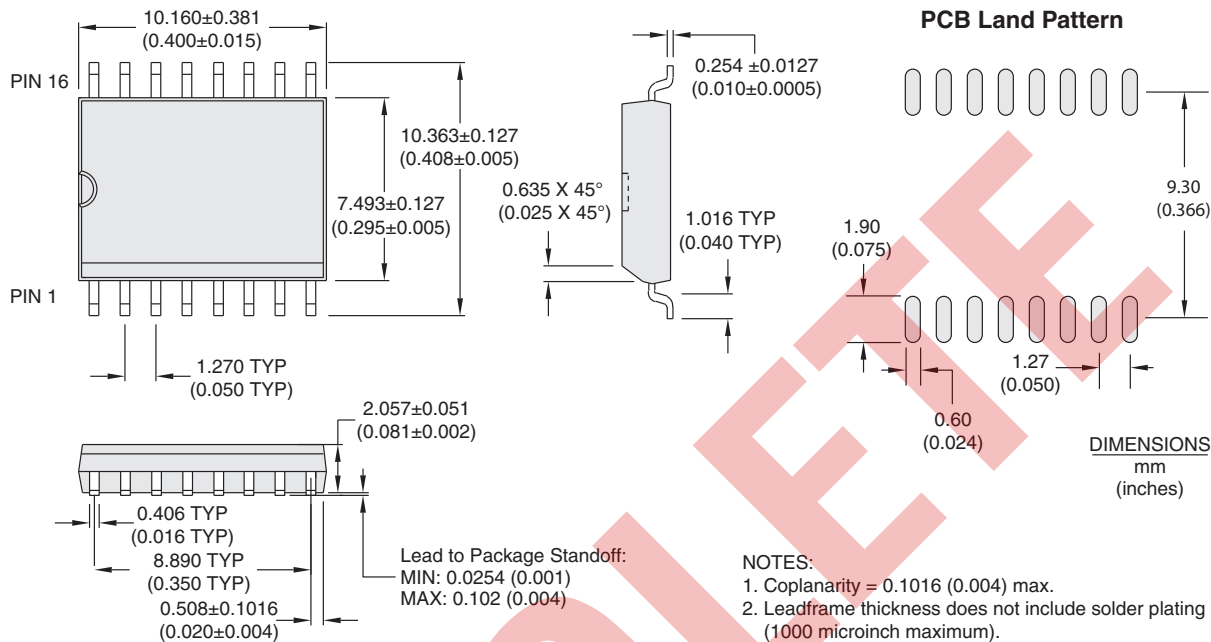| Device	Classification Temperature (T_c)	Dwell Time (t_P)	Max Reflow Cycles
IAD110P	245°C	30 seconds	3

Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.
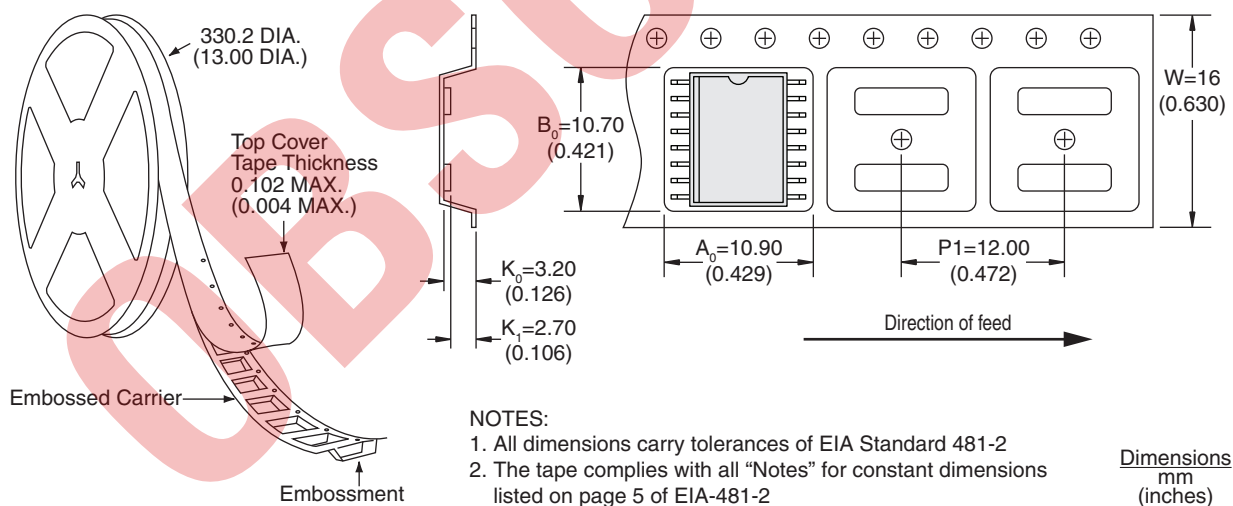
MECHANICAL DIMENSIONS

IAD110P

10.160±0.381
(0.400±0.015)

PIN 16

10.363±0.127
(0.408±0.005)

7.493±0.127
(0.295±0.005)

PIN 1

1.270 TYP
(0.050 TYP)

0.254 ±0.0127
(0.010±0.0005)

0.635 X 45°
(0.025 X 45°)

1.016 TYP
(0.040 TYP)

2.057±0.051
(0.081±0.002)

0.406 TYP
(0.016 TYP)

8.890 TYP
(0.350 TYP)

0.508±0.1016
(0.020±0.004)

Lead to Package Standoff:
MIN: 0.0254 (0.001)
MAX: 0.102 (0.004)

PCB Land Pattern

9.30
(0.366)

1.90
(0.075)

1.27
(0.050)

0.60
(0.024)

DIMENSIONS
mm
(inches)

NOTES:
1. Coplanarity = 0.1016 (0.004) max.
2. Leadframe thickness does not include solder plating
   (1000 microinch maximum).

IAD110PTR Tape & Reel

330.2 DIA.
(13.00 DIA.)

Top Cover
Tape Thickness
0.102 MAX.
(0.004 MAX.)

Embossed Carrier

Embossment

B_0=10.70
(0.421)

K_0=3.20
(0.126)

K_1=2.70
(0.106)

A_0=10.90
(0.429)

P1=12.00
(0.472)

W=16
(0.630)

Direction of feed

NOTES:
1. All dimensions carry tolerances of EIA Standard 481-2
2. The tape complies with all "Notes" for constant dimensions
   listed on page 5 of EIA-481-2

Dimensions
mm
(inches)

For additional information please visit our website at: https://www.ixysic.com

IXYS
A **Littelfuse** Technology