

Date: - 8 March 2018

Data Sheet Issue: P2

High Power Sonic FRD Type E1250HC45E

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{RRM}	Repetitive peak reverse voltage, (note 1)	4500	V
V_{RSM}	Non-repetitive peak reverse voltage, (note 1)	4600	V
$V_{R(d.c.)}$	Maximum reverse d.c. voltage (note 1)	2800	V

	OTHER RATINGS (note 6)	MAXIMUM LIMITS	UNITS
I _{F(AV)M}	Mean forward current, T _{sink} =55°C, (note 2)	1355	Α
$I_{F(AV)M}$	Mean forward current. T _{sink} =100°C, (note 2)	845	Α
I _{F(AV)M}	Mean forward current. T _{sink} =100°C, (note 3)	438	Α
$I_{F(AV)M}$	Mean forward current. T _{sink} =100°C, (note 4)	605	Α
I _{F(RMS)}	Nominal RMS forward current, T _{sink} =25°C, (note 2)	2550	Α
I _{F(d.c.)}	D.C. forward current, T _{sink} =25°C, (note 5)	1623	Α
I _{FSM}	Peak non-repetitive surge t _p =10ms, V _{RM} =60%V _{RRM} , (note 6)	20.5	kA
I _{FSM2}	Peak non-repetitive surge t _p =10ms, V _{RM} ≤10V, (note 6)	22.6	kA
I ² t	I ² t capacity for fusing t _p =10ms, V _{RM} =60%V _{RRM} , (note 6)	2.11×10 ⁶	A ² s
l²t	I²t capacity for fusing t _p =10ms, V _{RM} ≤10V, (note 6)	2.55×10 ⁶	A ² s
Prr	Maximum non-repetitive peak reverse recovery power, (note 8)	6.2	MW
T _{j op}	Operating temperature range	-40 to +140	°C
T_{stg}	Storage temperature range	-40 to +150	°C

Notes:-

- 1) De-rating factor of 0.13% per °C is applicable for T_i below 25°C.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Anode side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Cathode side cooled, single phase; 50Hz, 180° half-sinewave.
- 5) Double side cooled.
- 6) Half-sinewave, 140°C T_i initial.
- 7) Current (I_F) ratings have been calculated using V_{T0} and r_T (see page 2)
- 8) $T_j=T_{jop}$, $I_F=1250A$, di/dt=2000A/ μ s $V_r=2800V$ and $L_s=230nH$. Test circuit and sample waveform are shown in diagram 1. IGBT type T1200EB45E used as switch.



Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V	Maximum pook forward voltage	-	3.25	3.5	I _{FM} =1250A	V
V_{FM}	Maximum peak forward voltage	-	-	5.11	I _{FM} =2500A	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
V_{T0}	Threshold voltage	-	-	2.072	Company range 4255A 4065A (Note 2)	V
r _T	Slope resistance	-	-	1.166	Current range 1355A - 4065A (Note 2)	mΩ
V _{T01}	Threshold voltage	-	-	2.01	2 4 40504 07504	V
r _{T1}	Slope resistance	-	-	1.193	Current range 1250A - 3750A	
V _{FRM} M	Maximum forward recovery voltage	-	-	330	di/dt = 3000A/µs	V
		-	-	170	di/dt = 3000A/µs, T _j =25°C	
	De els sesses es sesses et	-	-	65	Rated V _{RRM}	0
I _{RRM} Peak rev	Peak reverse current	-	-	1.5	Rated V _{RRM} , T _j =25°C	mA
Q _{rr}	Recovered charge	-	1850	2100		μC
Q _{ra}	Recovered charge, 50% Chord	-	600	-		μC
I _{rm}	Reverse recovery current	-	1000	1200	I_{FM} =1250A, t_p =1ms, di/dt=2000A/ μ s, V_r =2800V, 50% Chord (note 3)	Α
t _{rr}	Reverse recovery time, 50% Chord	-	1.20	-		μs
Err	Reverse recovery energy loss	-	3.7	4.2		J
		-	-	0.0105	Double side cooled	K/W
R _{thJK}	Thermal resistance, junction to heatsink	-	-	0.0274	Anode side cooled	
			-	0.0173	Cathode side cooled	K/W
F	Mounting force	32	-	40	(Note 4)	kN
W_t	Weight	-	1.2	-		kg

Notes:-

- 1) Unless otherwise indicated $T_j=140$ °C.
- 2) V_{T0} and r_T were used to calculate the current ratings illustrated on page one.
- 3) Figures 3-7 were compiled using these conditions. Test circuit and sample waveform are shown in diagram 1.
- 4) For clamp forces outside these limits, please consult factory.



Additional information on Ratings and Characteristics

1.0 De-rating Factor

A blocking voltage de-rating factor of 0.13% per °C is applicable to this device for T_j below 25°C.

2.0 ABCD Constants

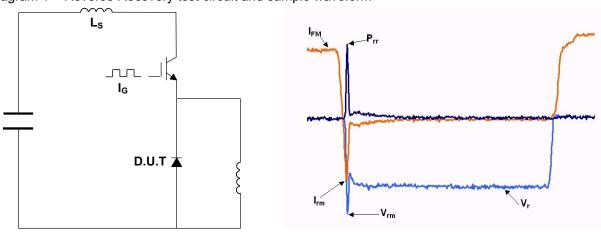
These constants (applicable only over current range of V_F characteristic in Figure 1) are the coefficients of the expression for the forward characteristic given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

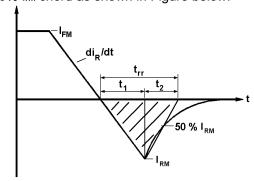
where I_F = instantaneous forward current.

3.0 Reverse recovery ratings

Diagram 1 – Reverse Recovery test circuit and sample waveform



(i) Qra is based on 50% Irm chord as shown in Figure below.



(ii) Q_{rr} is based on a 20 μs integration time.

l.e.
$$Q_{rr}=\int\limits_{0}^{20\mu s}i_{rr}.dt$$



(iii)
$$K Factor = \frac{t_1}{t_2}$$

4.0 Reverse Recovery Loss

The following procedure is recommended for use where it is necessary to include reverse recovery loss.

From waveforms of recovery current obtained from a high frequency shunt (see Note 1) and reverse voltage present during recovery, an instantaneous reverse recovery loss waveform must be constructed. Let the area under this waveform be E joules per pulse. A new sink temperature can then be evaluated from:

$$T_{SINK} = T_{J(MAX)} - E \cdot \left[k + f \cdot R_{th(J-Hs)}\right]$$

Where k = 0.2314 (°C/W)/s

E = Area under reverse loss waveform per pulse in joules (W.s.)

f = Rated frequency in Hz at the original sink temperature.

 $R_{th(J-Hs)} = d.c.$ thermal resistance (°C/W)

The total dissipation is now given by:

$$W_{(tot)} = W_{(original)} + E \cdot f$$

NOTE 1 - Reverse Recovery Loss by Measurement

This device has a low reverse recovered charge and peak reverse recovery current. When measuring the charge, care must be taken to ensure that:

- (a) AC coupled devices such as current transformers are not affected by prior passage of high amplitude forward current.
- (b) A suitable, polarised, clipping circuit must be connected to the input of the measuring oscilloscope to avoid overloading the internal amplifiers by the relatively high amplitude forward current signal.
- (c) Measurement of reverse recovery waveform should be carried out with an appropriate critically damped snubber, connected across diode anode to cathode. The formula used for the calculation of this snubber is shown below:

$$R^2 = 4 \cdot \frac{V_r}{C_s \cdot di/dt}$$

Where: V_r = Commutating source voltage

C_S = Snubber capacitance R = Snubber resistance

5.0 Computer Modelling Parameters

5.1 Device Dissipation Calculations



$$I_{AV} = \frac{-V_{T0} + \sqrt{{V_{T0}}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T}$$

Where V_{T0} =2.072V, r_T =1.166m Ω

ff = form factor (normally unity for fast diode applications)

$$W_{AV} = \frac{\Delta T}{R_{th}}$$
$$\Delta T = T_{i(MAX)} - T_{K}$$

5.2 Calculation of V_F using ABCD Coefficients

The forward characteristic I_F Vs V_F, on page 6 is represented in two ways;

- (i) the well established V_{T0} and r_T tangent used for rating purposes and
- (ii) a set of constants A, B, C, and D forming the coefficients of the representative equation for V_F in terms of I_F given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given in this report for both hot and cold characteristics. The resulting values for V_F agree with the true device characteristic over a current range, which is limited to that plotted.

	25°C Coefficients	140°C Coefficients
Α	0.3115327	0.2510302
В	0.1752856	0.04514263
С	0.4877645×10 ⁻³	0.4997541×10 ⁻³
D	0.04849651	0.06512479



Curves

Figure 1 – Forward characteristics of limit device

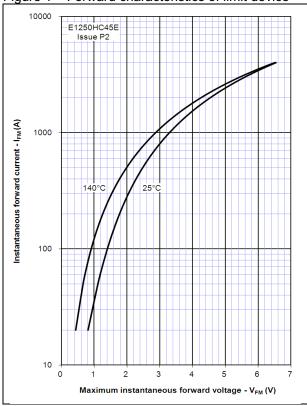


Figure 3 - Maximum recovered charge, Q_{rr}

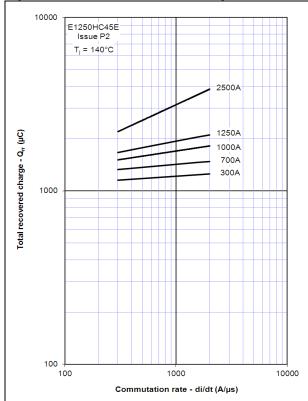


Figure 2 - Maximum forward recovery voltage

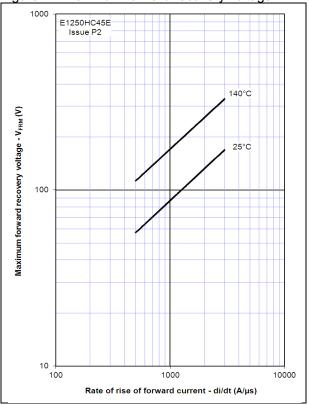
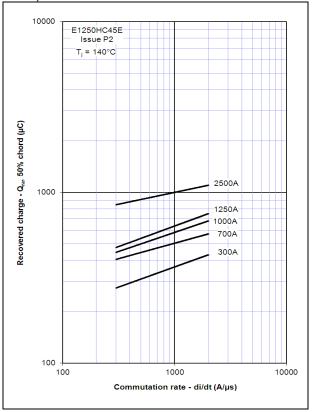
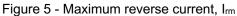


Figure 4 – Maximum recovery charge, Q_{ra} (50% chord)







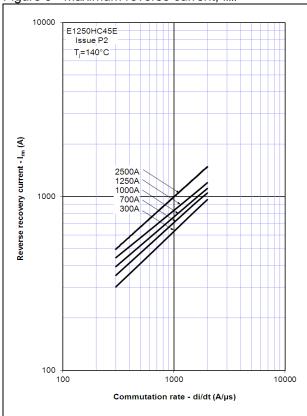


Figure 7 – Typical reverse recovery energy per pulse

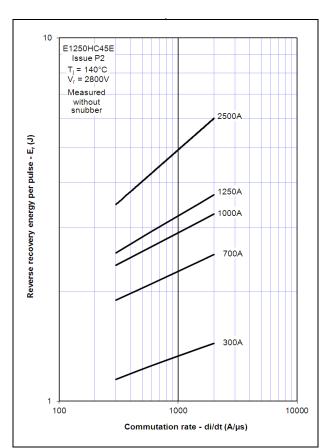


Figure 6 – Maximum recovery time, t_{rr} (50% chord)

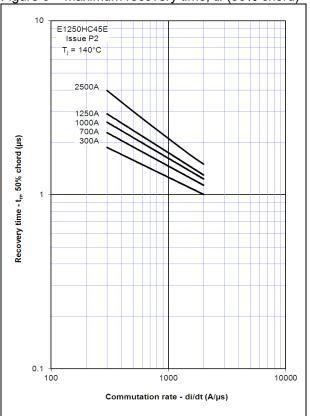
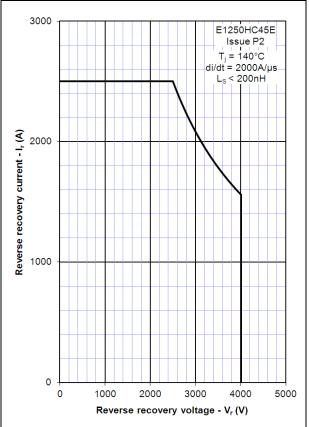
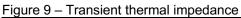
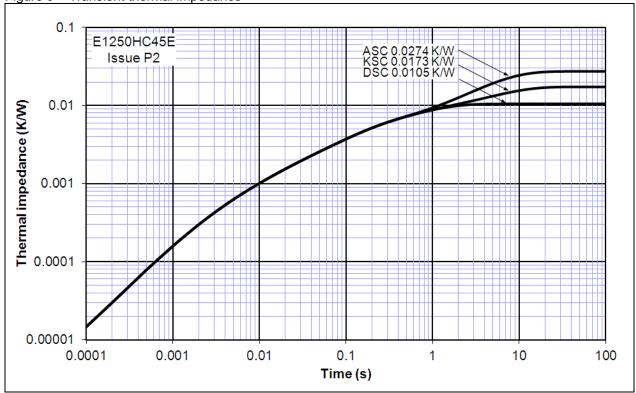


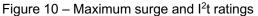
Figure 8 – Safe operating area

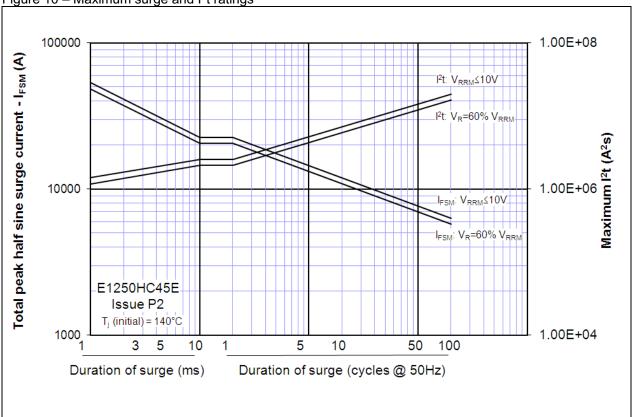






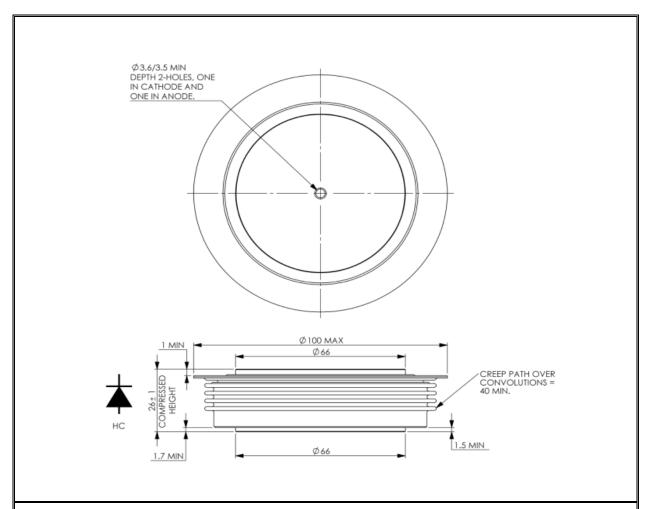








Outline Drawing & Ordering Information



E1250 HC	45	E
Fixed Fixed Type Code outline code	Fixed voltage code V _{RRM} /100 45	Fixed code, product series

Order code: E1250HC45E - 4500V V_{RRM}, 26mm clamp height capsule.

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