

Parameter	Ratings	Units
Blocking Voltage	400	V_P
Load Current	150	mA_{rms} / mA_{DC}
On-Resistance (max)	25	Ω

Features

- Current Limiting
- 3750V_{rms} Input/Output Isolation
- Low Drive Power Requirements
- Greater Reliability than Electromechanical Relays
- No EMI/RFI Generation
- Small 6-Pin Package
- Flammability Rating UL 94 V-0
- Surface Mount and Tape & Reel Version Available

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hook Switch
 - Dial Pulsing
 - Ground Start
 - Ringing Injection
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Industrial Controls

Description

PLA110L is a current-limited, normally open (1-Form-A) solid state relay that uses optically coupled MOSFET technology to provide 3750V_{rms} of input to output isolation.

Its optically coupled outputs, which use the patented OptoMOS architecture, are controlled by a highly efficient infrared LED.

The PLA110L can be used to replace mechanical relays. It offers the superior reliability associated with semiconductor devices, it has no moving parts, and it offers faster, bounce-free switching in a more compact surface mount or thru-hole package.

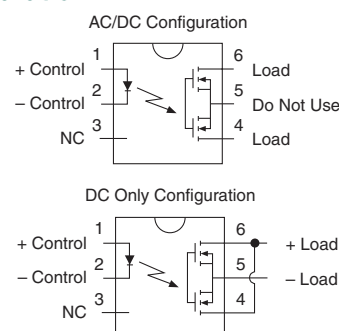
Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate # 1175739
- TUV EN 62368-1: Certificate # B 082667 0008

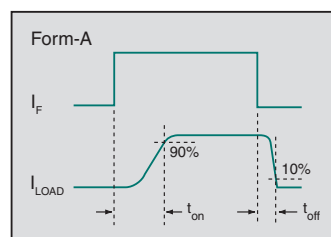
Ordering Information

Part #	Description
PLA110L	6-Pin Dip (50/Tube)
PLA110LS	6-Pin Surface Mount (50/Tube)
PLA110LSTR	6-Pin Surface Mount (1000/Reel)

Pin Configuration



Switching Characteristics of Normally Open Devices



Absolute Maximum Ratings @ 25°C

Parameter	Symbol	Ratings	Units
Blocking Voltage	V_{DRM}	400	V_{P}
Reverse Input Voltage	V_{R}	5	V
Input Control Current	I_{F}	50	mA
Peak (10ms)		1	A
Input Power Dissipation ¹	P_{IN}	150	mW
Total Power Dissipation ²	P_{T}	800	mW
Isolation Voltage Input to Output	V_{ISO}	3750	V_{rms}
Operational Temperature, Ambient	T_{A}	-40 to +85	°C
Storage Temperature	T_{STG}	-40 to +125	°C

¹ Derate linearly 1.33 mW / °C

² Derate output power linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

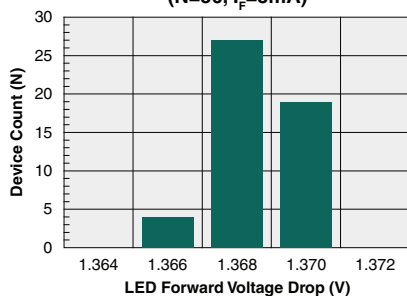
Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Blocking Voltage	$I_{\text{L}}=1\mu\text{A}$	V_{DRM}	400	-	-	V
Load Current	-	I_{L}	-	-	150	$\text{mA}_{\text{rms}} / \text{mA}_{\text{DC}}$
Continuous, AC/DC Configuration			-	-	250	mA_{DC}
Continuous, DC Configuration		I_{CL}	190	235	280	mA
On-Resistance ¹	$I_{\text{L}}=150\text{mA}$ $I_{\text{L}}=250\text{mA}$	R_{ON}	-	-	25	Ω
AC/DC Configuration			-	-	9	
DC Configuration						
Off-State Leakage Current	$V_{\text{L}}=400V_{\text{P}}$	I_{LEAK}	-	-	1	μA
Switching Speeds	$I_{\text{F}}=5\text{mA}$, $V_{\text{L}}=10\text{V}$	t_{on}	-	-	1	ms
Turn-On		t_{off}	-	-	0.5	ms
Turn-Off						
Output Capacitance	$I_{\text{F}}=0\text{mA}$, $V_{\text{L}}=50\text{V}$, $f=1\text{MHz}$	C_{OUT}	-	25	-	pF
Input Characteristics						
Input Control Current to Activate	$I_{\text{L}}=150\text{mA}$	I_{F}	-	-	5	mA
Input Control Current to Deactivate	-	I_{F}	0.4	0.7	-	mA
Input Voltage Drop	$I_{\text{F}}=5\text{mA}$	V_{F}	0.9	1.36	1.5	V
Reverse Input Current	$V_{\text{R}}=5\text{V}$	I_{R}	-	-	10	μA
Common Characteristics						
Input to Output Capacitance	$V_{\text{IO}}=0\text{V}$, $f=1\text{MHz}$	C_{IO}	-	3	-	pF

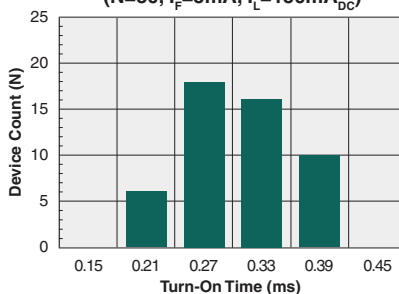
¹ Measurement taken within one second of on-time.

PERFORMANCE DATA*

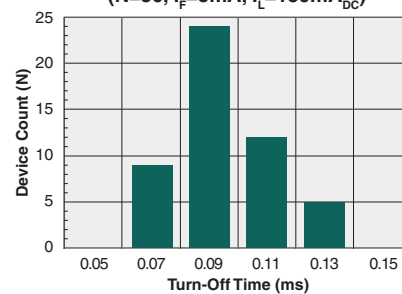
Typical LED Forward Voltage Drop
(N=50, $I_F=5\text{mA}$)



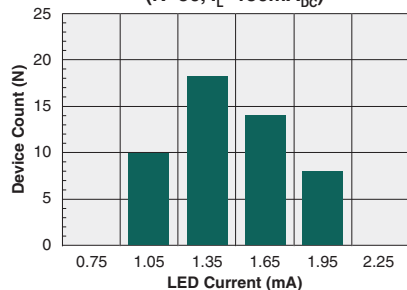
Typical Turn-On Time
(N=50, $I_F=5\text{mA}$, $I_L=150\text{mA}_{DC}$)



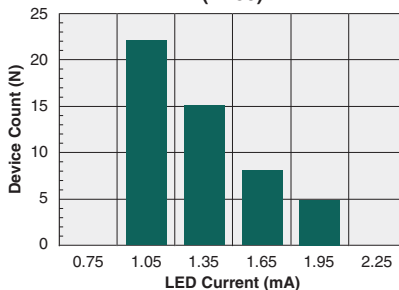
Typical Turn-Off Time
(N=50, $I_F=5\text{mA}$, $I_L=150\text{mA}_{DC}$)



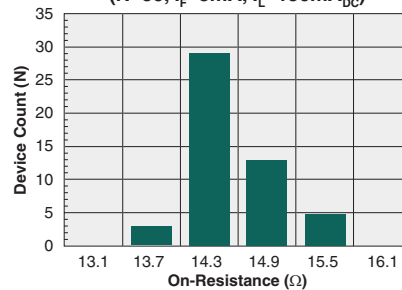
Typical I_F for Switch Operation
(N=50, $I_L=150\text{mA}_{DC}$)



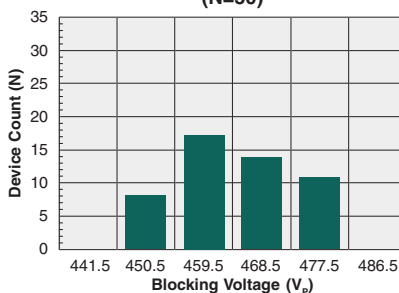
Typical I_F for Switch Dropout
(N=50)



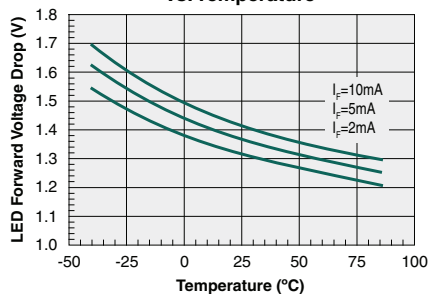
Typical On-Resistance Distribution
(N=50, $I_F=5\text{mA}$, $I_L=150\text{mA}_{DC}$)



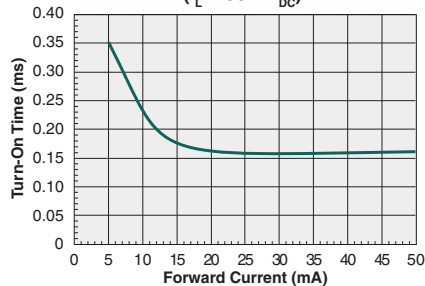
Typical Blocking Voltage Distribution
(N=50)



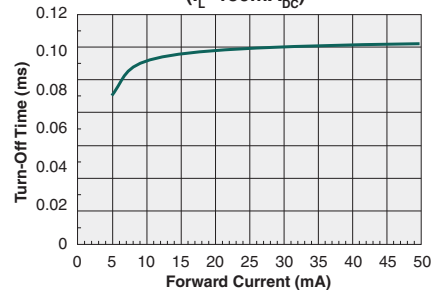
Typical LED Forward Voltage Drop
vs. Temperature



Typical Turn-On Time
vs. LED Forward Current
($I_L=150\text{mA}_{DC}$)

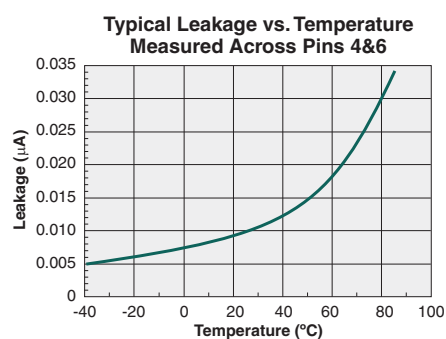
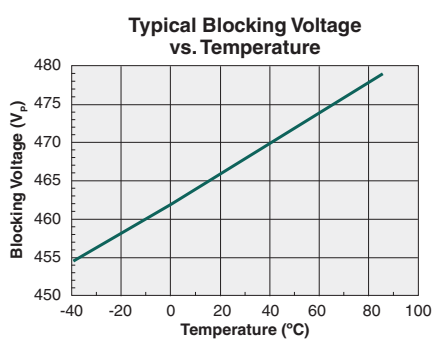
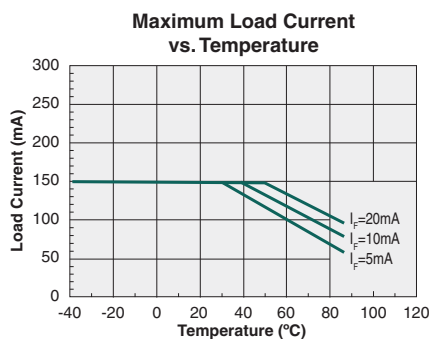
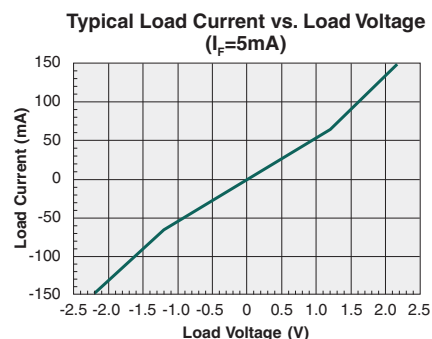
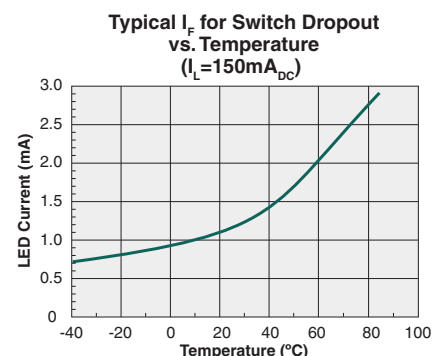
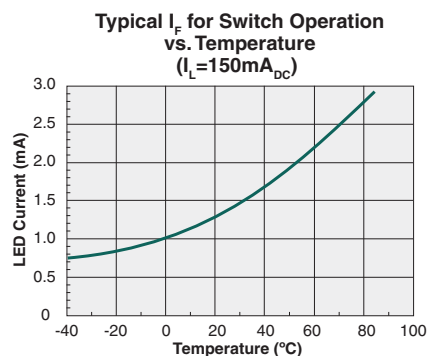
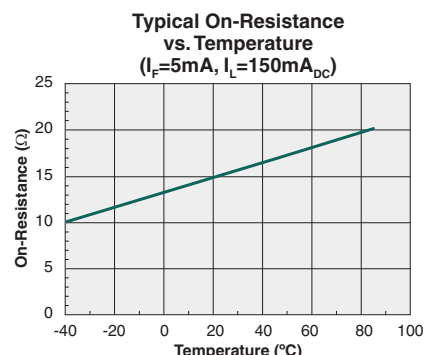
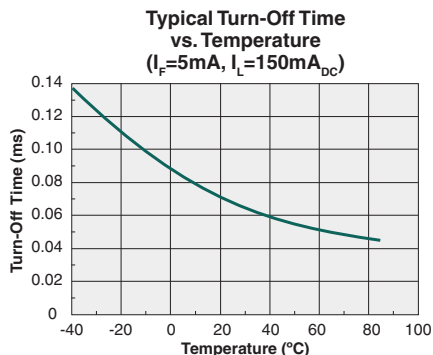
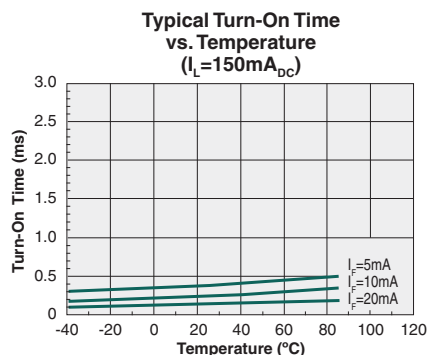


Typical Turn-Off Time
vs. LED Forward Current
($I_L=150\text{mA}_{DC}$)



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C .

PERFORMANCE DATA*



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL)** classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
PLA110LS	MSL 1

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the **IPC/JEDEC J-STD-020** Classification Temperature (T_C) and the maximum total dwell time (t_p) in all reflow processes that the body temperature of these surface mount devices may be ($T_C - 5$)°C or greater. The device's body temperature must not exceed the Classification Temperature at any time during reflow soldering processes.

Device	Classification Temperature (T_C)	Dwell Time (t_p)	Max Reflow Cycles
PLA110LS	250°C	30 seconds	3

For through-hole devices, the maximum pin temperature and maximum dwell time through all solder waves is provided in the table below. Dwell time is the interval beginning when the pins are initially immersed into the solder wave until they exit the solder wave. For multiple waves, the dwell time is from entering the first wave until exiting the last wave. During this time, pin temperatures must not exceed the maximum temperature given in the table below. Body temperature of the device must not exceed the limit shown in the table below at any time during the soldering process.

Device	Maximum Pin Temperature	Maximum Body Temperature	Maximum Dwell Time	Wave Cycles
PLA110L	260°C	250°C	10 seconds*	1

*Total cumulative duration of all waves.

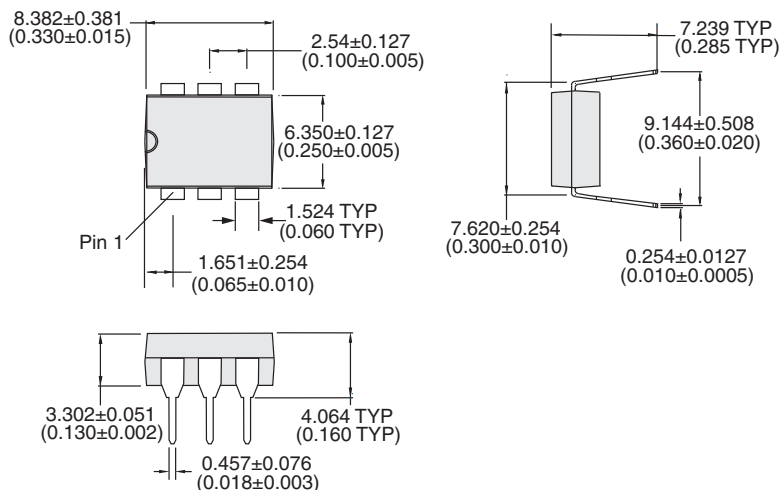
Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.

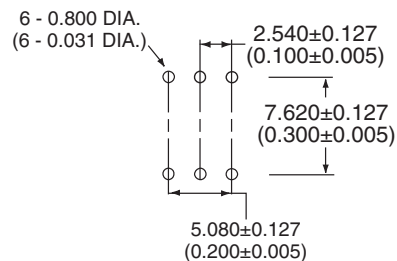


Mechanical Dimensions

PLA110L



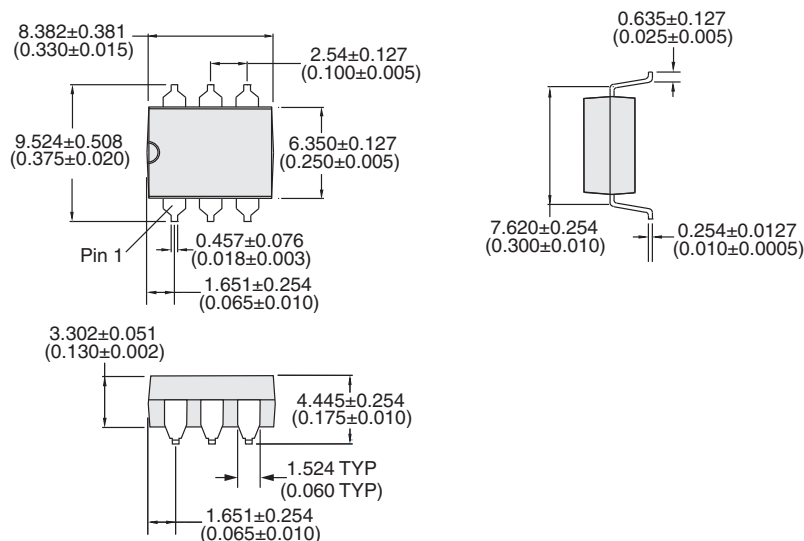
PCB Hole Pattern



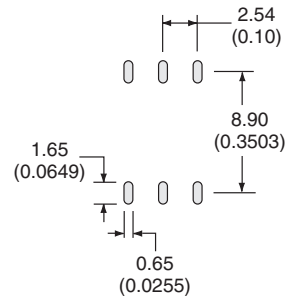
Controlling dimension: inches

Dimensions
mm
(inches)

PLA110LS



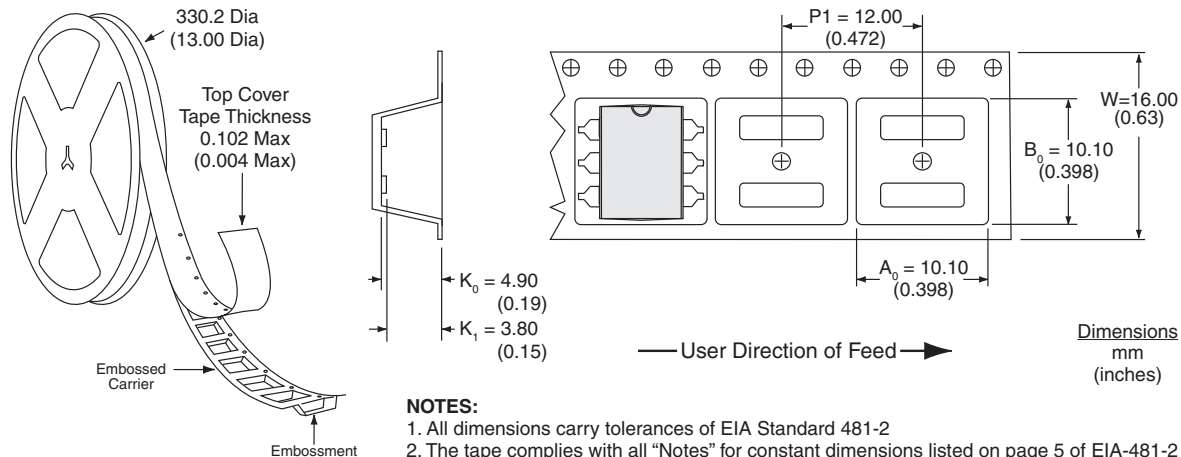
PCB Land Pattern



Controlling dimension: inches

Dimensions
mm
(inches)

PLA110LSTR Tape & Reel



For additional information please visit our website at: <https://www.ixysic.com>