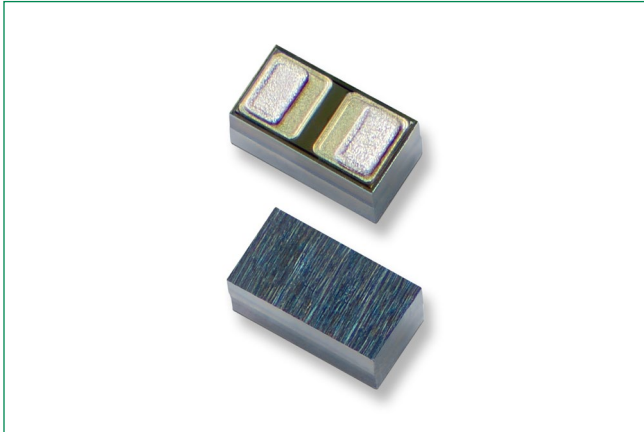


# SPxx Series

## 100W Discrete Bidirectional TVS Diode

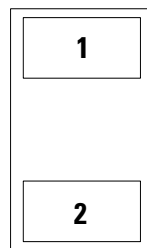


### Web Resources

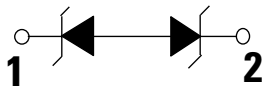


Download ECAD models, order samples, and find technical resources at [www.littelfuse.com](http://www.littelfuse.com)

### Pinout



### Functional Block Diagram



### Description

The SPxx-01WTG-C-HV series is designed to replace multilayer varistors (MLVs) in portable applications, LED lighting modules, and low speed I/Os. It will protect sensitive equipment from damage due to electrostatic discharge (ESD) and other overvoltage transients.

The SPxx-01WTG-C-HV series can safely absorb repetitive ESD strikes above the maximum level of the IEC 61000-4-2 international standard (Level 4,  $\pm 8\text{kV}$  contact discharge) without performance degradation and safely dissipate up to 8A (SP12-01WTG-C-HV) of induced surge current (IEC 61000-4-5,  $t_p=8/20\mu\text{s}$ ) with very low clamping voltages.

### Features & Benefits

- ESD, IEC 61000-4-2,  $\pm 30\text{kV}$  contact,  $\pm 30\text{kV}$  air
- EFT, IEC 61000-4-4, 40A (5/50ns)
- IEC 61000-4-5, 2nd Edition: 8/20 Surge, 8A Surge Immunity. SP12-01WTG-C-HV.
- Low clamping voltage
- Low leakage current
- Halogen free, Lead free and RoHS compliant

### Applications

- LED Lighting Modules
- Portable Instrumentation
- General Purpose I/O
- Mobile & Handhelds
- RS232 / RS485
- CAN bus

#### Life Support Note:

Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.

# SPxx Series

## 100W Discrete Bidirectional TVS Diode

### Absolute Maximum Ratings

Symbol	Parameter	Value	Units
$P_{pk}$	Peak Pulse Power ( $t_p=8/20\mu s$ )	100	W
$T_{OP}$	Operating Temperature	-40 to 125	°C
$T_{STOR}$	Storage Temperature	-55 to 150	°C

**Caution:** Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the component. This is a stress only rating and operation of the component at these or any other conditions above those indicated in the operational sections of this specification is not implied.

### Thermal Information

Parameter	Rating	Units
Maximum Junction Temperature	150	°C
Maximum Lead Temperature (Soldering 20-40s)	260	°C

### SP12-01WTG-C-HV Electrical Characteristics ( $T_{OP}=25^\circ C$ )

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Reverse Standoff Voltage	$V_{RWM}$	$I_R \leq 1\mu A$			12.0	V
Reverse Breakdown Voltage	$V_{BR}$	$I_R = 1mA$	13.3			V
Leakage Current	$I_{LEAK}$	$V_R = 12V$			0.1	$\mu A$
Clamp Voltage <sup>1</sup>	$V_C$	$I_{PP} = 1A, t_p = 8/20\mu s, Fwd$		16		V
		$I_{PP} = 8A, t_p = 8/20\mu s, Fwd$		19		V
Dynamic Resistance <sup>2</sup>	$R_{DYN}$	TLP, $t_p = 100ns$		0.4		$\Omega$
Peak Pulse Current	$I_{PP}$	$t_p = 8/20\mu s$			8.0	A
ESD Withstand Voltage <sup>1</sup>	$V_{ESD}$	IEC61000-4-2 (Contact Discharge)	$\pm 30$			kV
		IEC61000-4-2 (Air Discharge)	$\pm 30$			kV
Diode Capacitance <sup>1</sup>	$C_{D-GND}$	Reverse Bias=0V, f=1MHz		26	30	pF

**Note:**

<sup>1</sup> Parameter is guaranteed by design and/or component characterization.

<sup>2</sup> Transmission Line Pulse (TLP) test setting : Std.TDR(50 $\Omega$ ), $t_p=100ns$ ,  $t_r=0.2ns$  ITLP and VTLP averaging window: start  $t_1=70ns$  to end  $t_2=80ns$

### SP15-01WTG-C-HV Electrical Characteristics ( $T_{OP}=25^\circ C$ )

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Reverse Standoff Voltage	$V_{RWM}$	$I_R \leq 1\mu A$			15.0	V
Reverse Breakdown Voltage	$V_{BR}$	$I_R = 1mA$	16.7			V
Leakage Current	$I_{LEAK}$	$V_R = 15V$			0.1	$\mu A$
Clamp Voltage <sup>1</sup>	$V_C$	$I_{PP} = 1A, t_p = 8/20\mu s, Fwd$		21		V
		$I_{PP} = 5A, t_p = 8/20\mu s, Fwd$		27		V
Dynamic Resistance <sup>2</sup>	$R_{DYN}$	TLP, $t_p = 100ns$		0.43		$\Omega$
Peak Pulse Current	$I_{PP}$	$t_p = 8/20\mu s$			5.0	A
ESD Withstand Voltage <sup>1</sup>	$V_{ESD}$	IEC61000-4-2 (Contact Discharge)	$\pm 30$			kV
		IEC61000-4-2 (Air Discharge)	$\pm 30$			kV
Diode Capacitance <sup>1</sup>	$C_{I/O-GND}$	Reverse Bias=0V, f=1MHz		21	24	pF

**Note:**

<sup>1</sup> Parameter is guaranteed by design and/or component characterization.

<sup>2</sup> Transmission Line Pulse (TLP) test setting : Std.TDR(50 $\Omega$ ), $t_p=100ns$ ,  $t_r=0.2ns$  ITLP and VTLP averaging window: start  $t_1=70ns$  to end  $t_2=80ns$

# SPxx Series

## 100W Discrete Bidirectional TVS Diode

### SP24-01WTG-C-HV Electrical Characteristics (T<sub>OP</sub>=25°C)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Reverse Standoff Voltage	V <sub>RWM</sub>	I <sub>R</sub> ≤ 1 μA			24.0	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>R</sub> = 1 mA	26.7			V
Leakage Current	I <sub>LEAK</sub>	V <sub>R</sub> = 24V			0.1	μA
Clamp Voltage <sup>1</sup>	V <sub>C</sub>	I <sub>PP</sub> = 1A, t <sub>p</sub> = 8/20 μs, Fwd		32		V
		I <sub>PP</sub> = 3.0A, t <sub>p</sub> = 8/20 μs, Fwd		40		V
Dynamic Resistance <sup>2</sup>	R <sub>DYN</sub>	TLP, t <sub>p</sub> = 100ns		0.7		Ω
Peak Pulse Current	I <sub>PP</sub>	t <sub>p</sub> = 8/20 μs			3.0	A
ESD Withstand Voltage <sup>1</sup>	V <sub>ESD</sub>	IEC61000-4-2 (Contact Discharge)	±18			kV
		IEC61000-4-2 (Air Discharge)	±24			kV
Diode Capacitance <sup>1</sup>	C <sub>I/O-GND</sub>	Reverse Bias=0V, f=1MHz		13	17	pF

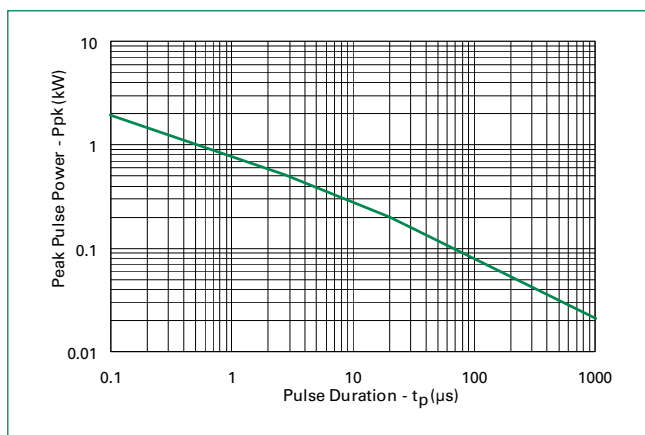
**Note:**<sup>1</sup> Parameter is guaranteed by design and/or component characterization.<sup>2</sup> Transmission Line Pulse (TLP) test setting : Std.TDR(50Ω),tp=100ns, tr=0.2ns ITLP and VTLP averaging window: start t1=70ns to end t2=80ns

### SP36-01WTG-C-HV Electrical Characteristics (T<sub>OP</sub>=25°C)

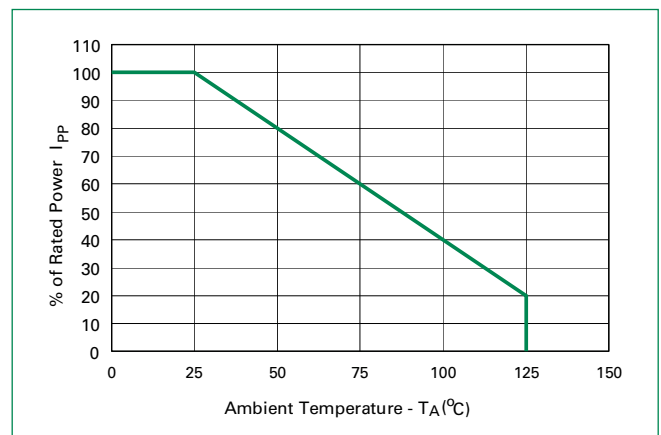
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Reverse Standoff Voltage	V <sub>RWM</sub>	I <sub>R</sub> ≤ 1 μA			36.0	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>R</sub> = 1 mA	40.0			V
Leakage Current	I <sub>LEAK</sub>	V <sub>R</sub> = 36V			0.1	μA
Clamp Voltage <sup>1</sup>	V <sub>C</sub>	I <sub>PP</sub> = 1A, t <sub>p</sub> = 8/20 μs, Fwd		48		V
Dynamic Resistance <sup>2</sup>	R <sub>DYN</sub>	TLP, t <sub>p</sub> = 100ns		1.4		Ω
Peak Pulse Current	I <sub>PP</sub>	t <sub>p</sub> = 8/20 μs			1.5	A
ESD Withstand Voltage <sup>1</sup>	V <sub>ESD</sub>	IEC61000-4-2 (Contact Discharge)	±10			kV
		IEC61000-4-2 (Air Discharge)	±15			kV
Diode Capacitance <sup>1</sup>	C <sub>I/O-GND</sub>	Reverse Bias=0V, f=1MHz		10	13	pF

**Note:**<sup>1</sup> Parameter is guaranteed by design and/or component characterization.<sup>2</sup> Transmission Line Pulse (TLP) test setting : Std.TDR(50Ω),tp=100ns, tr=0.2ns ITLP and VTLP averaging window: start t1=70ns to end t2=80ns

### Non-Repetitive Peak Pulse Power vs. Pulse Time



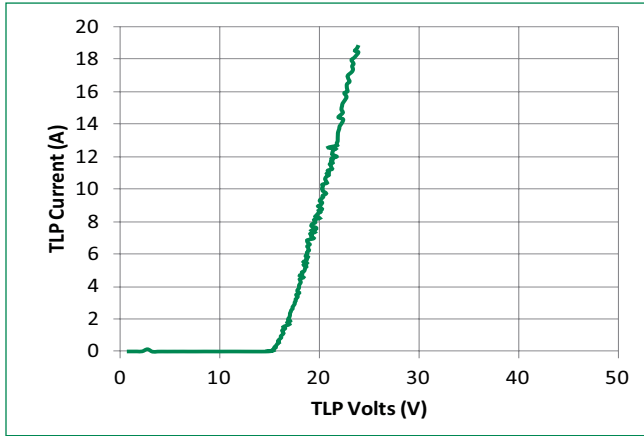
### Power Derating Curve



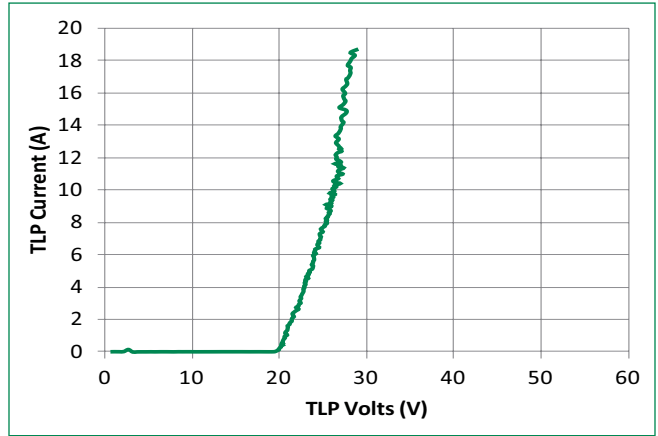
# SPxx Series

## 100W Discrete Bidirectional TVS Diode

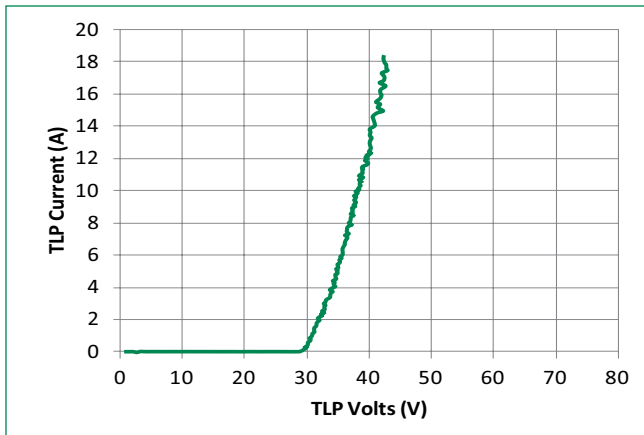
SP12-01WTG-C-HV Transmission Line Pulsing(TLP) Plot



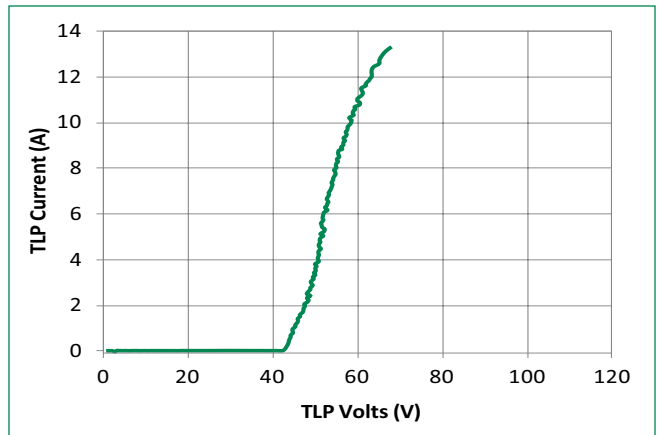
SP15-01WTG-C-HV Transmission Line Pulsing(TLP) Plot



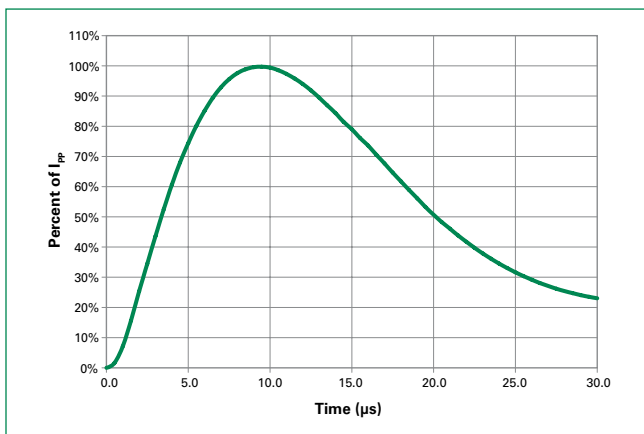
SP24-01WTG-C-HV Transmission Line Pulsing(TLP) Plot



SP36-01WTG-C-HV Transmission Line Pulsing(TLP) Plot



### Pulse Waveform

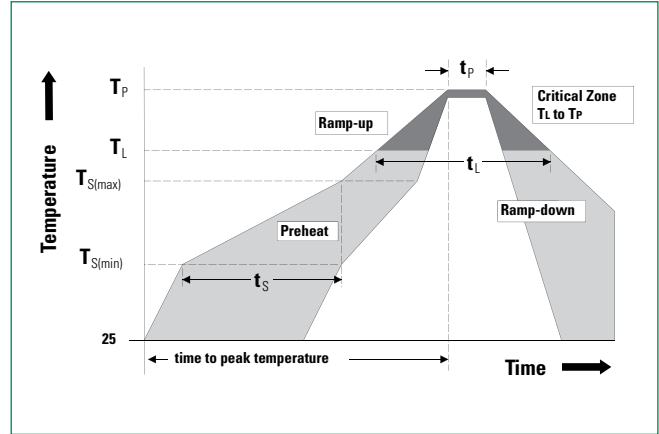


# SPxx Series

## 100W Discrete Bidirectional TVS Diode

### Soldering Parameters

<b>Reflow Condition</b>		Pb – Free assembly
<b>Pre Heat</b>	- Temperature Min ( $T_{s(min)}$ )	150°C
	- Temperature Max ( $T_{s(max)}$ )	200°C
	- Time (min to max) ( $t_s$ )	60 – 120 secs
<b>Average ramp up rate (Liquidus) Temp (<math>T_L</math>) to peak</b>		3°C/second max
<b><math>T_{S(max)}</math> to <math>T_L</math> - Ramp-up Rate</b>		3°C/second max
<b>Reflow</b>	- Temperature ( $T_L$ ) (Liquidus)	217°C
	- Temperature ( $t_L$ )	60 – 150 seconds
<b>Peak Temperature (<math>T_p</math>)</b>		260 <sup>+0/-5</sup> °C
<b>Time within 5°C of actual peak Temperature (<math>t_p</math>)</b>		30 seconds
<b>Ramp-down Rate</b>		6°C/second max
<b>Time 25°C to peak Temperature (<math>T_p</math>)</b>		8 minutes Max.
<b>Do not exceed</b>		260°C



### Product Characteristics

<b>Lead Plating</b>	Matte Tin
<b>Lead Material</b>	Copper bump
<b>Substitute Material</b>	Silicon
<b>Flammability</b>	'UL Recognized compound meeting flammability rating V-0

**Notes :**

1. All dimensions are in millimeters
2. Dimensions include solder plating.
3. Dimensions are exclusive of mold flash & metal burr.
4. Bto is facing up for mold and facing down for trim/form, i.e. reverse trim/form.
5. Package surface matte finish VDI 11-13.

### Ordering Information

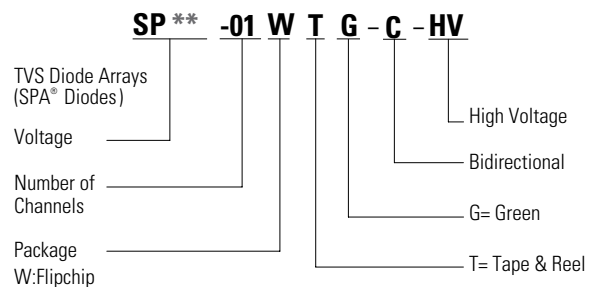
Part Number	Package	Marking	Min. Order Qty.
SP12-01WTG-C-HV	FLIPCHIP	2	10000
SP15-01WTG-C-HV	FLIPCHIP	5	10000
SP24-01WTG-C-HV	FLIPCHIP	4	10000
SP36-01WTG-C-HV	FLIPCHIP	6	10000

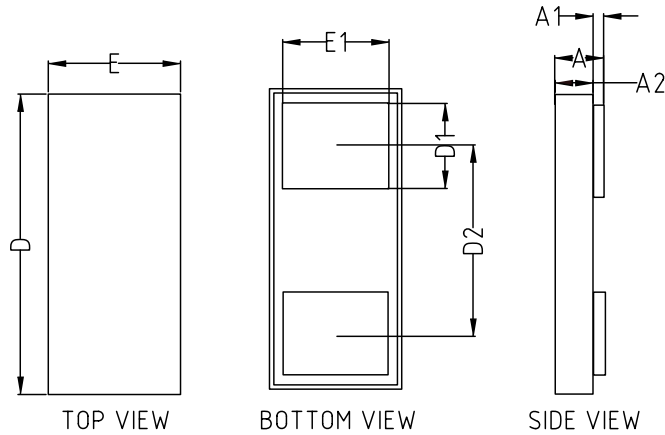
### Part Marking System



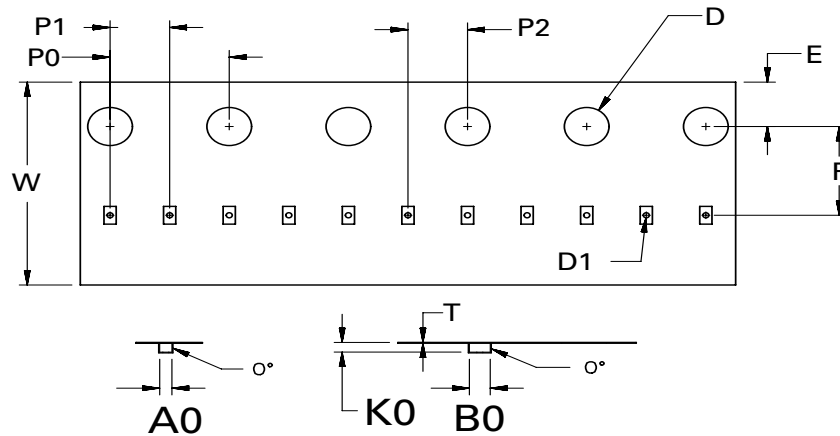
- 2: SP12-01WTG-C-HV
- 5: SP15-01WTG-C-HV
- 4: SP24-01WTG-C-HV
- 6: SP36-01WTG-C-HV

### Part Numbering System

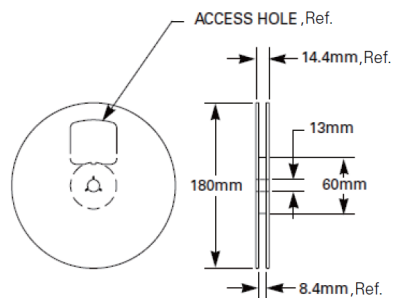


**SPxx Series****100W Discrete Bidirectional TVS Diode****Package Dimensions – FLIPCHIP**

Symbol	Package		FLIPCHIP				
	JEDEC	MO-236					
		Millimeters			Inches		
	Min	Typ	Max	Min	Typ	Max	
<b>A</b>	0.273	0.301	0.329	0.011	0.012	0.013	
<b>A1</b>	0.008	0.011	0.014	0.000	0.000	0.001	
<b>A2</b>	0.265	0.290	0.315	0.011	0.012	0.013	
<b>D</b>	0.605	0.640	0.655	0.024	0.026	0.027	
<b>D1</b>	0.145	0.15	0.155	0.006	0.006	0.006	
<b>D2</b>	0.400 REF			0.016 REF			
<b>E</b>	0.305	0.340	0.355	0.012	0.014	0.015	
<b>E1</b>	0.245	0.25	0.255	0.010	0.010	0.010	

**Embossed Carrier Tape & Reel Specification – FLIPCHIP**

Symbol	Millimeters
<b>A0</b>	0.41+/-0.03
<b>B0</b>	0.70+/-0.03
<b>D</b>	$\varnothing 1.50 + 0.10$
<b>D1</b>	$\varnothing 0.20 +/- 0.05$
<b>E</b>	1.75+/-0.10
<b>F</b>	3.50+/-0.05
<b>K0</b>	0.38+/-0.03
<b>P0</b>	4.00+/-0.10
<b>P1</b>	2.00+/-0.05
<b>P2</b>	2.00+/-0.05
<b>W</b>	8.00 + 0.30 -0.10
<b>T</b>	0.23+/-0.02



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