IXYS UK WESTCODE

Date:- 18th July, 2014

Data Sheet Issue:- 1

Wespack Phase Control Thyristor

Types N3597ML020 to N3597ML060

Development Type No.: NX313ML020-060

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{DRM}	Repetitive peak off-state voltage, (note 1)	200-600	V
V_{DSM}	Non-repetitive peak off-state voltage, (note 1)	300-700	V
V_{RRM}	Repetitive peak reverse voltage, (note 1)	200-600	V
V_{RSM}	Non-repetitive peak reverse voltage, (note 1)	300-700	V

	OTHER RATINGS		MAXIMUM LIMITS	UNITS
$I_{T(AV)M}$	Maximum average on-state current, T_{sink}=55°C, (note 2)		3597	A
$I_{T(AV)M}$	Maximum average on-state current. T_{sink}=85°C, (note 2)		2588	A
$I_{T(AV)M}$	Maximum average on-state current. T_{sink}=85°C, (note 3)		1244	A
$I_{T(RMS)M}$	Nominal RMS on-state current, T_{sink}=25°C, (note 2)		7030	A
$I_{T(d.c.)}$	D.C. on-state current, T_{sink}=25°C, (note 4)		5616	A
I_{TSM}	Peak non-repetitive surge t_p=10ms, V_{rm}=60%V_{RRM}, (note 5)		45.4	kA
I_{TSM2}	Peak non-repetitive surge t_p=10ms, V_{rm}≤10V, (note 5)		50.0	kA
I^2t	I^2t capacity for fusing t_p=10ms, V_{rm}=60%V_{RRM}, (note 5)		10.3×10^6	A^2s
I^2t	I^2t capacity for fusing t_p=10ms, V_{rm}≤10V, (note 5)		12.5×10^6	A^2s
$(di/dt)_{cr}$	Critical rate of rise of on-state current (note 6)	(continuous, 50Hz)	100	A/µs
		(repetitive, 50Hz, 60s)	200	
		(non-repetitive)	400	
V_{RGM}	Peak reverse gate voltage		5	V
$P_{G(AV)}$	Mean forward gate power		4	W
P_{GM}	Peak forward gate power		30	W
$T_{j\ op}$	Operating temperature range		-40 to +140	°C
T_{stg}	Storage temperature range		-40 to +150	°C

Notes:-

1) De-rating factor of 0.13% per °C is applicable for T_j below 25°C.
2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
3) Cathode side cooled, single phase; 50Hz, 180° half-sinewave.
4) Double side cooled.
5) Half-sinewave, 140°C T_j initial.
6) V_D=67% V_{DRM}, I_{TM}=2000A, I_{FG}=2A, t_r≤0.5µs, T_{case}=140°C.

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V_{TM}	Maximum peak on-state voltage	-	-	1.05	I_{TM}=4000A	V
V_{TM}	Maximum peak on-state voltage	-	-	1.53	I_{TM}=13000A	V
V_{T0}	Threshold voltage	-	-	0.840		V
r_T	Slope resistance	-	-	0.053		mΩ
$(dv/dt)_{cr}$	Critical rate of rise of off-state voltage	1000	-	-	V_D=80% V_{DRM}, linear ramp, gate o/c	V/µs
I_{DRM}	Peak off-state current	-	-	100	Rated V_{DRM}	mA
I_{RRM}	Peak reverse current	-	-	100	Rated V_{RRM}	mA
V_{GT}	Gate trigger voltage	-	-	3.0	T_j=25°C V_D=10V, I_T=3A	V
I_{GT}	Gate trigger current	-	-	300		mA
V_{GD}	Gate non-trigger voltage	-	-	0.25	Rated V_{DRM}	V
I_H	Holding current	-	-	1000	T_j=25°C	mA
t_{gd}	Gate-controlled turn-on delay time	-	0.7	2.0	V_D=67% V_{DRM}, I_T=2000A, di/dt=10A/µs, I_{FG}=2A, t_r=0.5µs, T_j=25°C	µs
t_{gt}	Turn-on time	-	1.3	3.0		µs
Q_{rr}	Recovered charge	-	1300	-	I_{TM}=1000A, t_p=1000µs, di/dt=10A/µs, V_r=50V	µC
Q_{ra}	Recovered charge, 50% Chord	-	750	850		µC
I_{rr}	Reverse recovery current	-	95	-		A
t_{rr}	Reverse recovery time	-	16	-		µs
t_q	Turn-off time	-	200	-	I_{TM}=1000A, t_p=1000µs, di/dt=10A/µs, V_r=50V, V_{dr}=80%V_{DRM}, dV_{dr}/dt=20V/µs	µs
		-	260	-	I_{TM}=1000A, t_p=1000µs, di/dt=10A/µs, V_r=50V, V_{dr}=80%V_{DRM}, dV_{dr}/dt=200V/µs	
R_{thJK}	Thermal resistance, junction to heatsink	-	-	0.018	Double side cooled	K/W
		-	-	0.033	Anode side cooled	K/W
		-	-	0.044	Cathode side cooled	K/W
F	Mounting force	25	-	31	Note 2.	kN
W_t	Weight	-	550	-		g

Notes:-
1) Unless otherwise indicated T_j=140°C.
2) For other clamp forces, please consult factory.

Curves

Figure 1 – On-state characteristics of Limit device

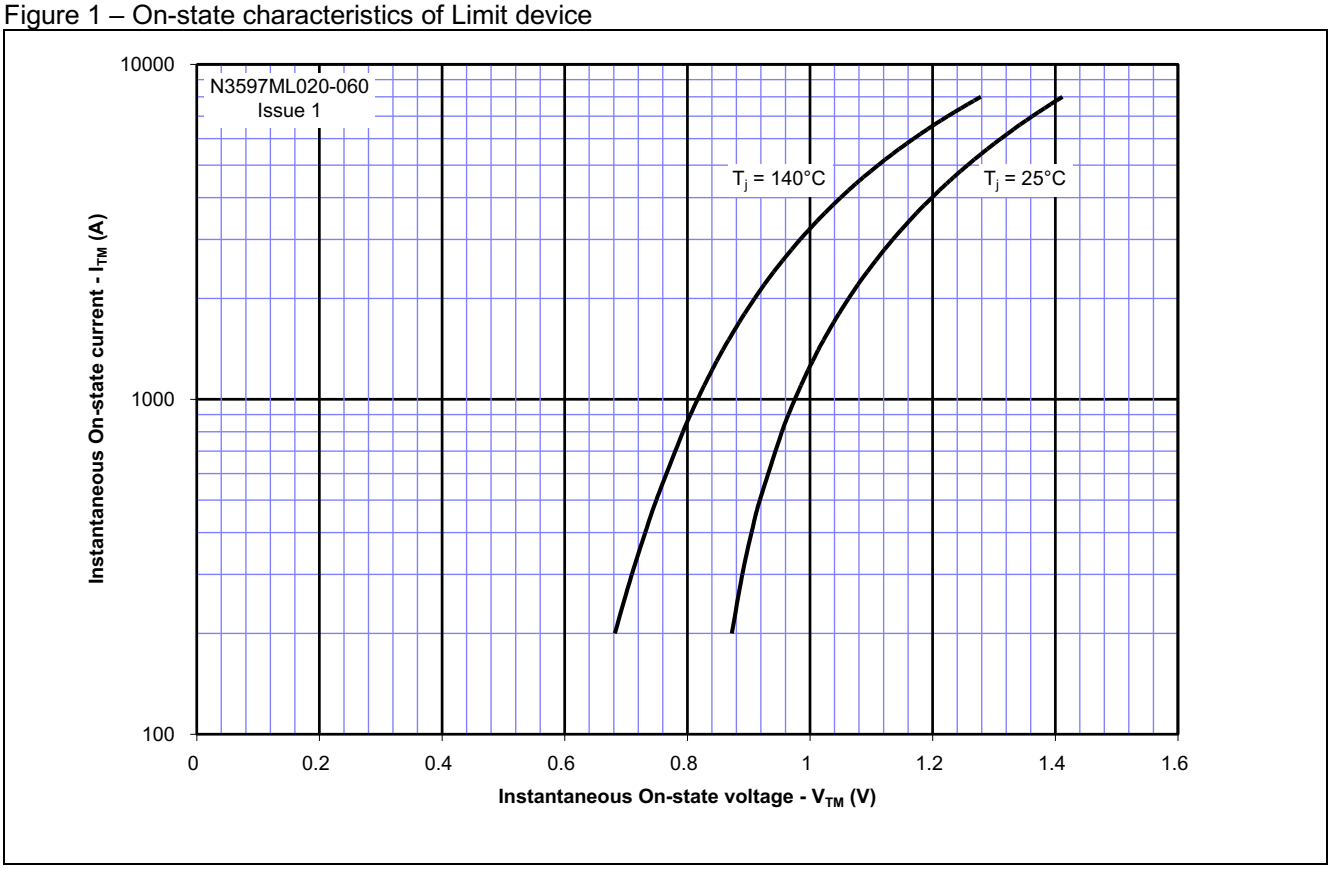


Figure 2 – Transient thermal impedance

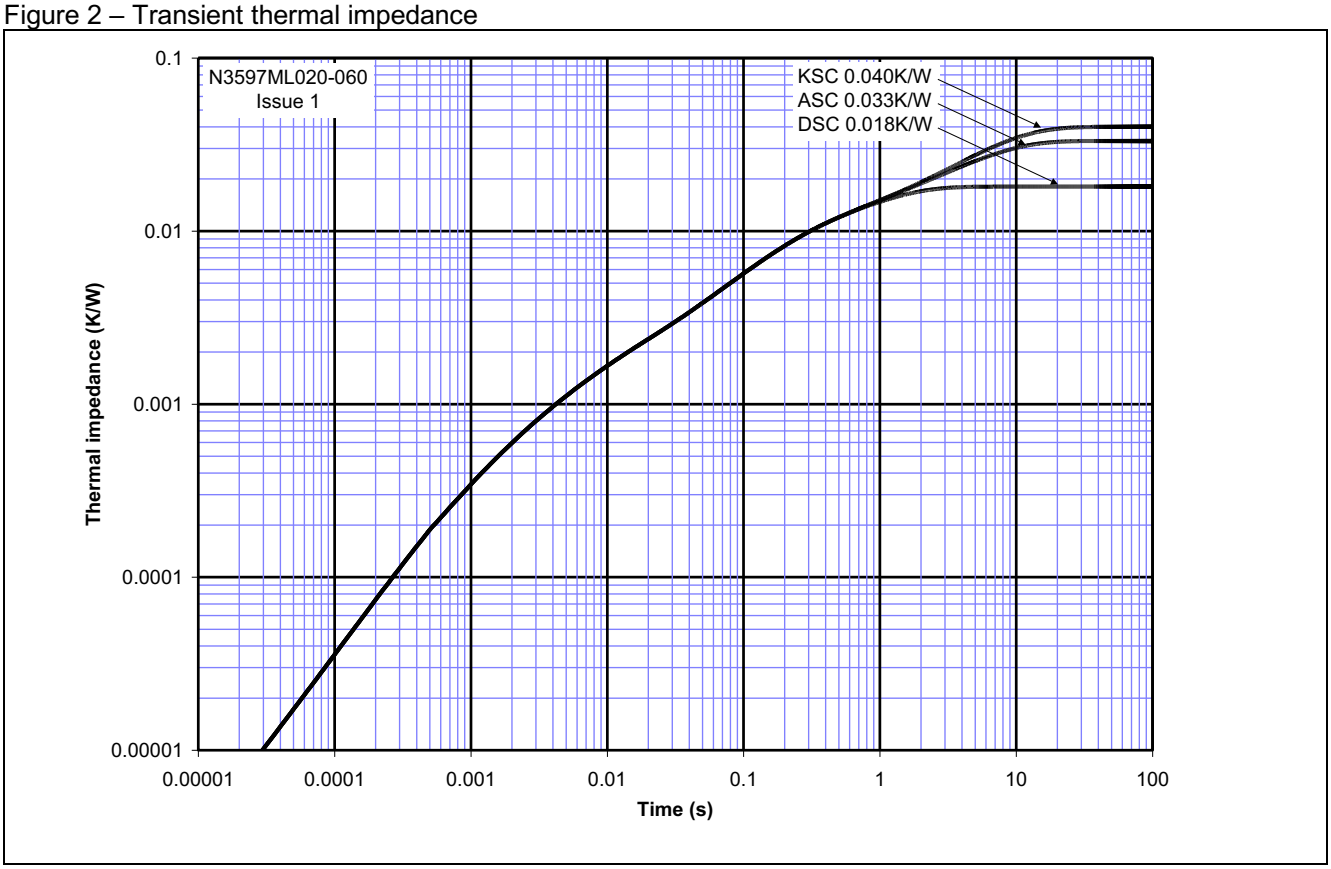
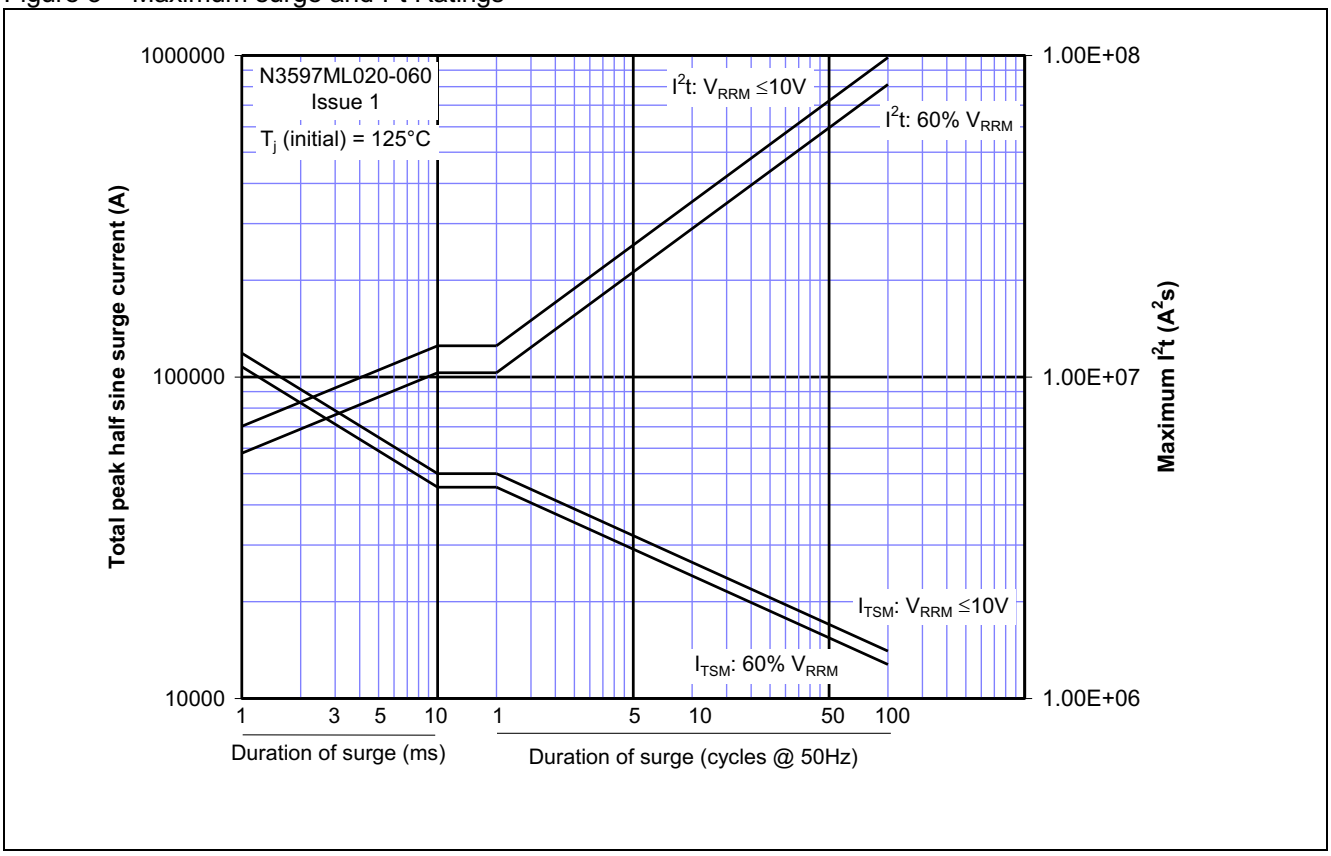
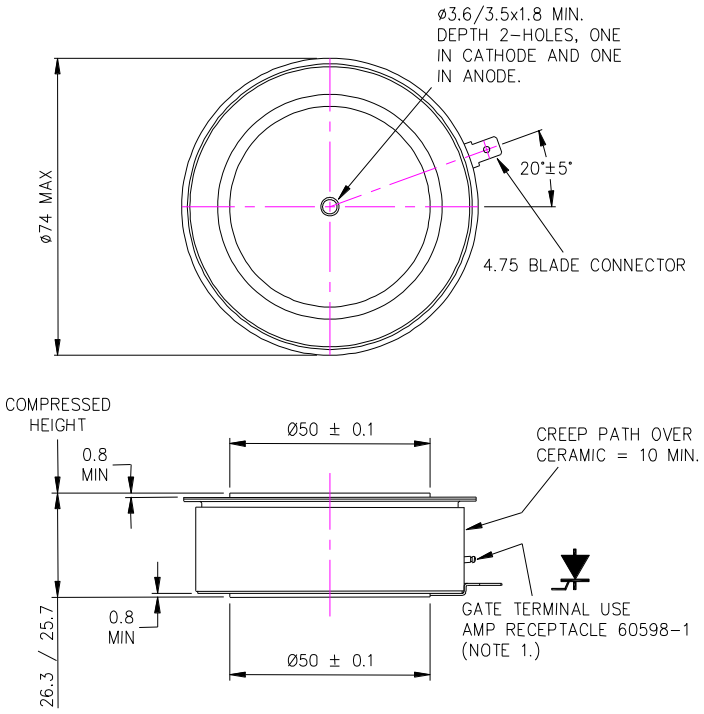


Figure 3 – Maximum surge and I^2t Ratings



Outline Drawing & Ordering Information



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ORDERING INFORMATION		(Please quote 10 digit code as below)	
N3597	**ML**	**♦♦**	**0**
Fixed Type Code	Fixed outline code	Voltage code V_{DRM}/100 16-18	Fixed turn-off time code

Order code: N3597ML060 – 600V V_{DRM}, V_{RRM}, 26mm clamp height capsule.

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