

SIDACtor® Protection Thyristors Commercial Product Reliability Information

This report shows reliability general results of commercial product families from Littelfuse SIDACtor products. All test standards listed are per the Mil-Std-750 unless otherwise stated.

For more information about any specific device, please contact sales@Littelfuse.com for further details.

Test	Standard	Test Condition	Sample Size
Pre-conditioning	JESD22A-113(SMD)	24 hours bake at 125° C, 168 hours 85° C /85% RH storage, reflow 3 times	Prior to TC/UHAST/ H3TRB
High Temperature Reverse Bias	MIL-STD-750 (Method 1040) JESD22-A-101	80% of Rated VDRM (V _{AC} -peak), T _j , 1,008 hours	3 lots 77pcs
Temperature Cycle	JESD22A -104	-55° C to +150° C, 15 minutes dwell, 1,000 cycles	3 lots 40 pcs
Unbiased HAST	JESD22A-118	130° C, 85% RH, 96 hours	3 lots 40 pcs
High Humidity High Temperature Reverse Bias	JESD22A-101	52V _{DC} , 85° C, 85% RH, 1,008 hours	3 lots 40 pcs
Resistance to Solder Heat	JESD22A-111 (SMD) JESD22B-106 (PTH)	SMD 260° C, 10 s PTH 270° C, 7 s	1 lot 30 pcs
Moisture Sensitivity Level	J-STD-020(SMD)	24 hours 125° C bake, 168 hours 85° C /85% RH soak, reflow 3 times	2 lots 11 pcs
Solderability	JESD22B-102	Method A for through hole Method B & D for SMD	1 lot 10 pcs

Estimate of Failure Rate, MTBF, FITS for a Given Operation Temperature

Temp °C	% FR/khrs	MTBF (K)	FITS
30	0.00004251	2352588	0
60	0.00133448	74918	13
80	0.00959617	10420	96
100	0.05584068	1790	558
125	0.39351454	254	3935

The Mean-Time-Between-Failure (MTBF) in hours and the percent failure rate per 1,000 hours (%FR/khr) are computed at a 60% confidence level using the chi square method and the Arrhenius derating model for various junction operating temperatures. For the calculations, a value of 1 eV was used for the activation energy.