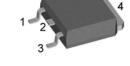
## XPT IGBT

### Copack

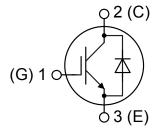
Part number

**IXA4IF1200UC** 

Marking on Product: X4TAUF



Backside: collector



### Features / Advantages:

- Easy paralleling due to the positive temperature coefficient of the on-state voltage
- Rugged XPT design (Xtreme light Punch Through) results in:
  - short circuit rated for 10 µsec.
  - very low gate charge
- low EMI
- square RBSOA @ 3x lc
- Thin wafer technology combined with the XPT design results in a competitive low VCE(sat)
- SONIC<sup>™</sup> diode
- fast and soft reverse recovery
- low operating forward voltage

### **Applications:**

- AC motor drives
- Solar inverter
- Medical equipment
- Uninterruptible power supply
- Air-conditioning systems
- Welding equipment
  Switched-mode and resonant-mode power supplies
- Inductive heating, cookers
- Pumps, Fans

### Package: TO-252 (DPak)

- · Industry standard outline
- RoHS compliant
- Epoxy meets UL 94V-0

IXYS reserves the right to change limits, conditions and dimensions.

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### IXA4IF1200UC

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V<sub>CES</sub>

|<sub>C25</sub>

V<sub>CE(sat)</sub> =

preliminary 1200V

**9**A

1.8V

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## IXA4IF1200UC

preliminary

IGBT							1	Ratings	;	
Symbol	Definition		Cond	itions			min.	typ.	max.	Unit
	collector emitter voltage				T <sub>vj</sub> =	25°C			1200	V
V <sub>GES</sub>	max. DC gate voltage								±20	V
V <sub>GEM</sub>	max. transient gate emitter voltage								±30	V
I <sub>C25</sub>	collector current				T <sub>c</sub> =	25°C			9	A
I <sub>C 100</sub>					T <sub>c</sub> =	100°C			5	A
Ptot	total power dissipation				T <sub>c</sub> =	25°C			45	W
V <sub>CE(sat)</sub>	collector emitter saturation voltage		I <sub>c</sub> =	3A; V <sub>GE</sub> = 15 V	T <sub>VJ</sub> =	25°C		1.8	2.1	V
					T <sub>VJ</sub> =	125°C		2.1		V
V <sub>GE(th)</sub>	gate emitter threshold voltage		$I_c = 0$	.1mA; V <sub>GE</sub> = V <sub>CE</sub>	T <sub>VJ</sub> =	25°C	5.4	5.9	6.5	V
ICES	collector emitter leakage current		V <sub>CE</sub> = Y	$V_{\text{CES}}; V_{\text{GE}} = 0 \text{ V}$	T <sub>VJ</sub> =	25°C			0.1	mA
					T <sub>VJ</sub> =	125°C		0.1		mA
I <sub>GES</sub>	gate emitter leakage current		$V_{GE}$ =	±20 V					500	nA
Q <sub>G(on)</sub>	total gate charge		$V_{CE}$ =	600 V; $V_{GE}$ = 15 V; $I_{C}$ =	3 A			12		nC
t <sub>d(on)</sub>	turn-on delay time	٦						70		ns
tr	current rise time				_			40		ns
t <sub>d(off)</sub>	turn-off delay time	l		tive load	I <sub>VJ</sub> =	125°C		250		ns
tr	current fall time	7		$600 \text{ V}; \text{ I}_{\text{c}} = 3 \text{ A}$				100		ns
Eon	turn-on energy per pulse		$V_{GE}$ =	±15 V; R <sub>G</sub> =330 Ω				0.4		mJ
E <sub>off</sub>	turn-off energy per pulse	J						0.3		mJ
RBSOA	reverse bias safe operating area	٦	V <sub>GE</sub> =	±15 V; R <sub>G</sub> =330 Ω	T <sub>VJ</sub> =	125°C				1 1 1 1
I <sub>CM</sub>		ſ	$V_{CEmax}$	= 1200V					9	A
SCSOA	short circuit safe operating area	٦	$V_{\text{CEmax}}$	= 900V						
tsc	short circuit duration	}	V <sub>CE</sub> =	900 V; V <sub>GE</sub> = ±15 V	T <sub>vj</sub> =	125°C			10	μs
l <sub>sc</sub>	short circuit current	J		30Ω; non-repetitive				12		A
R <sub>thJC</sub>	thermal resistance junction to case								2.7	K/W
R <sub>thCH</sub>	thermal resistance case to heatsink							0.50		K/W
Diode					_					
V <sub>RRM</sub>	max. repetitive reverse voltage					25°C			1200	V
I <sub>F25</sub>	forward current					25°C			10	A
I <sub>F 100</sub>	· · ·					100°C			6	A
V <sub>F</sub>	forward voltage		<sub>F</sub> =	3A		25°C			2.20	V
						125°C		1.90		V
I <sub>R</sub>	reverse current		$V_R = V$	RRM		25°C			*	mA
	* not applicable, see Ices value abov	'e			$T_{VJ} =$	125°C		*		mA
Q <sub>r</sub>	reverse recovery charge	٦	V. = 6	300 V				0.5		μC
I <sub>RM</sub>	max. reverse recovery current	Y	-di₋ /d	t = -150 A/us	T <sub>V1</sub> =	125°C		5		A
t <sub>rr</sub>	reverse recovery time		<sub>=</sub> =	600 V t =  -150 A/µs 3A; V <sub>GE</sub> = 0 V	- vJ			350		ns
Erec	reverse recovery energy	J	•r					0.1		mJ
R <sub>thJC</sub>	thermal resistance junction to case								3	
R <sub>thCH</sub>	thermal resistance case to heatsink							0.50		K/W

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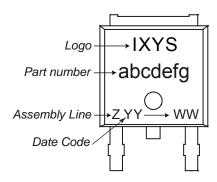


## IXA4IF1200UC

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Package TO-252 (DPak)				Ratings			
Symbol	Definition	Conditions	min.	typ.	max.	Unit	
I <sub>RMS</sub>	RMS current	per terminal			20	Α	
T <sub>vj</sub>	virtual junction temperature		-40		150	°C	
T <sub>op</sub>	operation temperature		-40		125	°C	
T <sub>stg</sub>	storage temperature		-40		150	°C	
Weight				0.3		g	
Fc	mounting force with clip		20		60	Ν	

### **Product Marking**



### Part number

- I = IGBT
- X = XPT IGBT A = Gen 1 / std
- 4 = Current Rating [A]
- IF = Copack
- 1200 = Reverse Voltage [V] UC = TO-252AA (DPak)

1	Ordering	Part Number	Marking on Product	Delivery Mode	Quantity	Code No.
	Standard	IXA4IF1200UC	X4TAUF	Tape & Reel	2500	510217

Similar Part	Package	Voltage class
IXA4IF1200TC	TO-268AA (D3Pak) (2)	1200

Equiva	lent Circuits for Simulation	* on die level		T <sub>vj</sub> = 15	50 °C
	- R <sub>o</sub> -		IGBT	Diode	
V <sub>0 max</sub> →	threshold voltage		1.1	1.25	V
$R_{0 max}$	slope resistance *		460	280	mΩ

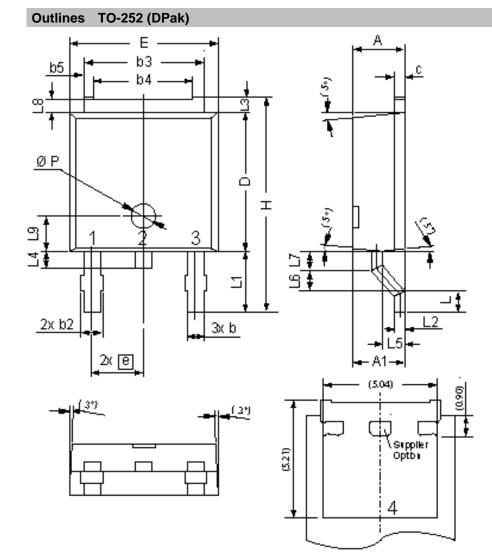
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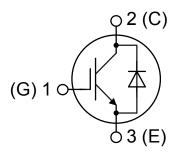
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## IXA4IF1200UC

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Dim.	Millimeters		Inches		
Ulm.	min	max	min	max	
A	2.20	2.40	0.087	0.094	
A1	2.10	2.50	0.083	0.098	
b	0.66	0.86	0.026	0.034	
b2	-	0.96	-	0.038	
b3	5.04	5.64	0.198	0.222	
-b4	4.34	BSC	0.171	BSC	
b5	0.50	BSC	0.020	BSC	
С	0.40	0.86	0.016	0.034	
D	5.90	6.30	0.232	0.248	
Е	6.40	6.80	0.252	0.268	
е	2.10	2.50	0.083	0.098	
Η	9.20	10.10	0.362	0.398	
L	0.55	1.28	0.022	0.050	
L1	2.50	2.90	0.098	0.114	
L2	0.40	0.60	0.016	0.024	
L3	0.50	0.90	0.020	0.035	
L4	0.60	1.00	0.024	0.039	
L5	0.82	1.22	0.032	0.048	
L6	0.79	0.99	0.031	0.039	
L7	0.81	1.01	0.032	0.040	
L8	0.40	0.80	0.016	0.031	
L9	1.50	BSC	0.059	BSC	
ØΡ	1.00	BSC	0.039	BSC	



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