The SP4065 integrates low capacitance diodes with an additional zener diode to protect each I/O pin against ESD and high surge events. This robust device can safely absorb up to 20A per IEC 61000-4-5, 2nd Edition (tp=8/20μs) without performance degradation and a minimum ±30kV ESD per IEC 61000-4-2 International Standard. Their low loading capacitance also makes them ideal for protecting highspeed signal pins.

Features
- ESD, IEC 61000-4-2, ±30kV contact, ±30kV air
- EFT, IEC 61000-4-4, 40A (5/50ns)
- Lightning, IEC 61000-4-5, 2nd Edition 20A (8/20μs)
- Low capacitance of 4.4pF (TYP) per I/O
- Low leakage current of 1µA (MAX) at 3.3V
- Halogen free, Lead-free and RoHS compliant
- Moisture Sensitivity Level (MSL - Level 1)

Applications
- LCD/LED TVs
- Desktops
- Game Consoles
- Set Top Boxes
- Notebooks
- 1Gb Ethernet
- Network Hardware
- Small Cells

Application Example

Life Support Note:
Not Intended for Use in Life Support or Life Saving Applications
The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.
CAUTION: Stresses above those listed in “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

**Absolute Maximum Ratings**

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Value</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>IPP</td>
<td>Peak Current (t_p=8/20μs)</td>
<td>20.0</td>
<td>A</td>
</tr>
<tr>
<td>PPK</td>
<td>Peak Pulse Power (t_p=8/20μs)</td>
<td>300</td>
<td>W</td>
</tr>
<tr>
<td>T_OP</td>
<td>Operating Temperature</td>
<td>-40 to 125</td>
<td>°C</td>
</tr>
<tr>
<td>T_STOR</td>
<td>Storage Temperature</td>
<td>-55 to 150</td>
<td>°C</td>
</tr>
</tbody>
</table>

**Electrical Characteristics (T_OP=25 °C)**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Test Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Reverse Standoff Voltage</td>
<td>V_RMWM</td>
<td></td>
<td>3.3</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Snap Back Voltage</td>
<td>V_SB</td>
<td>I_SB=50mA</td>
<td>2.8</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Reverse Leakage Current</td>
<td>I_LEAK</td>
<td>V_I=3.3V, I/O to GND</td>
<td>0.5</td>
<td>1.0</td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>Clamp Voltage¹</td>
<td>V_C</td>
<td>I_p=1A, t_p=8/20μs, Fwd</td>
<td>5.5</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>I_p=5A, t_p=8/20μs, Fwd</td>
<td>7.0</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>I_p=10A, t_p=8/20μs, Fwd</td>
<td>9.0</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>I_p=20A, t_p=8/20μs, Fwd</td>
<td>13.5</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Dynamic Resistance</td>
<td>R_DYN</td>
<td>(V_C1 - V_C2) / (I_P2 - I_P1)</td>
<td>0.4</td>
<td>Ω</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ESD Withstand Voltage¹</td>
<td>V_ESD</td>
<td>IEC61000-4-2 (Contact)</td>
<td>±30</td>
<td>kV</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>IEC61000-4-2 (Air)</td>
<td>±30</td>
<td>kV</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Diode Capacitance¹</td>
<td>C_DG-D</td>
<td>Reverse Bias=0V</td>
<td>4.4</td>
<td>5.0</td>
<td>pF</td>
<td></td>
</tr>
<tr>
<td>Diode Capacitance¹</td>
<td>C_DI-D</td>
<td>Reverse Bias=0V</td>
<td>2.2</td>
<td>pF</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Thermal Information**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Rating</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Storage Temperature Range</td>
<td>-55 to 150</td>
<td>°C</td>
</tr>
<tr>
<td>Maximum Junction Temperature</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>Maximum Lead Temperature (Soldering 20-40s)</td>
<td>260</td>
<td>°C</td>
</tr>
</tbody>
</table>

¹ Parameter is guaranteed by design and/or device characterization.

**Clamping Voltage vs. IPP**

![Clamping Voltage vs. IPP graph](image)

**Capacitance vs. Bias**

![Capacitance vs. Bias graph](image)
### 8/20μs Pulse Waveform

![8/20μs Pulse Waveform Graph]

### Product Characteristics

- **Lead Plating**: Pre-Plated Frame
- **Lead Material**: Copper Alloy
- **Lead Coplanarity**: 0.0004 inches (0.102mm)
- **Substrate material**: Silicon
- **Body Material**: Molded Epoxy, rated UL 94 V-0

**Notes**:
1. All dimensions are in millimeters.
2. Dimensions include solder plating.
3. Dimensions are exclusive of mold flash & metal burr.
4. Blo is facing up for mold and facing down for trim/form, i.e. reverse trim/form.

### Soldering Parameters

**Reflow Condition**
- Pb – Free assembly

<table>
<thead>
<tr>
<th>Pre Heat</th>
<th>Temperature Min (T_{min})</th>
<th>150°C</th>
</tr>
</thead>
<tbody>
<tr>
<td>- Temperature Max (T_{max})</td>
<td>200°C</td>
<td></td>
</tr>
<tr>
<td>- Time (min to max) (t_{s})</td>
<td>60 – 180 secs</td>
<td></td>
</tr>
</tbody>
</table>

**Average ramp up rate (Liquidus) Temp (T_{L}) to peak**
- 3°C/second max

**T_{s}\text{Lux} to T_{L} - Ramp-up Rate**
- 3°C/second max

**Reflow**
- Temperature (T_{L}) (Liquidus) | 217°C |
- Temperature (t_{L}) | 60 – 150 seconds |

**Peak Temperature (T_{P})**
- 260°C ±5 °C

**Time within 5°C of actual peak Temperature (t_{P})**
- 20 – 40 seconds

**Ramp-down Rate**
- 6°C/second max

**Time 25°C to peak Temperature (T_{P})**
- 8 minutes Max.

**Do not exceed**
- 260°C

### Part Numbering System

- **SP4065-08ATG**: MSOP-10

### Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
<th>Min. Order Qty.</th>
</tr>
</thead>
<tbody>
<tr>
<td>SP4065-08ATG</td>
<td>MSOP-10</td>
<td>4000</td>
</tr>
</tbody>
</table>

### Part Marking System

- **YYWM**: Date code
- **YYWW**: Date code
- **AA**: Series
- **MA**: Number of Channels
- **TA**: Assembly Site

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Revised: 09/19/19
**Package Dimensions — MSOP-10**

![Diagram of MSOP-10 package dimensions](image)

**Embossed Carrier Tape & Reel Specification — MSOP-10**

![Diagram of embossed carrier tape & reel specification](image)

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